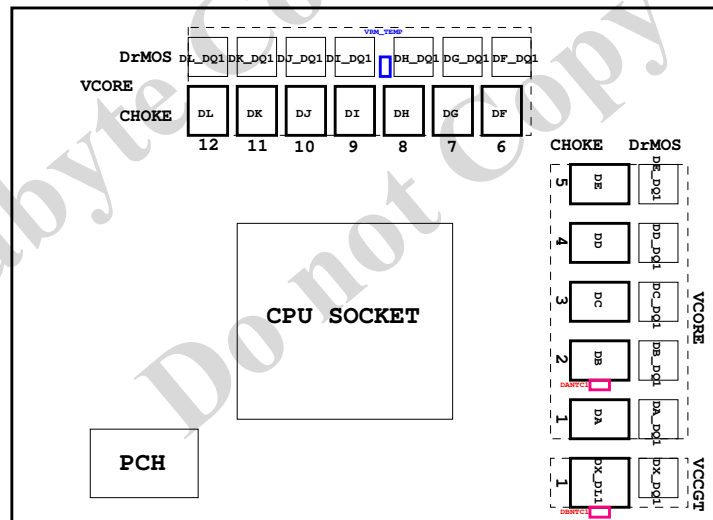


# Model Name: B560M AORUS ELITE DB

SHEET TITLE Rev 1.01

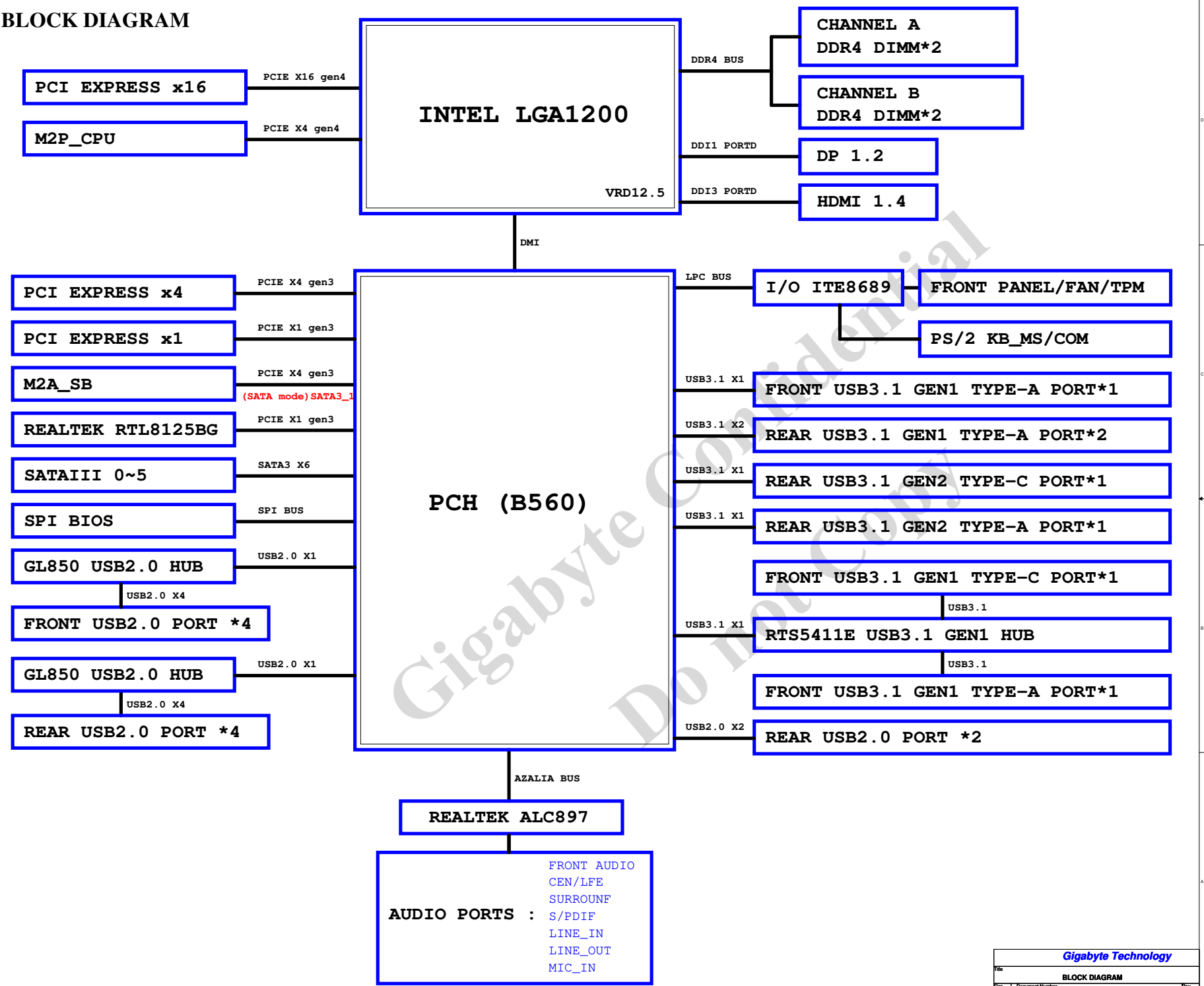
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1200-A (RKL_R0.1)
05	CPU_LGA1200-B-DDR4 (RKL_R0.1)
06	CPU_LGA1200-C (RKL_R0.1)
07	CPU_LGA1200-D (RKL_R0.1)
08	DDR 4 CHANNEL A (CML_R0.1)
09	DDR 4 CHANNEL B (CML_R0.1)
10	PCH CLK,DMI,CNVI (RKL_R0.1)
11	PCH SPI,USB (RKL_R0.1)
12	PCH PCIE,SATA (RKL_R0.1)
13	PCH ESPI,MISC (RKL_R0.1)
14	PCH GPP,HDA (RKL_R0.1)
15	PCH PWR,GND (RKL_R0.1)
16	Heatsink
17	ITE ITE8689 (RKL_R0.1)
18	HWM (RKL_R0.1)
19	FAN CTRL-CFL-SIO_5 FAN (RKL_R0.1)
20	Single BIOS for CS mode (RKL_R0.1)
21	PCI EXPRESS X16 SLOT (REV0.3)
22	PCI EXPRESS X4 SLOT(PCH) (REV0.51)
23	PCI EXPRESS X1 (REV0.51)
24	SATA
25	M.2 x4 (A)_SB (RKL_R0.1)
26	(RKL_R0.1)
27	POSCAP_0+0_H570_Q570 (RKL_R0.1)
28	M.2 x4 (P)_CPU (RKL_R0.1)
29	COM,LPT,TPM, THB (CML_R0.94)
30	ISL69269_L=0.15u (RKL_R0.1)
31	RAA229001_L=0.15u (RKL_R0.1)
32	VCORE_PSTAGE-1_L=0.15u (RKL_R0.1)
33	VCORE_PSTAGE-2_L=0.15u (RKL_R0.1)
34	VCORE_PSTAGE-3_L=0.15u (RKL_R0.1)
35	VCCGT_PSTAGE_L=0.15u (RKL_R0.1)
36	VCCSA_MOS (RKL_R0.1)
37	VCCIO-Ferrite-Z系列 (RKL_R0.1)
38	VCCIO2-Ferrite-Z系列 (RKL_R0.1)
39	RT8237_DDR_B560 (RKL_R0.1)
40	RT8068A_VPP_B560
41	NCP81269_VCC18_PCH
42	RT8068_VCC1V8_PRIM
43	DISCRETE POWER (REV0.1)
44	ATX POWER , A_-PROCHOT
45	GL850S_1_Rear USB20 X4 (CML_R0.94)
46	CNVi_M2_WIFI (CML_R0.94)
47	HDMI / DP (RKL_R0.1)
48	U32G2 (RKL_R0.1)
49	(RKL_R0.1)

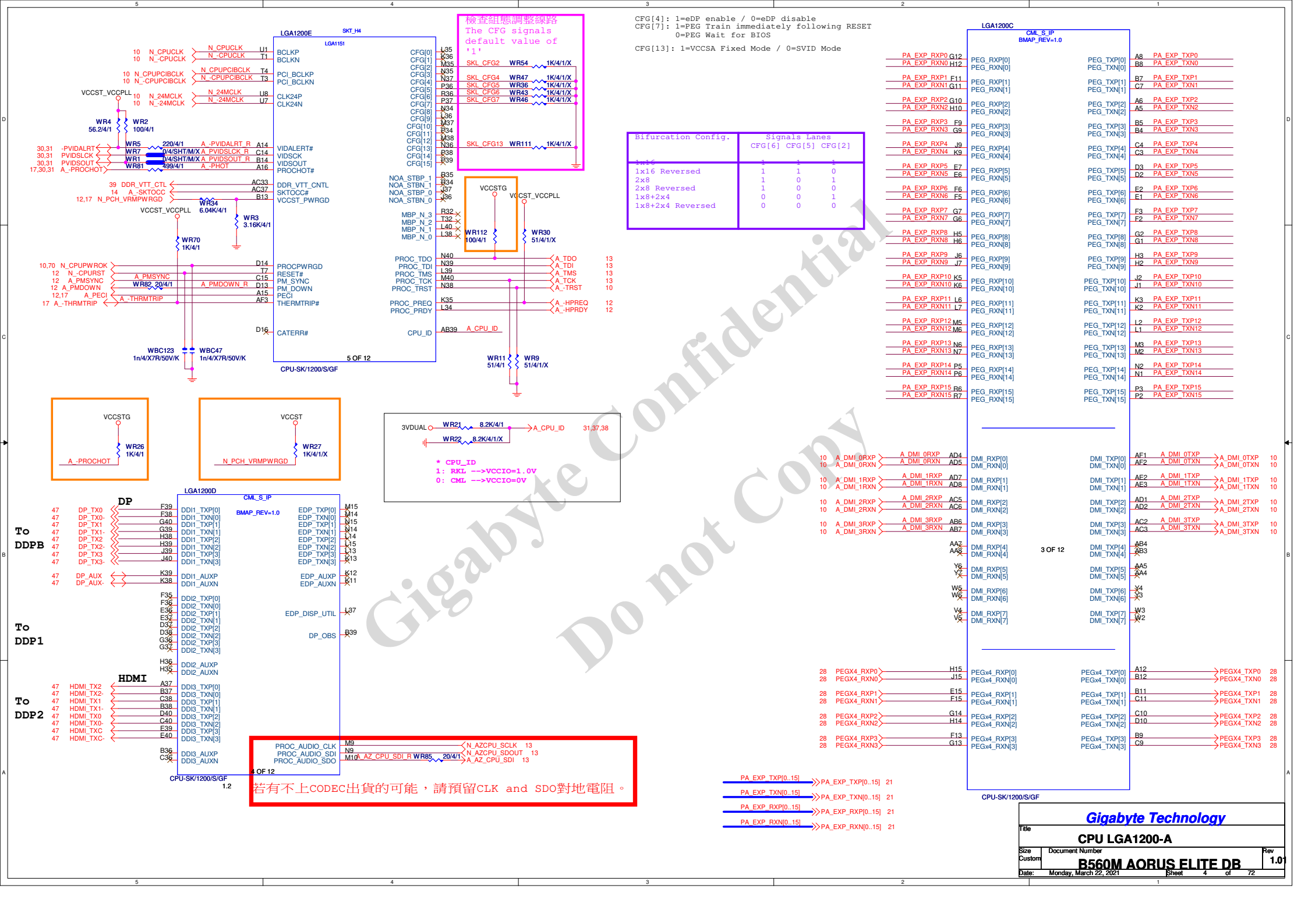
SHEET	TITLE
50	GL850S_2_Fornt USB20 X4 (CML_R0.94)
51	ITE5201_G_Type-C (RKL_R0.1)
52	RTS5411 4port Hub ( Chip ) (RKL_R0.1)
53	Front Type-C (CML_R0.94)
54	F_U32 (CML_R0.94)
55	KB_MS_USB (CML_R0.94)
56	Redriver_A_Type-A (RKL_R0.1)
57	REALTEK RTL8125BS (CML_R0.3)
58	USB30_LAN CONNECTOR-8125 (CML_R0.3)
59	Realtek ALC897 (RKL_R0.6)
60	REAR AUDIO JACK (RKL_R0.6)
61	CPU POWER-1 (RKL_R0.1)
62	CPU POWER-1 (RKL_R0.1)
63	NCT3933
64	F_PANEL
65	IT5702 (RKL_R1.0)
66	PCH/AUDIO/DEBUG/C_LED1/2 (RKL_R1.0)
67	D_LED1/D_LED2
68	SMBUS SWITCH (RKL_R0.1)
69	(RKL_R0.1)
70	EMI-ESD
71	POWER MAP
72	NTC MAP



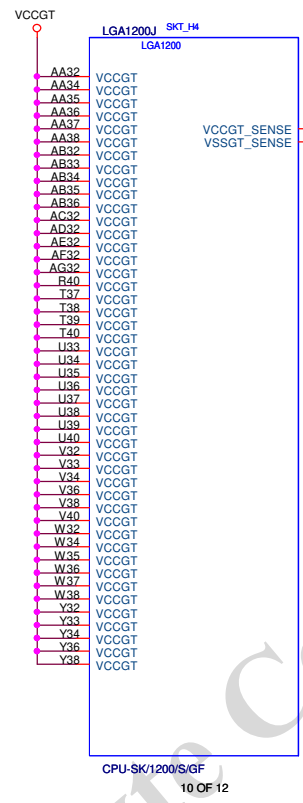
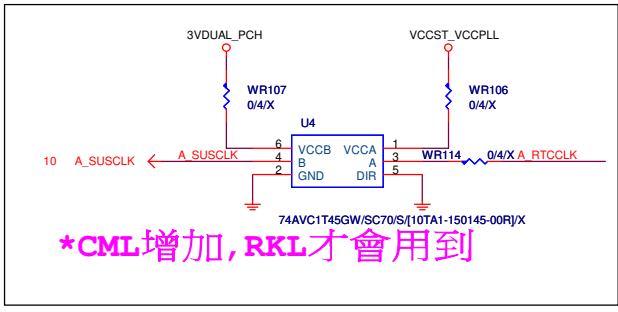
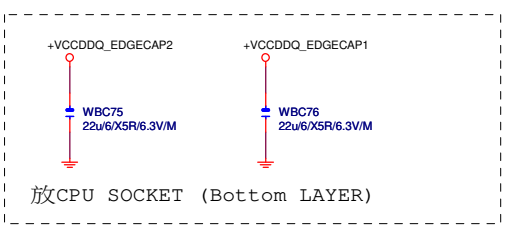
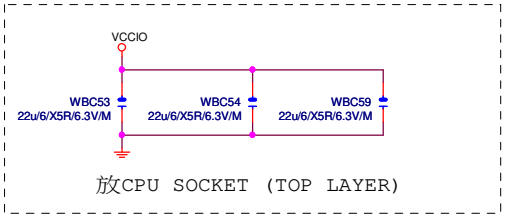
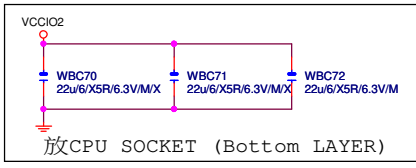
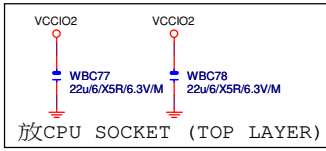
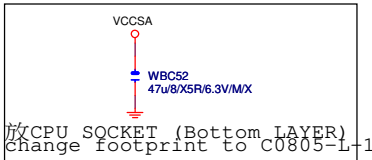
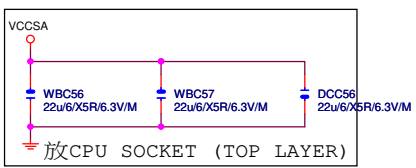
DATE	Change Item	Reason
2000/10/09 PCR03	1. CD400-0301 Footprint修正R0301	
2000/10/14 PCR03	1. DP AM DPAGLEDPAG	
2000/10/15 PCR03	1. HUI12K1CHQPN-PSB101	
2000/10/28 PCR10	B500M ACRIUS ELITE Rev 1.0 Layout Request 1. 由B500M ACRIUS ELITE Rev 0.2 來修改 2. TMC25 Footprint change to SINE_B500M_ACRIUS_ELITE-T	
2000/12/01 PCR101	B500M ACRIUS ELITE Rev 1.01 Layout Request 1. B500M ACRIUS ELITE Rev 1.0 來修改 2. FTV 由Haseco 轉到Haseco	

BLOCK DIAGRAM





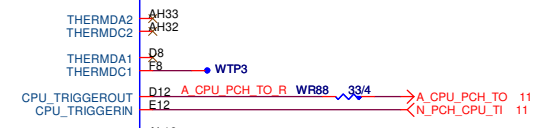
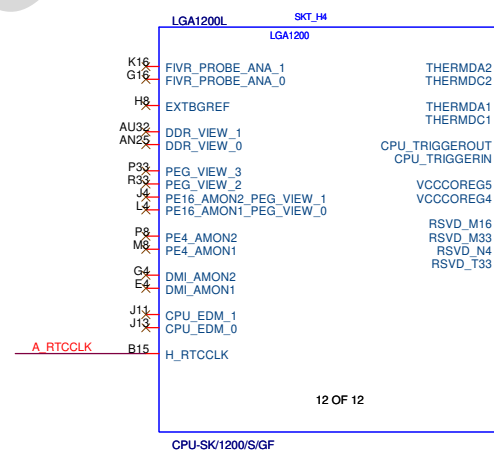
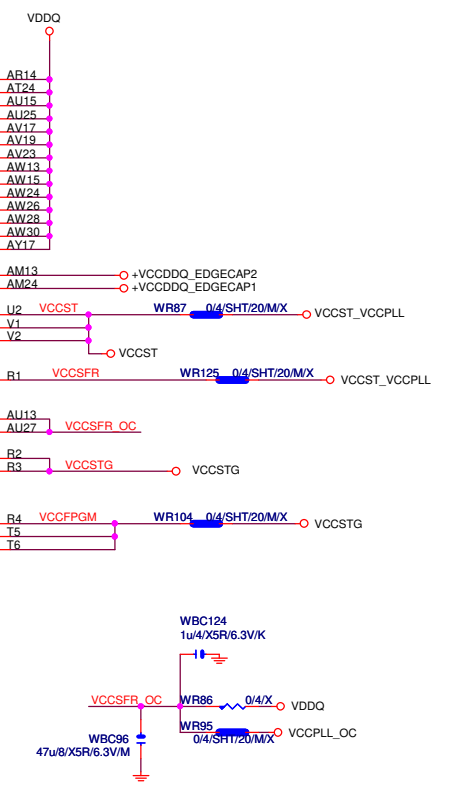
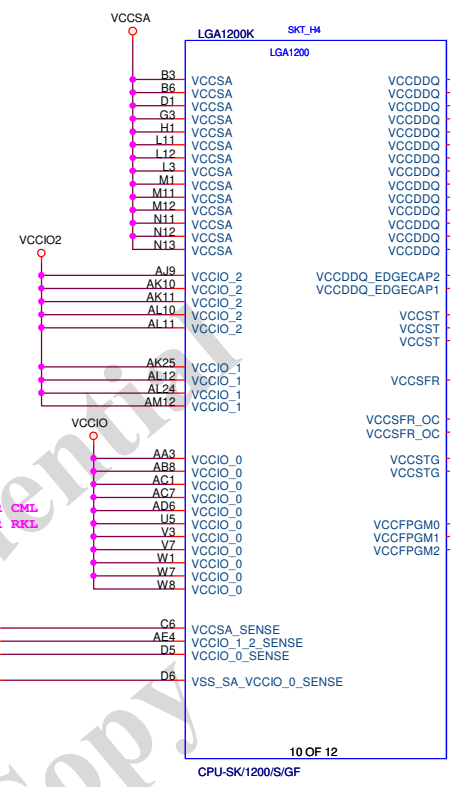




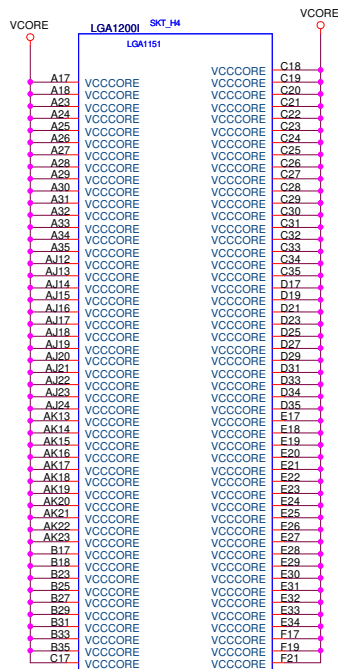
VCCIO\_1\_2  
VOUT = 0.95V  
IOUT = 9.5A

VCCIO\_0  
VOUT = 0.95V FOR CML  
VOUT = 1.05V FOR RKL  
IOUT = 8A

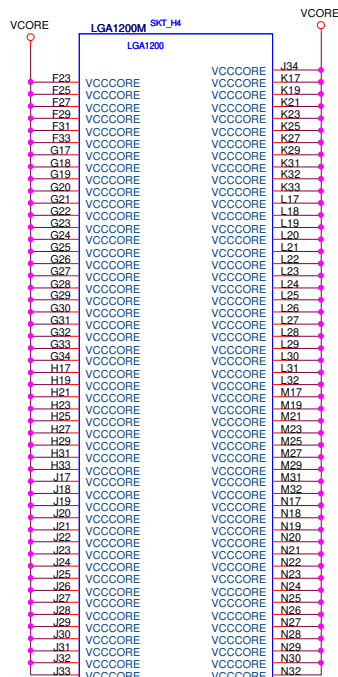
31 VCCSA\_SENSE  
38 VCCIO2\_SENSE  
37 VCCIO\_SENSE  
31,37 VSSSA\_VIO\_SENSE





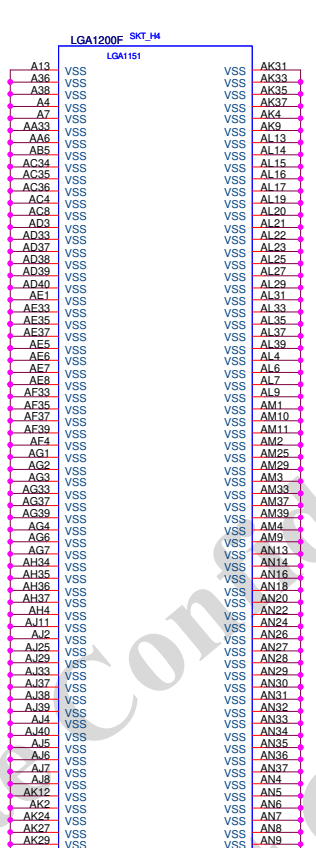


CPU-SK/1200/S/GF  
9 OF 12

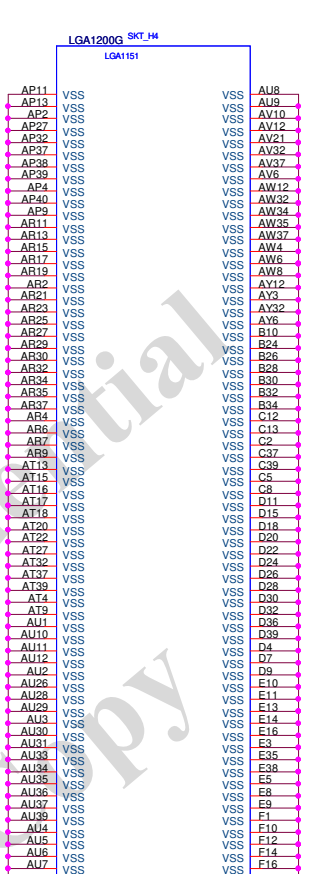


VCCORE\_SENSE  
VCCORE\_SENSE  
B16 → VCCORE\_VCC\_SEN 30  
C16 → VCCORE\_VSS\_SEN 30

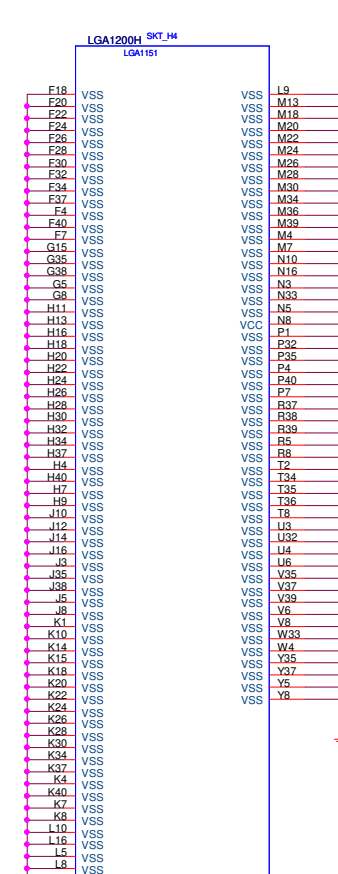
CPU-SK/1200/S/GF  
9 OF 12



CPU-SK/1200/S/GF  
6 OF 12



CPU-SK/1200/S/GF  
7 OF 12



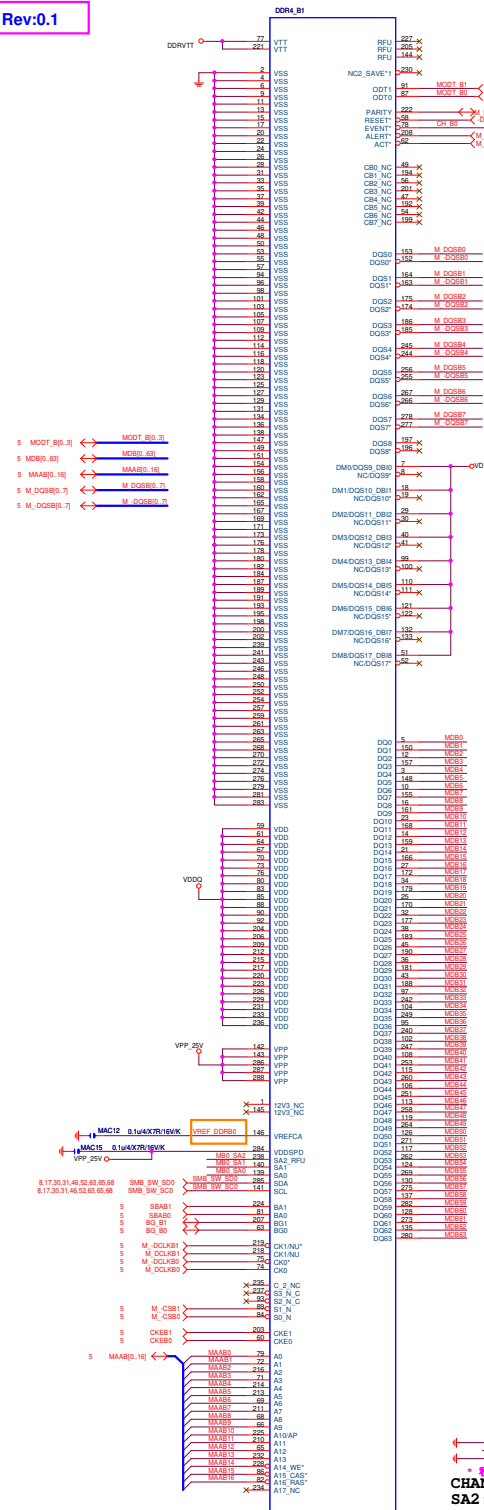
8 OF 12

CPU-SK/1200/S/GF

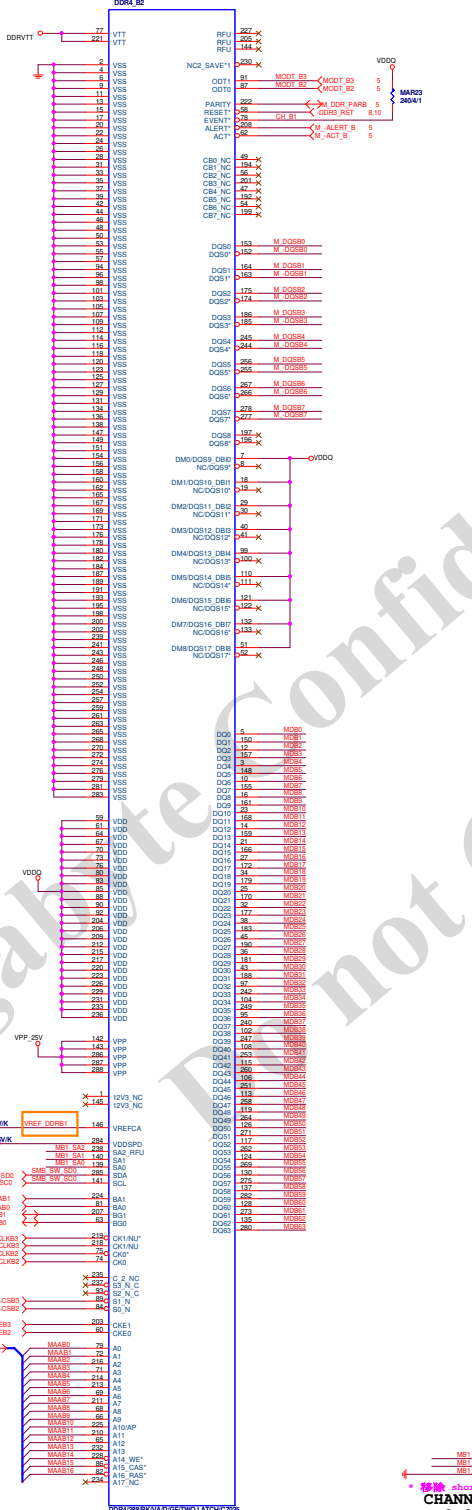
\* 刪 Vcore 電容



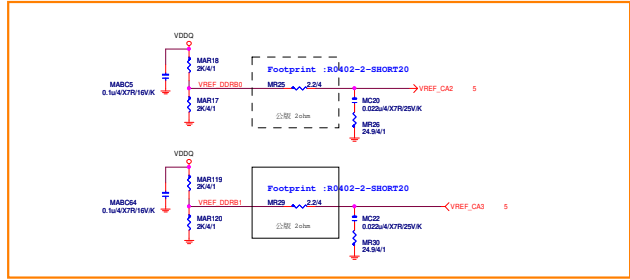




✱ 移除 short pad  
CHANNEL B0  
SA2:0=010

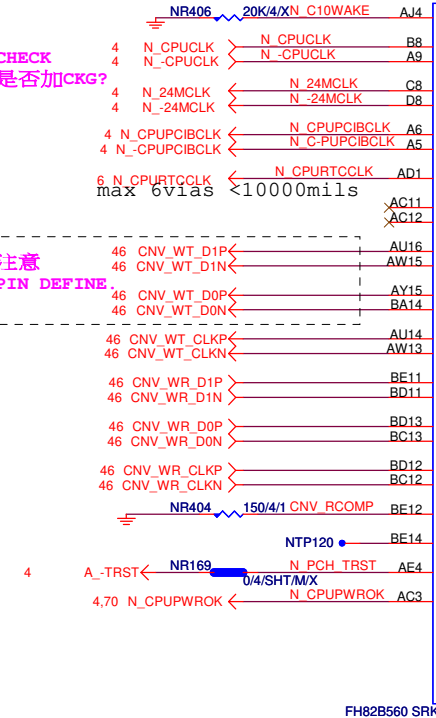


✱ 移除 short pad  
CHANNEL B1  
SA2:0=011



# RKL\_TGP\_PCH-H R0.1

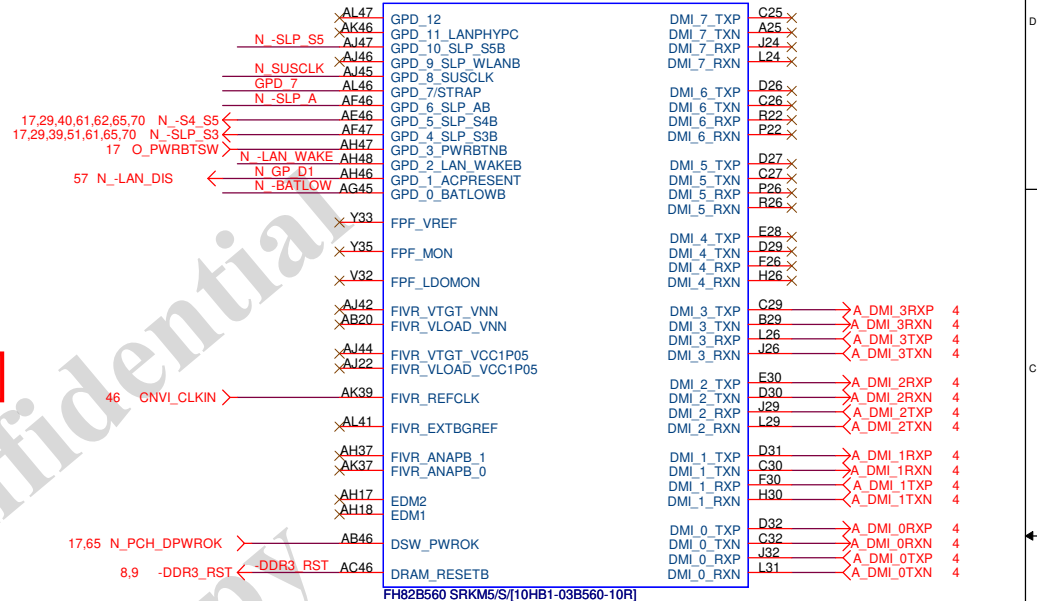
C10\_WAKE RESERVED/BIOS NEED TO PROGRAM  
INTERNAL PD ON THIS PIN.



FH82B560 SRKM5/S[10HB1-03B560-10R]

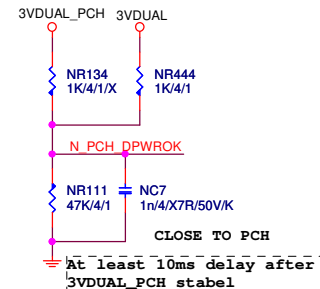
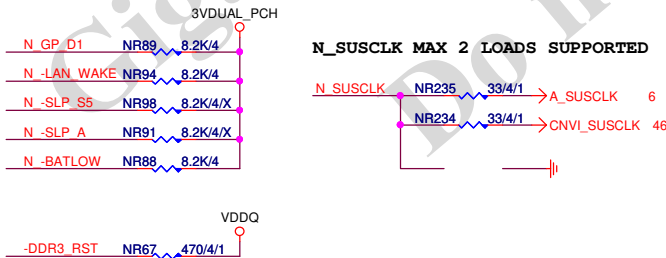
CLKOUT\_PCIE\_P/N [9, 7, 4, 3, 0] = Must be used for PCIe\* Gen4 support

## PCHB

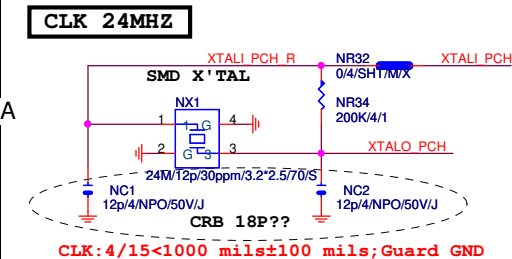
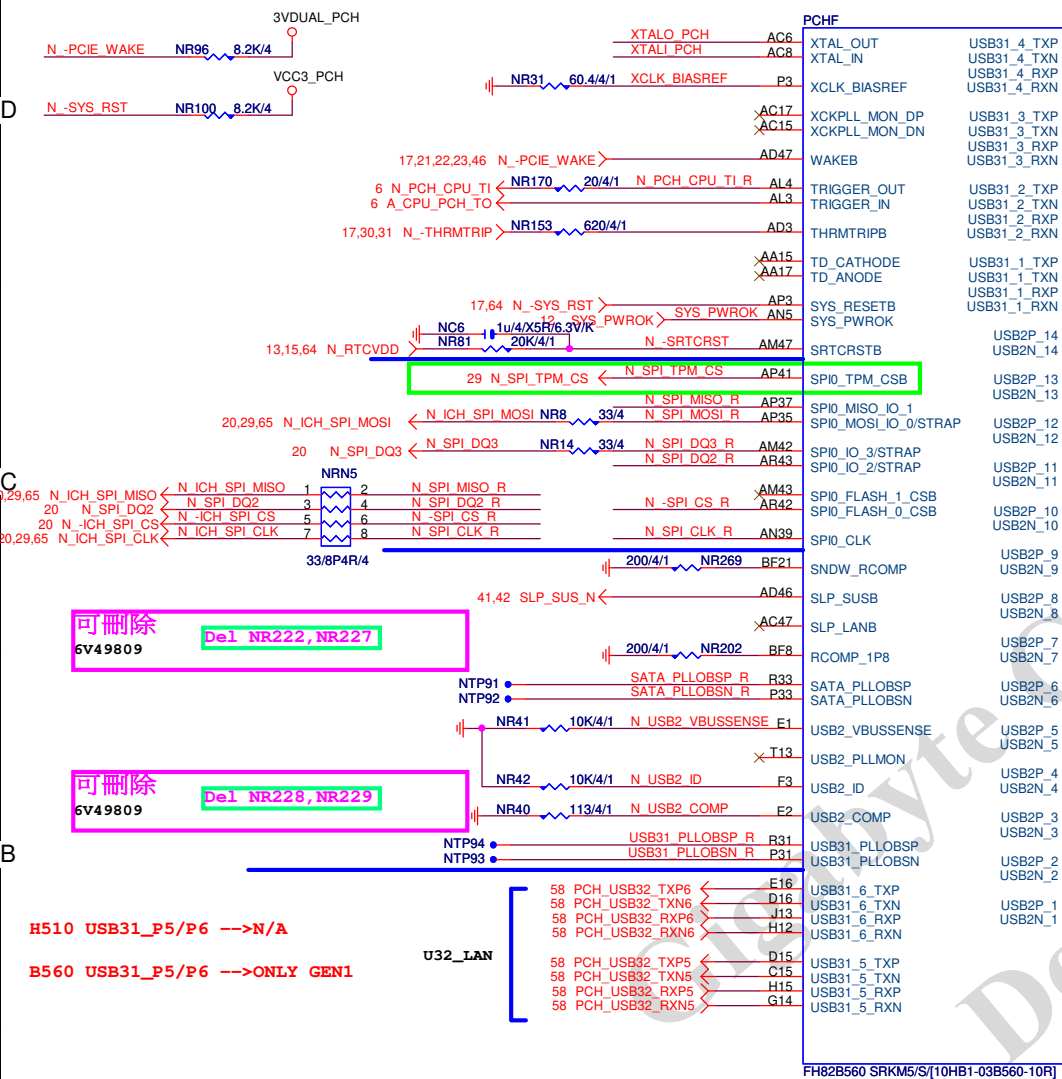


## STRAP

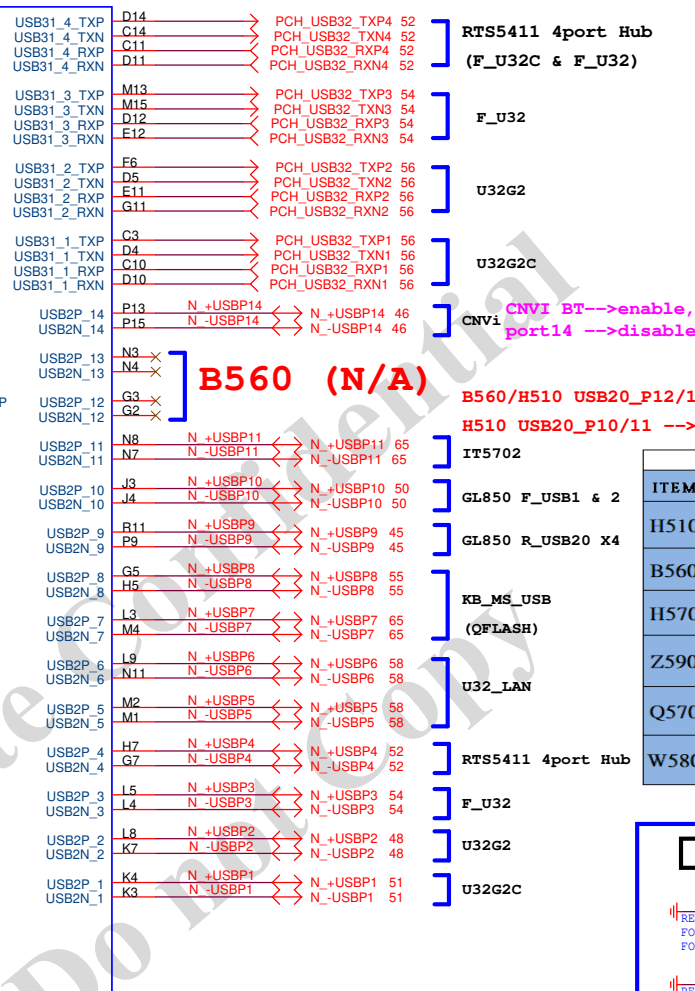
3VDUAL\_PCH  
GPD\_7 NR237 100K/4/X  
Reserved. This strap should sample LOW INTERNAL PD.



ANS 8736362		<b>Gigabyte Technology</b>	
Title		PCH CLK,DMI,CNVI	
Size B	Document Number		Rev 1.0
B560M AORUS ELITE DB			
Date: Monday, March 22, 2021	Sheet 10	of 72	

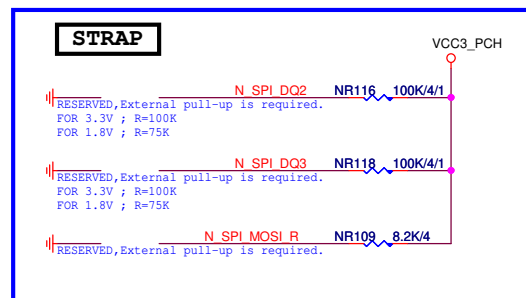


PCH Signal Glitch Free



Intel 500 series PCH USB configuration P1~P6						
ITEM	USB P1	USB P2	USB P3	USB P4	USB P5	USB P6
H510	U3.2 Gen1x1	U3.2 Gen1x1	U3.2 Gen1x1	U3.2 Gen1x1	NA	NA
B560	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen1x1	U3.2 Gen1x1
H570	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen1x1	U3.2 Gen1x1
Z590	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1
Q570	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1
W580	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1
	Gen2x2		Gen2x2		Gen2x2	

Intel 500 series PCH USB20 configuration						
ITEM	USB P1-9	USB P10	USB P11	USB P12	USB P13	USB P14
H1510	USB2	NA	NA	NA	NA	For Intel® Wireless AC
B560	USB2	USB2	USB2	NA	NA	
H570	USB2	USB2	USB2	USB2	USB2	
Z590	USB2	USB2	USB2	USB2	USB2	
Q570	USB2	USB2	USB2	USB2	USB2	
W580	USB2	USB2	USB2	USB2	USB2	



**Pin 1 to 100 connection diagram for the B560 (N/A) motherboard.**

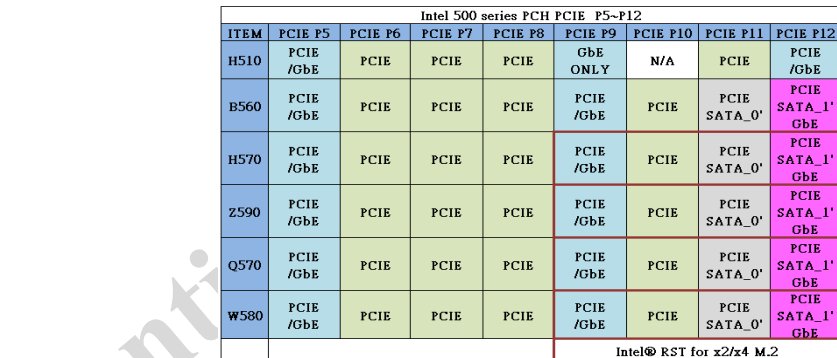
The diagram shows the connection of various components to the motherboard pins. It includes headers for SATA, USB, and other peripherals, as well as internal components like the CPU, memory, and storage. The diagram is color-coded and includes a legend for the components.

**Legend:**

- NR173, NR175:** 8.2K/4/X
- NR167, NR175:** 0/4/SHT/M/X N\_XDP\_PRDY
- NR166, NR175:** 0/4/SHT/M/X N\_XDP\_PREQ
- NR185:** 33/4 N\_PMSYNC
- NR154, NR155:** 0/4/X A\_PECI\_R
- NR155:** 1K/4/1
- NR125:** 47K/4/1/X
- NR125:** N\_Y2
- NR125:** N\_Y1

**Pin Connections:**

- Pin 1:** NR173, NR175, 8.2K/4/X
- Pin 2:** NR167, NR175, 0/4/SHT/M/X N\_XDP\_PRDY
- Pin 3:** NR166, NR175, 0/4/SHT/M/X N\_XDP\_PREQ
- Pin 4:** NR185, 33/4 N\_PMSYNC
- Pin 5:** NR154, NR155, 0/4/X A\_PECI\_R
- Pin 6:** NR155, 1K/4/1
- Pin 7:** NR125, 47K/4/1/X
- Pin 8:** NR125, N\_Y2
- Pin 9:** NR125, N\_Y1
- Pin 10:** PCIE\_12\_SATA\_1A\_LAN\_0C\_TXP
- Pin 11:** PCIE\_12\_SATA\_1A\_LAN\_0C\_TXN
- Pin 12:** PCIE\_12\_SATA\_1A\_LAN\_0C\_RXP
- Pin 13:** PCIE\_12\_SATA\_1A\_LAN\_0C\_RXN
- Pin 14:** PCIE\_11\_SATA\_0A\_TXP
- Pin 15:** PCIE\_11\_SATA\_0A\_TXN
- Pin 16:** PCIE\_11\_SATA\_0A\_RXP
- Pin 17:** PCIE\_11\_SATA\_0A\_RXN
- Pin 18:** PCIE\_10\_TSN\_1\_TXP
- Pin 19:** PCIE\_10\_TSN\_1\_TXN
- Pin 20:** PCIE\_10\_TSN\_1\_RXP
- Pin 21:** PCIE\_10\_TSN\_1\_RXN
- Pin 22:** PCIE\_9\_LAN\_0B\_TSN\_0\_TXP
- Pin 23:** PCIE\_9\_LAN\_0B\_TSN\_0\_TXN
- Pin 24:** PCIE\_9\_LAN\_0B\_TSN\_0\_RXP
- Pin 25:** PCIE\_9\_LAN\_0B\_TSN\_0\_RXN
- Pin 26:** PCIE\_8\_TXP
- Pin 27:** PCIE\_8\_TXN
- Pin 28:** PCIE\_8\_RXP
- Pin 29:** PCIE\_8\_RXN
- Pin 30:** PCIE\_7\_TXP
- Pin 31:** PCIE\_7\_TXN
- Pin 32:** PCIE\_7\_RXP
- Pin 33:** PCIE\_7\_RXN
- Pin 34:** PCIE\_6\_TXP
- Pin 35:** PCIE\_6\_TXN
- Pin 36:** PCIE\_6\_RXP
- Pin 37:** PCIE\_6\_RXN
- Pin 38:** PCIE\_5\_LAN\_0A\_TXP
- Pin 39:** PCIE\_5\_LAN\_0A\_TXN
- Pin 40:** PCIE\_5\_LAN\_0A\_RXP
- Pin 41:** PCIE\_5\_LAN\_0A\_RXN
- Pin 42:** PCIE\_4\_USB31\_10\_TXP
- Pin 43:** PCIE\_4\_USB31\_10\_TXN
- Pin 44:** PCIE\_4\_USB31\_10\_RXP
- Pin 45:** PCIE\_4\_USB31\_10\_RXN
- Pin 46:** PCIE\_3\_USB31\_9\_TXP
- Pin 47:** PCIE\_3\_USB31\_9\_TXN
- Pin 48:** PCIE\_3\_USB31\_9\_RXP
- Pin 49:** PCIE\_3\_USB31\_9\_RXN
- Pin 50:** PCIE\_2\_USB31\_8\_TXP
- Pin 51:** PCIE\_2\_USB31\_8\_TXN
- Pin 52:** PCIE\_2\_USB31\_8\_RXP
- Pin 53:** PCIE\_2\_USB31\_8\_RXN
- Pin 54:** PCIE\_1\_USB31\_7\_TXP
- Pin 55:** PCIE\_1\_USB31\_7\_TXN
- Pin 56:** PCIE\_1\_USB31\_7\_RXP
- Pin 57:** PCIE\_1\_USB31\_7\_RXN
- Pin 58:** PCIE\_16\_SATA\_3\_TXP
- Pin 59:** PCIE\_16\_SATA\_3\_TXN
- Pin 60:** PCIE\_16\_SATA\_3\_RXP
- Pin 61:** PCIE\_16\_SATA\_3\_RXN
- Pin 62:** PCIE\_15\_SATA\_2\_TXP
- Pin 63:** PCIE\_15\_SATA\_2\_TXN
- Pin 64:** PCIE\_15\_SATA\_2\_RXP
- Pin 65:** PCIE\_15\_SATA\_2\_RXN
- Pin 66:** PCIE\_14\_SATA\_1B\_TXP
- Pin 67:** PCIE\_14\_SATA\_1B\_TXN
- Pin 68:** PCIE\_14\_SATA\_1B\_RXP
- Pin 69:** PCIE\_14\_SATA\_1B\_RXN
- Pin 70:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 71:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 72:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 73:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 74:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 75:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 76:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 77:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 78:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 79:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 80:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 81:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 82:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 83:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 84:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 85:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 86:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 87:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 88:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 89:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 90:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 91:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 92:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 93:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 94:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 95:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 96:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP
- Pin 97:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXN
- Pin 98:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXP
- Pin 99:** PCIE\_13\_SATA\_0B\_LAN\_0D\_TXN
- Pin 100:** PCIE\_13\_SATA\_0B\_LAN\_0D\_RXP



Intel 500 series PCH PCIe P13~P24												
ITEM	PCIE P13	PCIE P14	PCIE P15	PCIE P16	PCIE P17	PCIE P18	PCIE P19	PCIE P20	PCIE P21	PCIE P22	PCIE P23	PCIE P24
H510	SATA_0 /GbE	SATA_1	SATA_2	SATA_3	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A
B560	SATA_0 /GbE	SATA_1	SATA_2	SATA_3	SATA_4	SATA_5	N/A	N/A	PCIE	PCIE	PCIE	PCIE
H570	PCIE SATA_0 GbE	PCIE SATA_1	PCIE SATA_2	PCIE SATA_3	PCIE SATA_4	PCIE SATA_5	PCIE	PCIE	PCIE	PCIE	PCIE	PCIE
Z590	PCIE SATA_0 GbE	PCIE SATA_1	PCIE SATA_2	PCIE SATA_3	PCIE SATA_4	PCIE SATA_5	PCIE	PCIE	PCIE	PCIE	PCIE	PCIE
Q570	PCIE SATA_0 GbE	PCIE SATA_1	PCIE SATA_2	PCIE SATA_3	PCIE SATA_4	PCIE SATA_5	PCIE	PCIE	PCIE	PCIE	PCIE	PCIE
W580	PCIE SATA_0 GbE	PCIE SATA_1	PCIE SATA_2	PCIE SATA_3	PCIE SATA_4	PCIE SATA_5	PCIE SATA_6	PCIE SATA_7	PCIE	PCIE	PCIE	PCIE
					Intel® RST for x2/x4 M.2				Intel® RST for x2/x4 M.2			

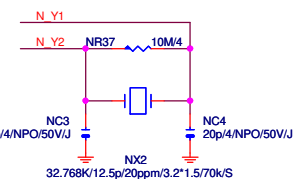
Intel 500 series PCH USB P7-P10				
ITEM	USB P7	USB P8	USB P9	USB P10
H510	NA	NA	NA	NA
B560	NA	NA	NA	NA
H570	U3.2 Gen1x1	U3.2 Gen1x1	PCIe	PCIe
Z590	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1
	PCIe	PCIe	PCIe	PCIe
Q570	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen1x1	U3.2 Gen1x1
	PCIe	PCIe	PCIe	PCIe
W580	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1	U3.2 Gen2x1
	PCIe	PCIe	PCIe	PCIe

DIP X'TAL

Del DIP NX1

## OPTION

SMD X'TAL

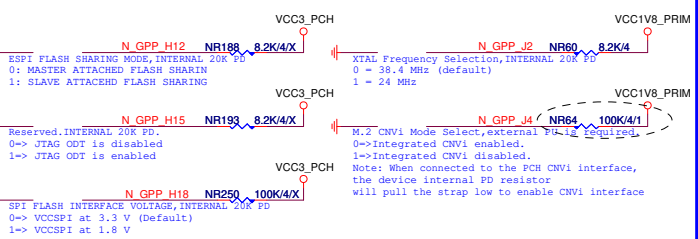
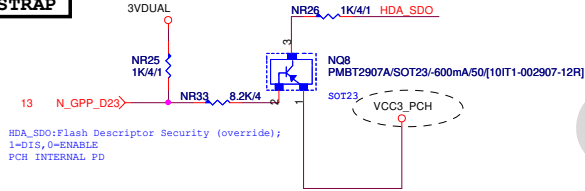
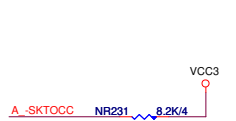
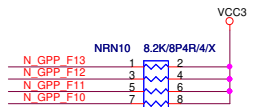
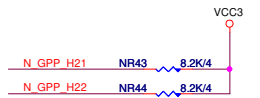
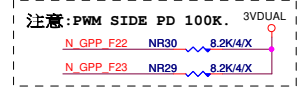
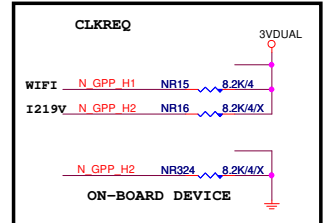
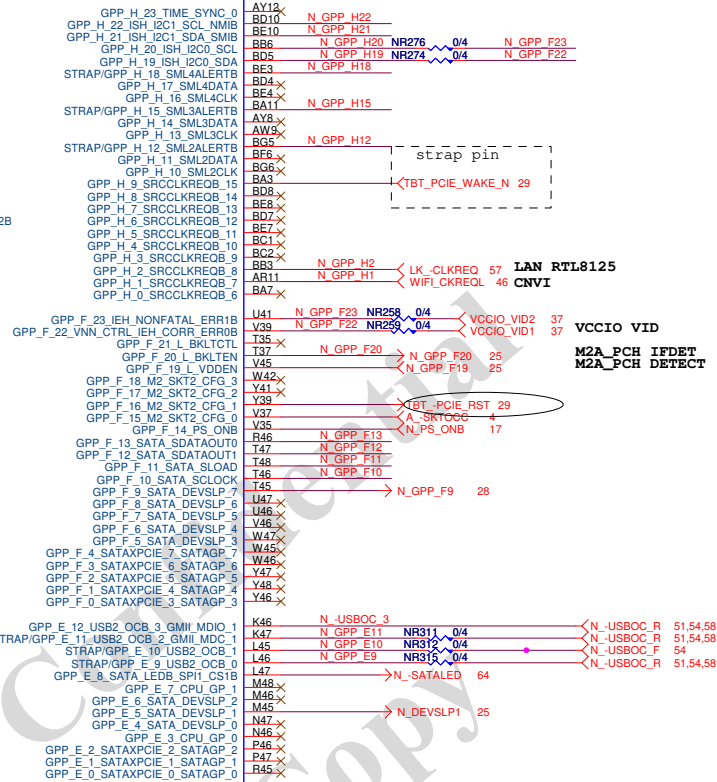
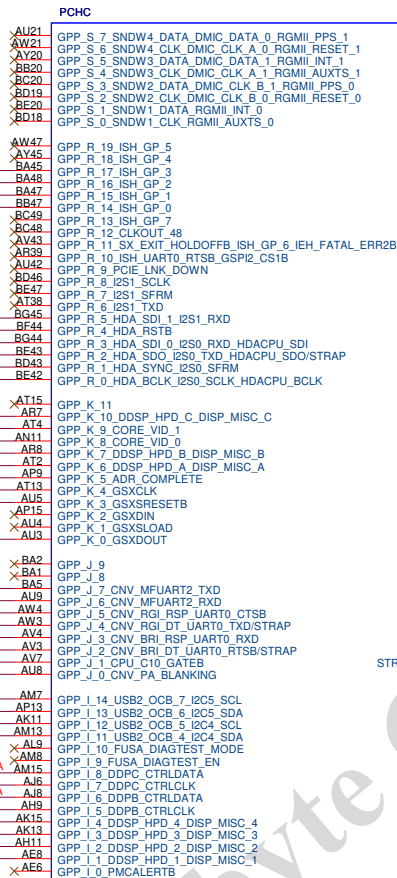
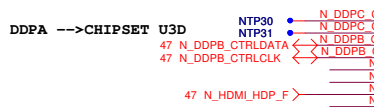
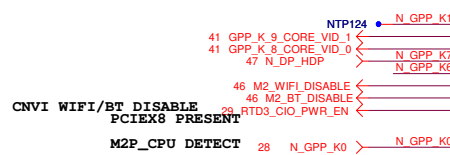
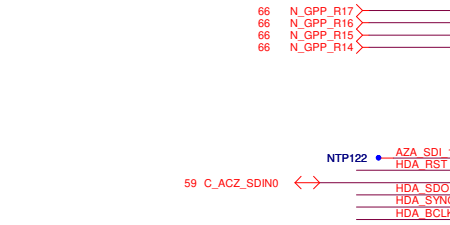
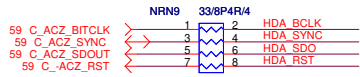


(09/21) intel 500 note item 6





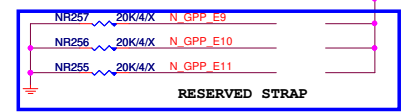
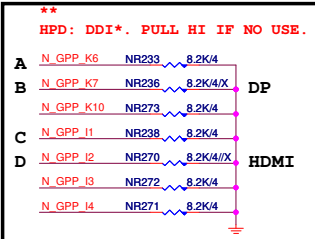
RKL\_TGP\_PCH-H R0.1



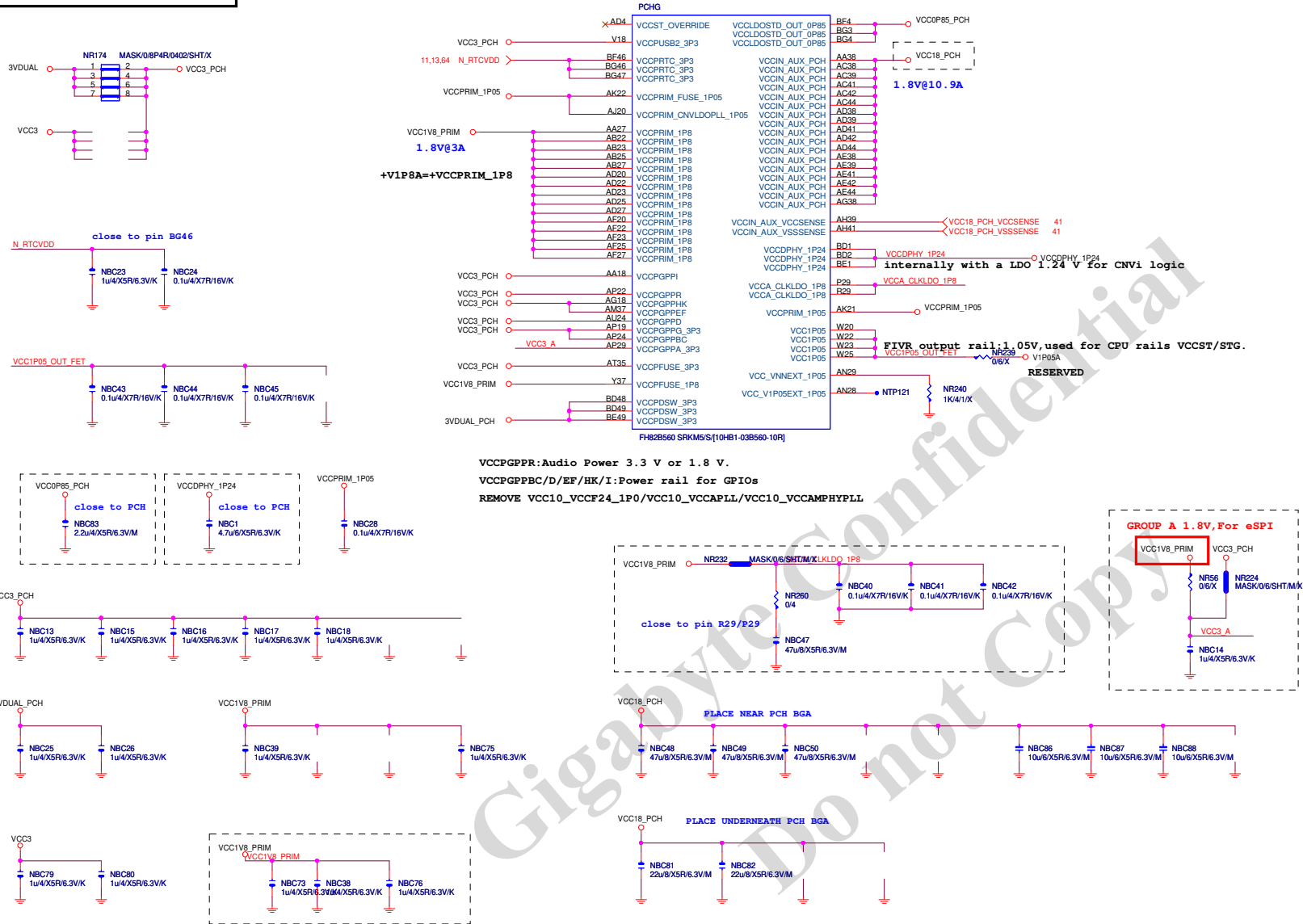
Display Port DDC/HOT PLUG SIGNAL		
CPU PORT	PCH DDC	PCH HPD
DDI_A/EDP	GPP_G0/G1	GPP_K6
DDI_1/B	GPP_I5/I6	GPP_K7
DDI_2/C	GPP_I12/I13	GPP_K1
DDI_3/D	GPP_G14/G15	GPP_I2

Fixed voltage on certain GPIO groups: GPD (3.3 V), GPP\_J and GPP\_S (1.8 V/VCCPRIM\_1P8)

PCH Signal Glitch Free







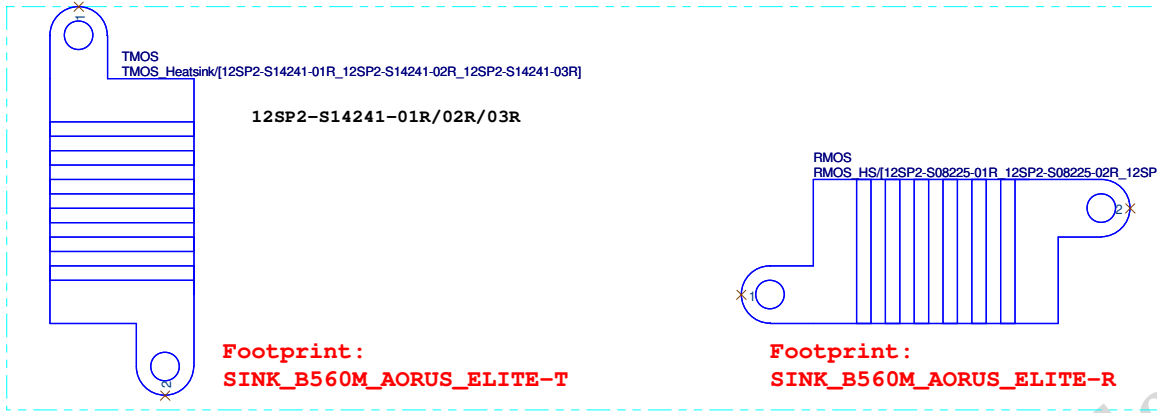
PCHH			PCHI		
AW2S	VSS	F42	A17	VSS	A123
AW39	VSS	F8	A2	VSS	A125
AY4S	VSS	G1	A28	VSS	A127
AY4S	VSS	G41	A3	VSS	A128
AY5	VSS	G48	A33	VSS	A129
AY7	VSS	G49	A37	VSS	A17
B1	VSS	G9	A4	VSS	AK18
B2	VSS	G25	A45	VSS	AK20
B4	VSS	H3	A46	VSS	AK25
B46	VSS	H8	A46	VSS	AK26
B48	VSS	J11	A47	VSS	AK28
B49	VSS	J49	A48	VSS	AK29
BE	VSS	J39	AA12	VSS	AK30
BA41	VSS	JR	AA13	VSS	AK32
BA43	VSS	J11	AA20	VSS	AK33
BA49	VSS	K39	AA22	VSS	AK35
BA9	VSS	K45	AA23	VSS	AK38
BB2S	VSS	K5	AA25	VSS	AK45
BB4	VSS	L14	AA29	VSS	AK46
BB8	VSS	L25	AA30	VSS	AL19
BC11	VSS	M12	AA32	VSS	AL22
BC15	VSS	M17	AA33	VSS	AL24
BC19	VSS	M17	AA35	VSS	AL25
BC24	VSS	M21	AA37	VSS	AL26
BC25	VSS	M22	AA48	VSS	AL28
BC26	VSS	M24	A45	VSS	AL29
BC35	VSS	M25	AB28	VSS	AL31
BC36	VSS	M26	AC13	VSS	AM1
BC39	VSS	M28	AC18	VSS	AM2
BC41	VSS	M29	AC35	VSS	AM17
BC9	VSS	M31	AC37	VSS	AM33
BF1	VSS	M33	AC4	VSS	AM35
BF13	VSS	M38	AC45	VSS	AM38
BF2	VSS	M40	AC5	VSS	AM49
BF42	VSS	M5	AC9	VSS	AN17
BF48	VSS	M12	AD11	VSS	AN19
BF49	VSS	P21	AD12	VSS	AN22
BG17	VSS	P24	AD13	VSS	AN24
BG2	VSS	P25	AD15	VSS	AN25
BG22	VSS	P28	AD17	VSS	AN26
BG25	VSS	P38	AD18	VSS	AN31
BG28	VSS	P4	AD28	VSS	AN33
BG34	VSS	P45	AD35	VSS	AN35
BG37	VSS	P5	AD37	VSS	AP12
BG41	VSS	R21	AD45	VSS	AP21
BG48	VSS	R24	AD48	VSS	AP25
BH	VSS	R25	AD5	VSS	AP26
C1	VSS	R28	AD6	VSS	AP28
C12	VSS	T1	AD8	VSS	AP31
C24	VSS	T12	AD9	VSS	AP32
C4	VSS	T15	AE12	VSS	AP38
C49	VSS	T17	AE13	VSS	AP45
D1	VSS	T33	AE15	VSS	AP4
D7	VSS	T38	AE17	VSS	AP45
D13	VSS	T49	AE18	VSS	AT12
D2	VSS	U19	AE35	VSS	AT17
D2S	VSS	U21	AE37	VSS	AT19
D2S	VSS	U22	AE45	VSS	AT21
D33	VSS	U24	AE5	VSS	AT22
D37	VSS	U25	AF28	VSS	AT24
D46	VSS	U26	AG1	VSS	AT29
D49	VSS	U28	AG12	VSS	AT26
D7	VSS	U29	AG17	VSS	AT28
E1	VSS	V31	AG30	VSS	AT29
E15	VSS	V12	AG22	VSS	AT31
E19	VSS	V17	AG23	VSS	AT33
E22	VSS	V21	AG26	VSS	AT37
E25	VSS	V22	AG37	VSS	AT39
E25	VSS	V24	AG28	VSS	AT5
E26	VSS	V25	AG35	VSS	AG25
E33	VSS	V33	AG37	VSS	AV11
E38	VSS	V38	AH12	VSS	AV39
E37	VSS	V5	AH13	VSS	AV45
E38	VSS	V27	AH15	VSS	AV49
E9	VSS	W28	AH35	VSS	AW11
F25	VSS	W30	AH38	VSS	AW24
	VSS	W44			
	VSS	Y12			
	VSS	Y13			
	VSS	Y15			
	VSS	Y17			
	VSS	Y18			
	VSS	Y32			
	VSS	Y38			

FH82560 SFRKMS/10H-B1-03B560-10R

FH82560 SFRKMS/10H-B1-03B560-10R

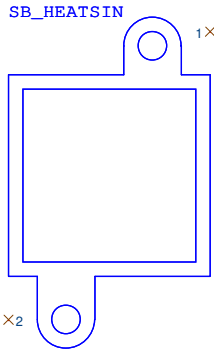
裝甲HEATSINK 分成四大部份

MOS\_HS



PCH\_HS

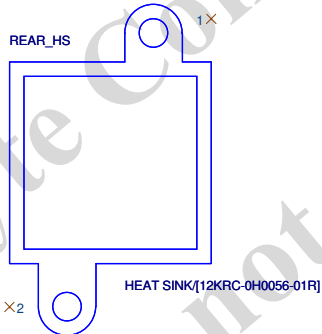
Location: PCH\_HS 12SP2-S04207-J2R/J4R



FOOTPRINTS :  
sink\_b550m\_aorus\_pro\_pch

裝甲

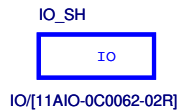
Location: REAR\_HS 12KRC-0H0044-01R



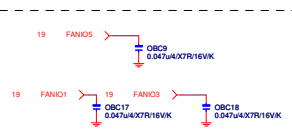
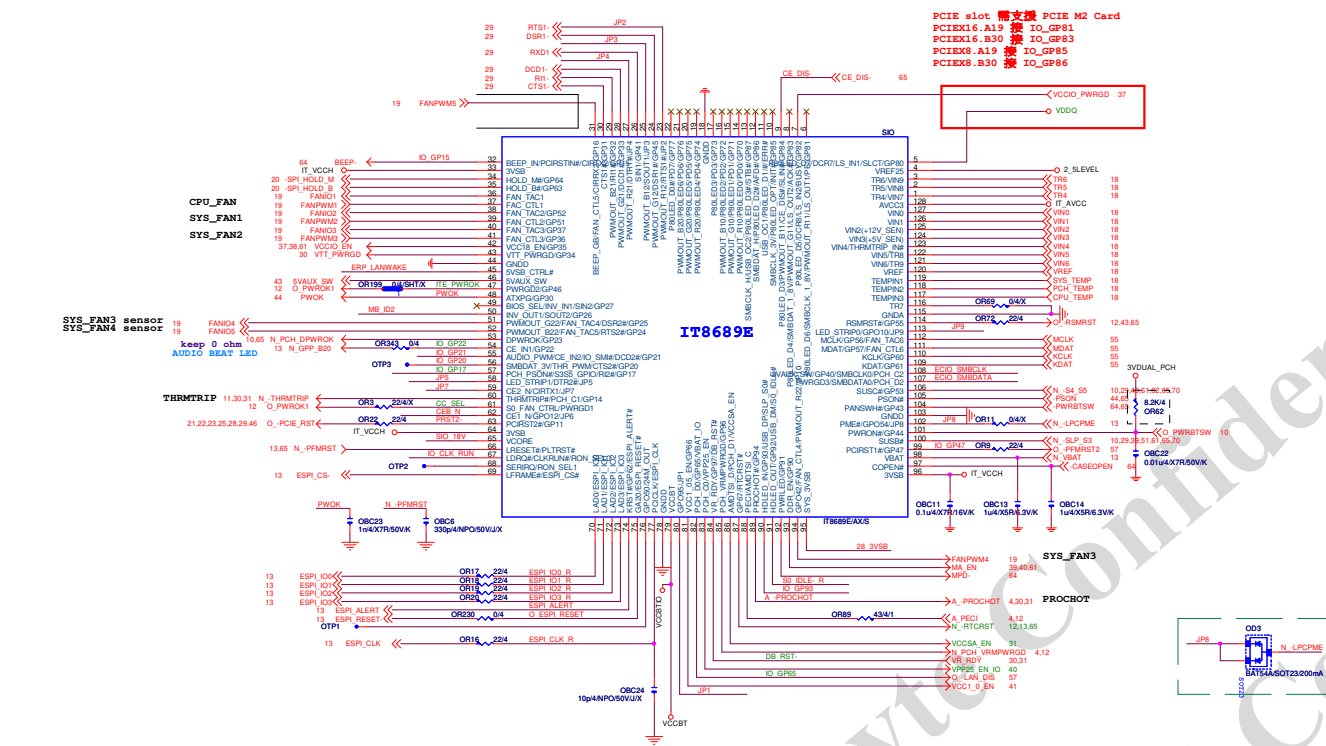
Footprint :  
Z590\_AORUS\_ELITE\_AX\_IO\_COVER

後窗鐵片

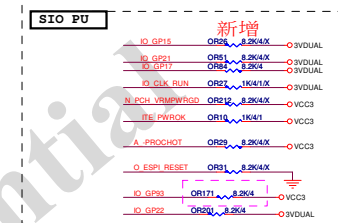
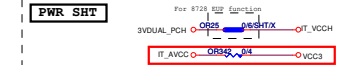
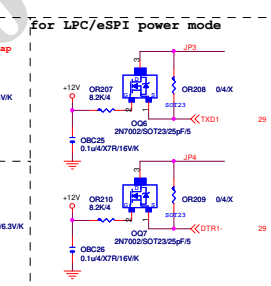
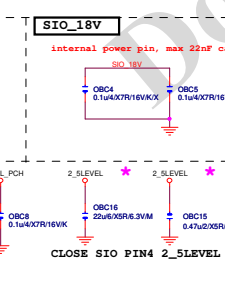
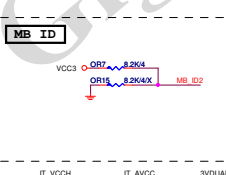
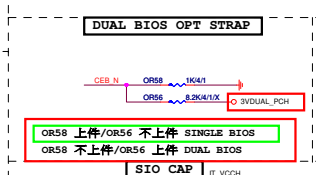
Location: IO\_SH 11AIO-0C0015-22R



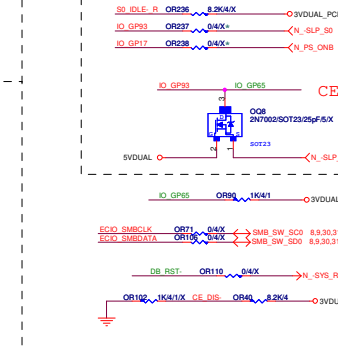
- \* PCB顏色 : 咖啡黑
- \* 文字面 : 灰色
- \* 疊構 : E
- \* 圖騰: ID設計Z590版GIGABYTE GAMING



FAN TABLE	
CPU_FAN	FAN_CTL1
FAN_FAN1	FAN_CTL2
FAN_FAN2	FAN_CTL3
FAN_FAN3	FAN_CTL4
FAN_FAN4	FAN_CTL5
THRMTRIP	PIN56
PROCHOT	PIN89



SIO STRAP	
JP2	1 Disable WDT to rest PWRK
JP3	0 Enable WDT to rest PWRK
JP4	Dual-BIOS CS pin mode select bit '0' See the below table
JP5	1 LPC/ESPI power VCCBT = 3.3V
JP6	0 LPC/ESPI power VCCBT = 1.8V
JP7	0 ESPI I/F
JP8	1 Enable Dual BIOS Function (for GigaByte Only)
JP9	0 Disable Dual BIOS Function (for GigaByte Only)
JP10	Dual-BIOS CE pin mode select bit '1' See the below table
JP11	1 CE pin disable (Hold pin mode)
JP12	0 CE mode 1
JP13	0 CE mode 2
JP14	0 CE mode 3



請依開案規格，選擇Support Exp下 LAN Wake up組態。

(組態一) PCIE LAN (Single & Dual LAN)

(組態二) INTEL219 LAN (Single LAN)

(組態三) INTEL LAN+PCIE LAN (Dual LAN)

ERP Wake on LAN

Single LAN	Realtek	組態一
Dual LAN (只有一條 LAN 交換器)	Atheros	組態二
ERP WAKE UP	Intel 219	組態三
No Support ERP	Atheros+Realtek	組態四
	Intel 219+Intel 219	組態五

Gigabyte Technology

File	IT8688
Rev	1.01
Docu	B560M AORUS ELITE DB
Date	Monday, March 22, 2021
Sheet	17 of 72

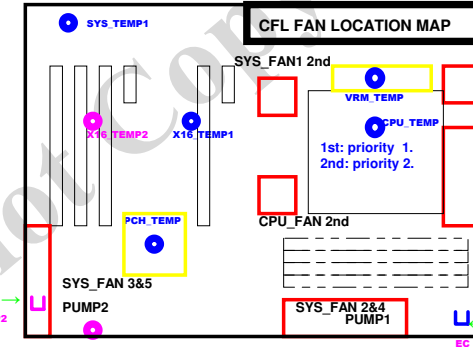
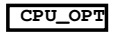
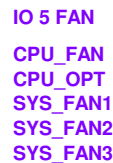
The diagram illustrates the power supply section of the T7828 EX module. It shows the following components and connections:

- VCCSA:** Connected to VIN3 through resistor OR75 (8.2K/4).
- VDDQ\_SIO:** Connected to VIN2 through resistor OR74 (8.2K/4).
- VCC3:** Connected to VIN2 through resistor OR57 (6.49K/4/1).
- +12V:** Connected to VIN2 through resistor OR79 (75K/4/1), which is circled in red. A 2.0V regulator is indicated next to it.
- VCCGT:** Connected to VIN2 through resistor OR76 (8.2K/4).
- VCC\_SIO:** Connected to VIN2 through resistor OR78 (15K/4/1), which is highlighted with a red box.
- Internal Components:**
  - Resistors: ORC9, ORC8, OC4, ORC6, OR70, ORC10, ORC12, ORS3, ORC7.
  - Capacitors: OC9, OC8, OC4, OC12, OC10, OC7.
  - Regulator: 2.0V (OR79, 75K/4/1).
- Annotations:**
  - VIN2 must +12V input
  - VIN3 must VCC input
  - The division voltage of VIN2 & VIN3 must be around 2.9V



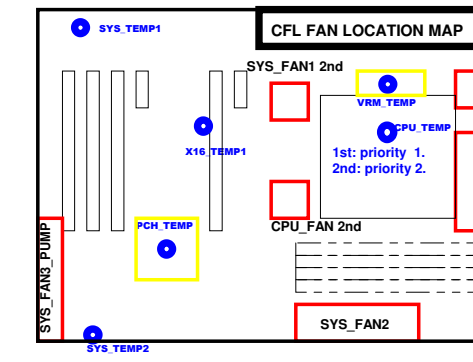
## Gigabyte Technology

Title				HWM,KB/MS, FAN CTRL			
Size	Document Number					Rev	
Custom	<b>B560M AORUS ELITE DB</b>					1.01	
Date:	Monday, March 22, 2021	Sheet	18	of	72		

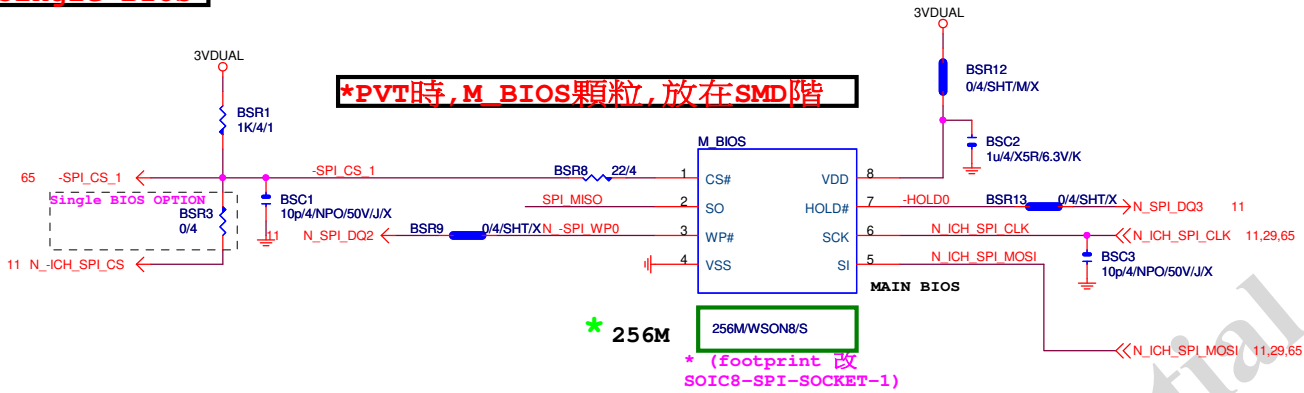


8 FAN from IO & EC

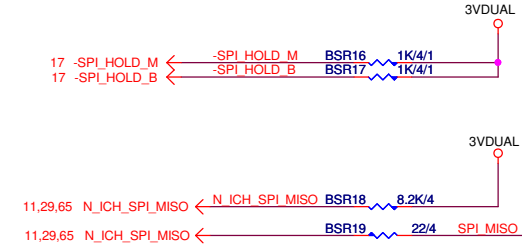
- EC TEMP SENSE 879X
- IO TEMP SENSE 8686

5 FAN from IO  IO TEMP SENSE 8686

DUAL BIOS \*Single BIOS

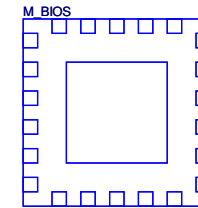
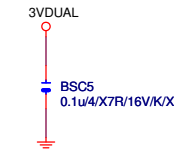


MOSI For DMI RX Termination Voltage



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating  
0 means PD 1K



LCP/G-FL/1.27mm/200MIL/WHITE[10SL2-000008-71R]/X

\* 試產先上, PVT 移除

Gigabyte Technology

Title			BIOS
Size	Document Number	Rev	1.01
Custom	B560M AORUS ELITE DB		
Date:	Monday, March 22, 2021	Sheet	20 of 72

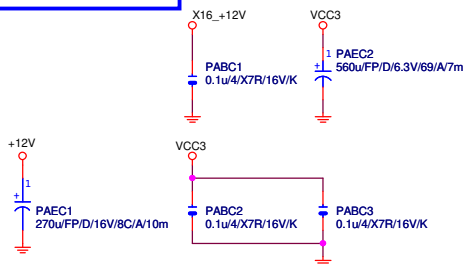


Rev 0.3

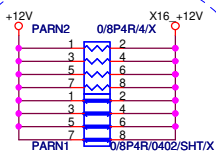
PCIEX16 CAP

PCIEX16 SLOT

PCIESLOT-1645TH



PCIEX16 PROTECT SHT

+12 protect  
short-wire test

PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

FOR SMBUS  
PCIEX16不能short pad

22.23.68 SMB\_SW\_SC1  
22.23.68 SMB\_SW\_SD

PAR3 0/4/SHT/X  
PAR4 0/4  
PAR5 0/4

3VDUAL VCC3

11,17,22,23,46 N\_PCIE\_WAKE

-PCIEX16\_PR

PA EXP TXP0 C

PA EXP TXN0 C

-PCIEX16\_PR

PA EXP TXP1 C

PA EXP TXN1 C

PA EXP TXP2 C

PA EXP TXN2 C

PA EXP TXP3 C

PA EXP TXN3 C

-PCIEX16\_PR

PA EXP TXP4 C

PA EXP TXN4 C

PA EXP TXP5 C

PA EXP TXN5 C

PA EXP TXP6 C

PA EXP TXN6 C

PA EXP TXP7 C

PA EXP TXN7 C

-PCIEX16\_PR

PA EXP TXP8 C

PA EXP TXN8 C

PA EXP TXP9 C

PA EXP TXN9 C

PA EXP TXP10 C

PA EXP TXN10 C

PA EXP TXP11 C

PA EXP TXN11 C

PA EXP TXP12 C

PA EXP TXN12 C

PA EXP TXP13 C

PA EXP TXN13 C

PA EXP TXP14 C

PA EXP TXN14 C

PA EXP TXP15 C

PA EXP TXN15 C

-PCIEX16\_PR

PCIEX16 3GIO\_\*16

KEY

12V

12V

RSVD

GND

SMCLK

SMDAT

GND

3.3V

3.3V AUX

WAKE\*

RSVD

GND

HSOP0

HSOP0

HSOP0

HSOP0

HSOP0

HSOP0

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PCI-E16X-164P/BK/LONG DOUBLE/HK2/SHELL/GEN4.0

黑色 金屬加強

X16 +12V

PAR1 0/4/SHT/X

PAR2 0/4/SHT/X

VCC3

GND

GND

GND

GND

GND

GND

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PCIEX16:16/5/5/5/16

PA EXP RXP[0..15] &gt;&gt;&gt; PA\_EXP\_RXP[0..15] 4

PA EXP RXN[0..15] &gt;&gt;&gt; PA\_EXP\_RXN[0..15] 4

PA EXP TXP[0..15] &gt;&gt;&gt; PA\_EXP\_TXP[0..15] 4

PA EXP TXN[0..15] &gt;&gt;&gt; PA\_EXP\_TXN[0..15] 4

PCI-E REV:1.1--&gt; 2.5GHz

PCE-E X1(單向) BANDWIDTH=2.5GHz\*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWIDTH=2.5GHz\*(8b/10b) X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWIDTH=2.5GHz\*(8b/10b) X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWIDTH=2.5GHz\*(8b/10b) X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--&gt; 5GHz

PCE-E X1(單向) BANDWIDTH=5GHz\*(8b/10b)=4Gb/s=500MB/s

PCI-E REV:3.0--&gt; 8GHz

PCE-E X1(單向) BANDWIDTH=8GHz\*(128b/130b)=8Gb/s=1GB/s

Gigabyte Technology

Title			PCI EXPRESS * 16
Size	Document Number	Rev	1.01
Custom	B560M AORUS ELITE DB		
Date:	Monday, March 22, 2021	Sheet	21 of 72

Rev 0.51

PCIE\*4

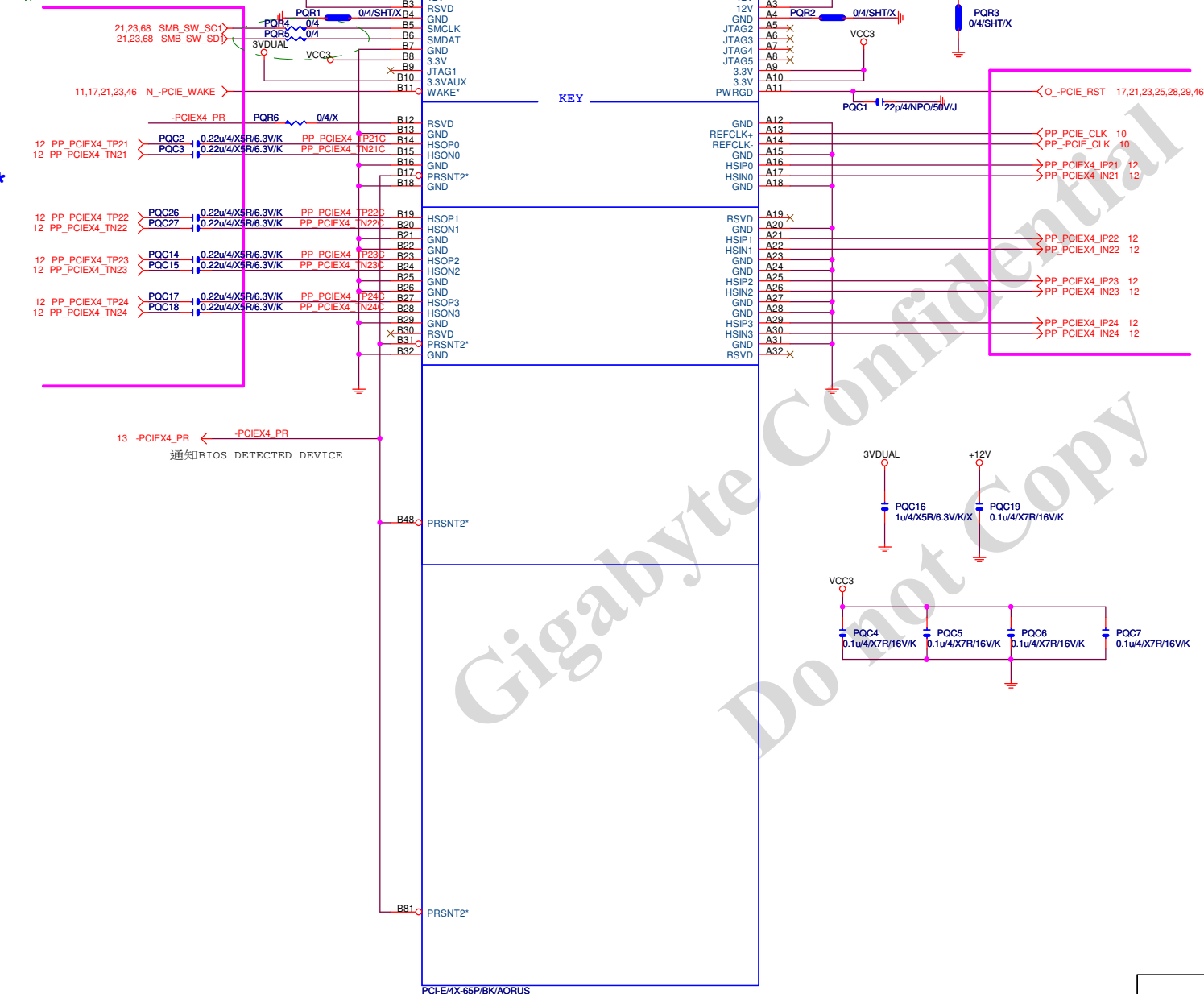


Footprint "PCIESLOT-64P-1"

# FOR SMBUS

PCIE4 SM Bus電阻layout 改0 ohm

上件



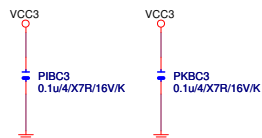
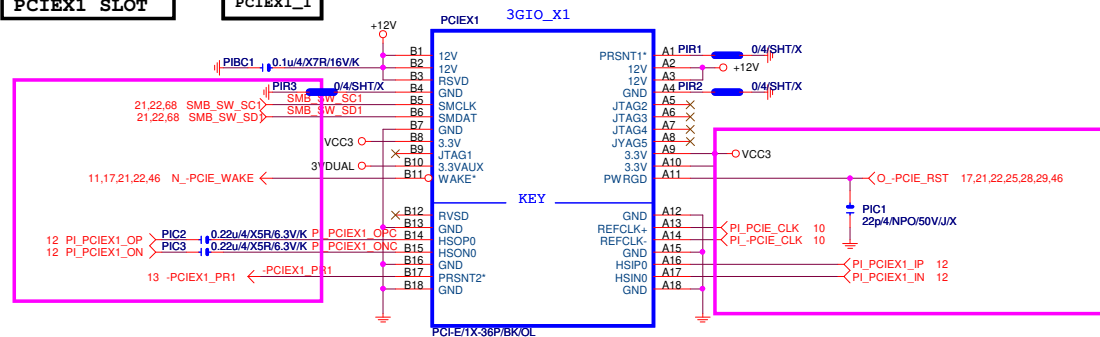
黑色

Gigabyte Technology			
Title			
PCIE X4			
Size	Document Number	Rev	
Custom	B560M AORUS ELITE DB	1.01	
Date:	Monday, March 22, 2021	Sheet	22 of 72

Rev 0.51

PCIEX1 SLOT

PCIEX1\_1



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Do not Copy

Gigabyte Technology

Title				
PCIE X1 *3				
Size	Document Number			Rev
Custom	B560M AORUS ELITE DB			1.0
Date:	Monday, March 22, 2021	Sheet	23	of 72

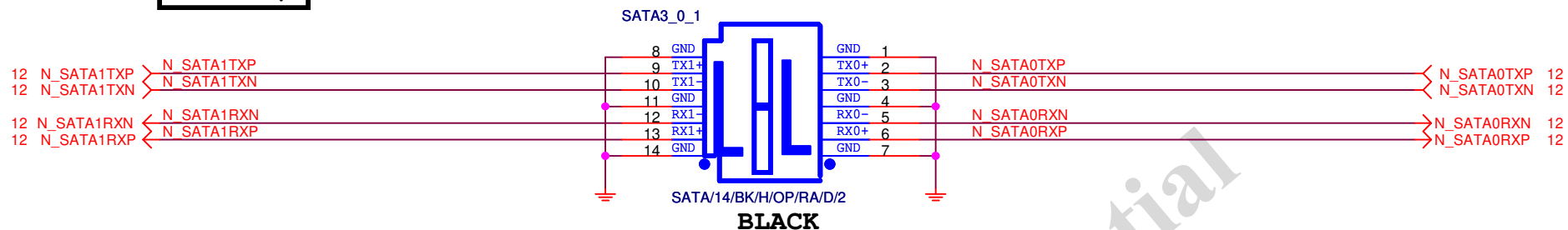
## IO18/IO19 To SATA3 port0/1

6 SATA3 from Z490 (90度R-A)

**SATA3 0/1**

上 Port (8~14)

下 Port (1~7)

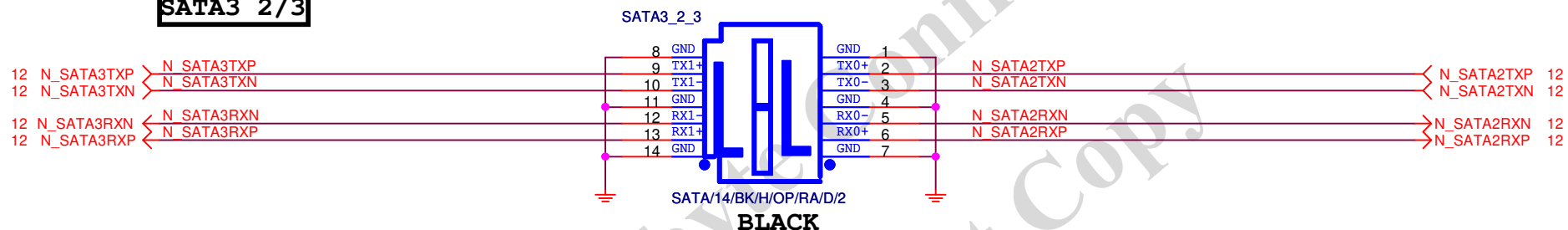


Footprint : H2X7-SATA2-D90

## IO20/IO21 To SATA3 port2/3

上 Port (8~14)

下 Port (1~7)

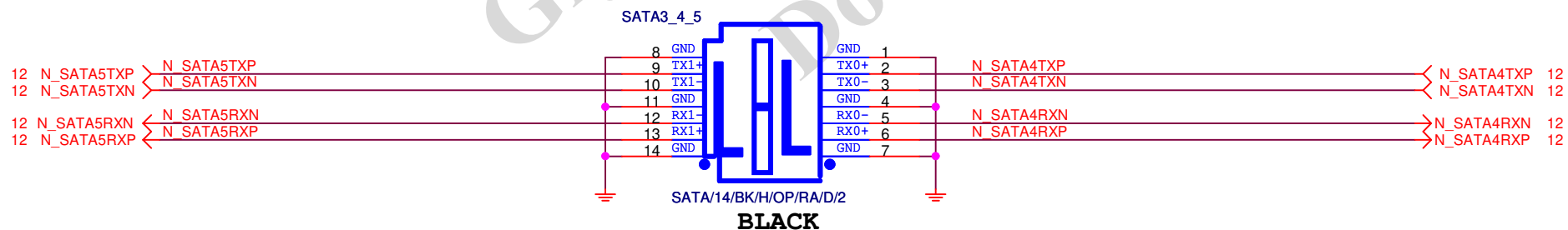


Footprint : H2X7-SATA2-D90

## IO22/IO23 To SATA3 port4/5

上 Port (8~14)

下 Port (1~7)



Footprint : H2X7-SATA2-D90

**Gigabyte Technology**

Title

**SATA**

Size

Document Number

Rev

**B560M AORUS ELITE DB**

**1.01**

Date: Monday, March 22, 2021

Sheet 24 of 72

Rev 0.1

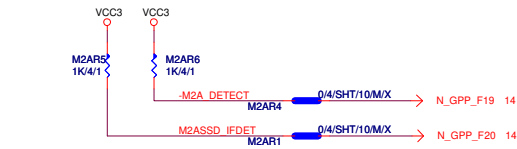
**M.2 Lane4 from PCH port9**

## M.2 Lane3 from PCH port10

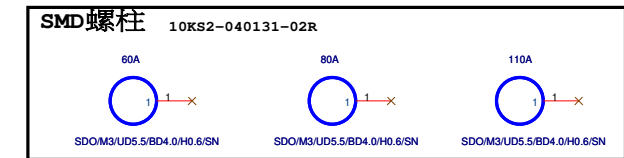
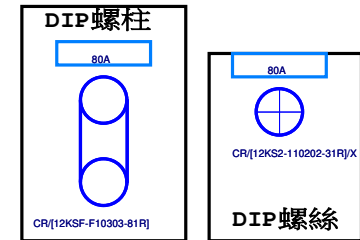
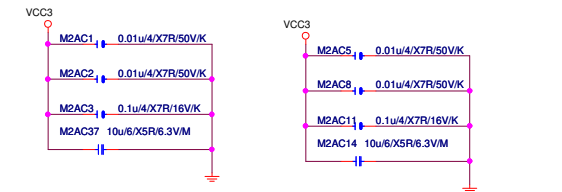
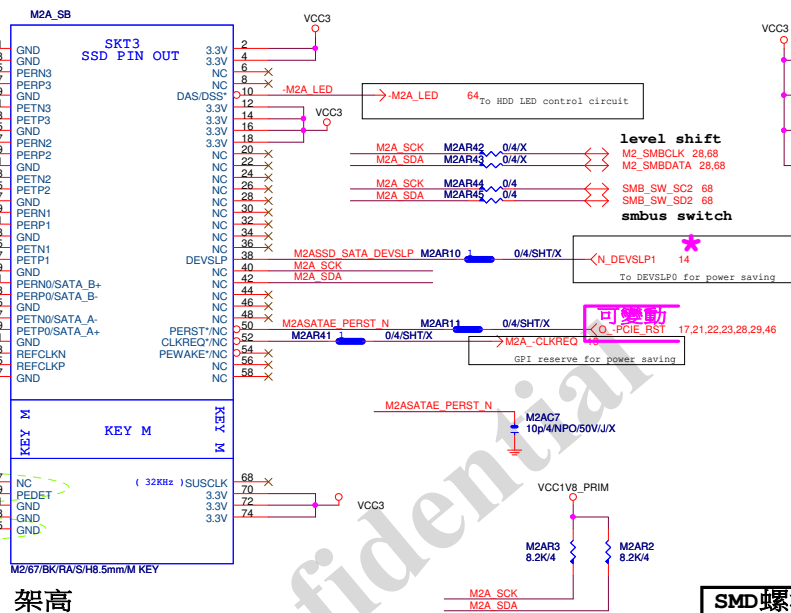
**M.2 Lane2 from PCH port11**

**M.2 Lane1 from PCH port12**

支援SATA and M.2 function



Footprint : M2\_110\_H2MM8W



\* Footprint : HOLE\_C236D165-A  
10KS2-040131-02R:SDO/M3/UD5.5/BD4.0/H0.6/SN

Flex IO priority	N_GPP_K1	IO14 PCIe#9	IO15 PCIe#10	IO16 PCIe#11	IO17 PCIe#12
M2A SATA	L	PCIE	PCIE	SATA 0	SATA 1
M2A PCIE (PCIE Reverse)	H	PCIE	PCIE	PCIE	PCIE

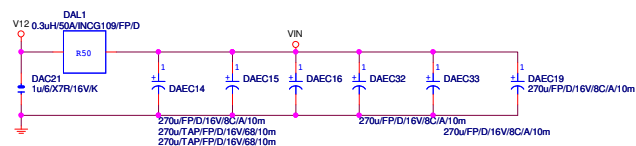
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GIGABYTE™			
Title AQC113C			
Size Custom	Document Number B560M AORUS ELITE DB		Rev 1.01
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## VIN CAP

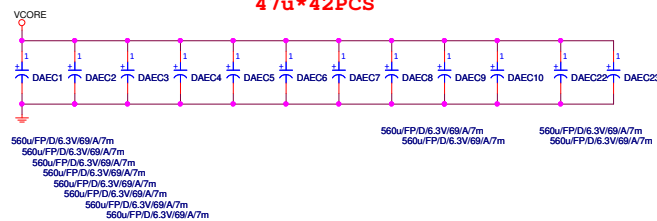
270u\*6PCS



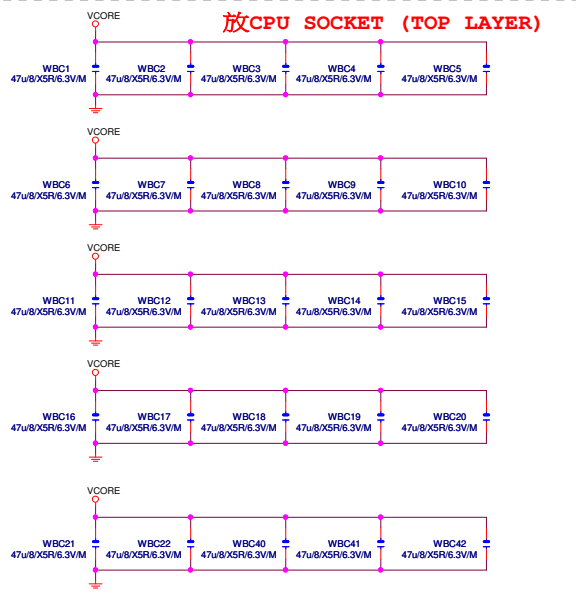
## VCORE CAP

560u\*12PCS

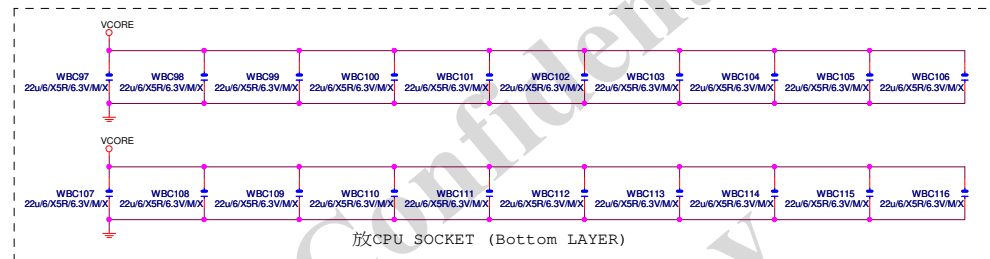
47u\*42PCS



## 放CPU SOCKET (TOP LAYER)



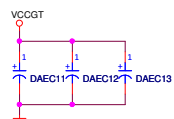
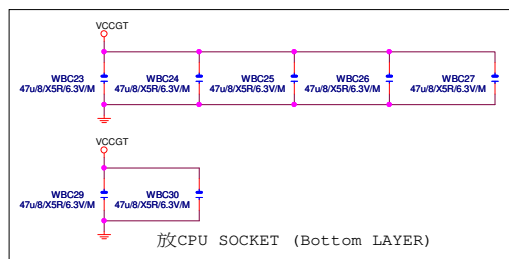
## 放CPU SOCKET (Bottom LAYER)



## VCCGT CAP

560u\*3PCS

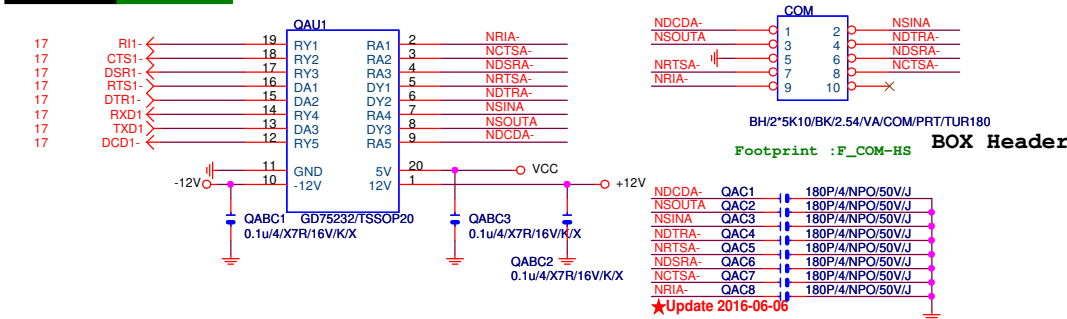
47u\*7PCS

560uF/6.3V/69A/7m  
560uF/6.3V/69A/7m  
560uF/6.3V/69A/7m



COM PORT

Rev: 0.94

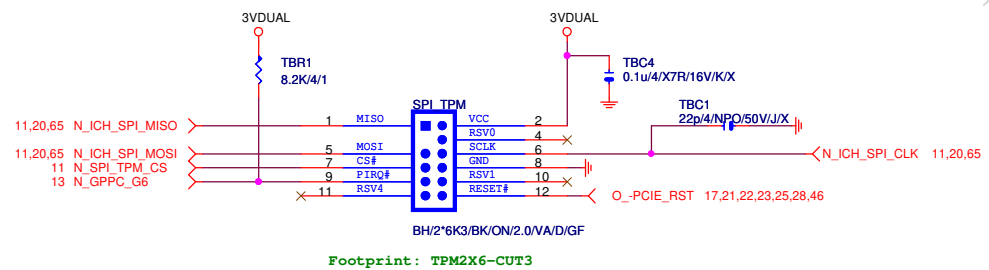


LPT PORT

RTD3 GPIO refer by Intel RVP

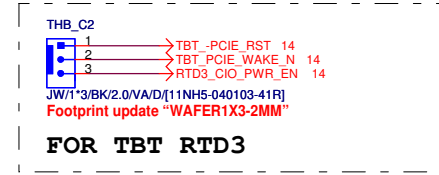
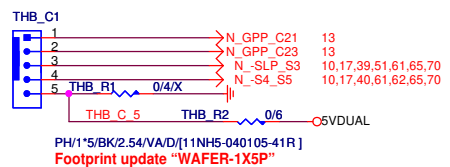
GPIO	CFL-S	CML-U	CML-H	CML-S
TBT_PERST_N	GPP_F_2	GPPC_C15_SLOT1_RST_N	GPP_F4_SATAPCIE7	GPP_F2
TBT_Wake_N	GPP_H_15	GPPC_D11_SLOT1_WAKE_N	GPPK_18	GPP_H15
RTD3_PWN_EN	GPP_I_5	GPPC_D15	GPP_H_16_SML4_CLK	GPP_K23

TPM CONNECT



Thunderbolt

★Update 2015-12-29



FOR TBT RTD3

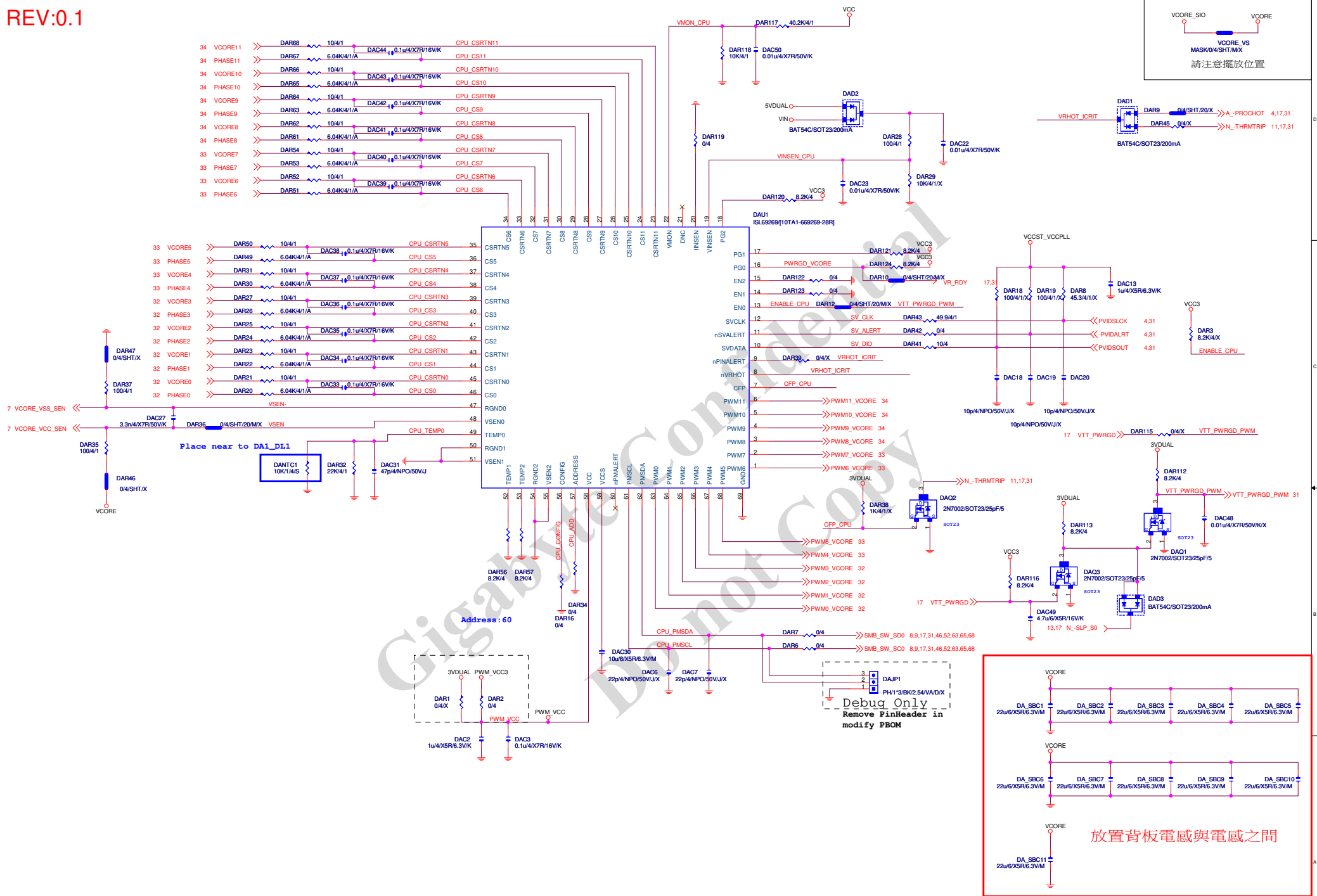
Z490系列使用

TBT\_-PCIE\_RST : CFL connector to GPP\_F\_2

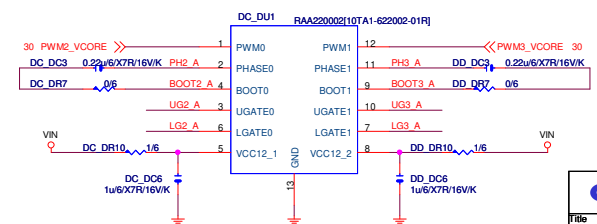
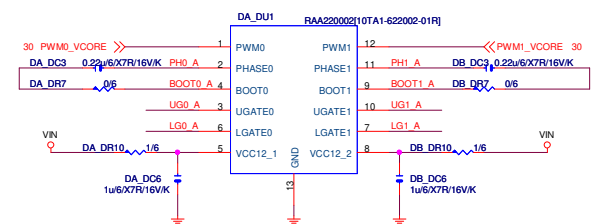
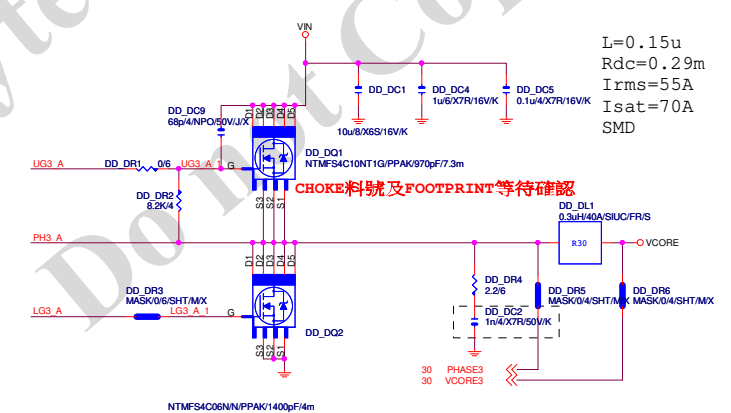
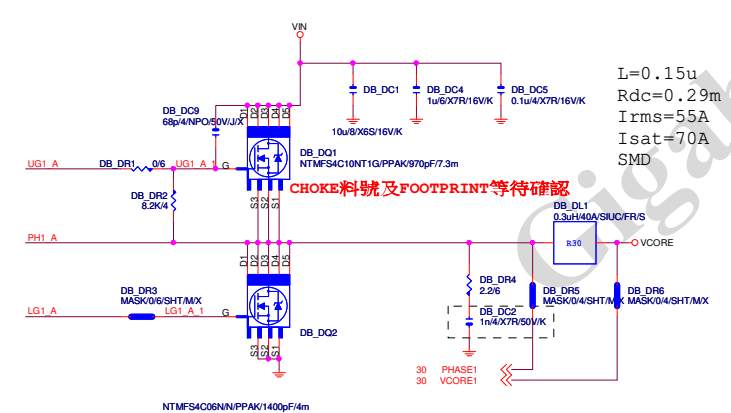
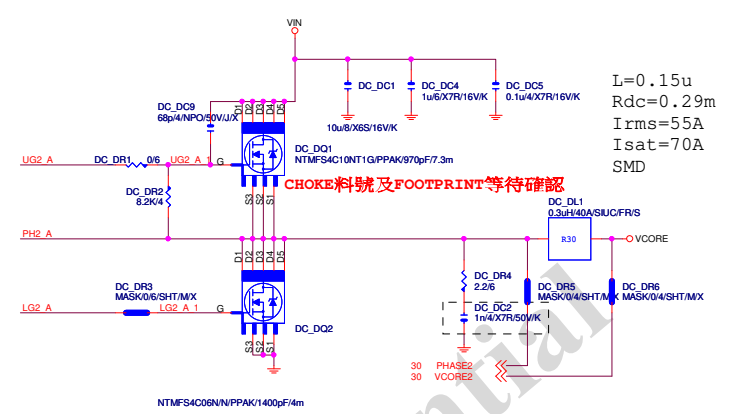
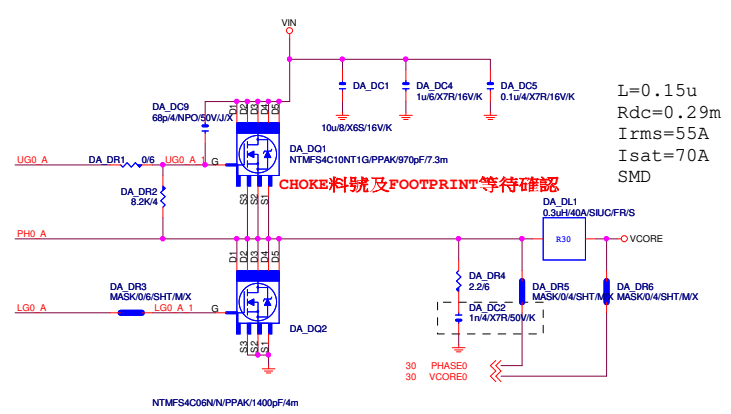
TBT\_PCIE\_WAKE\_N : CFL connector to GPP\_H\_15

RTD3\_CIO\_PWR\_EN : CFL connector to GPP\_K\_23

REV:0.1





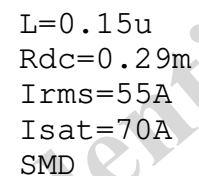


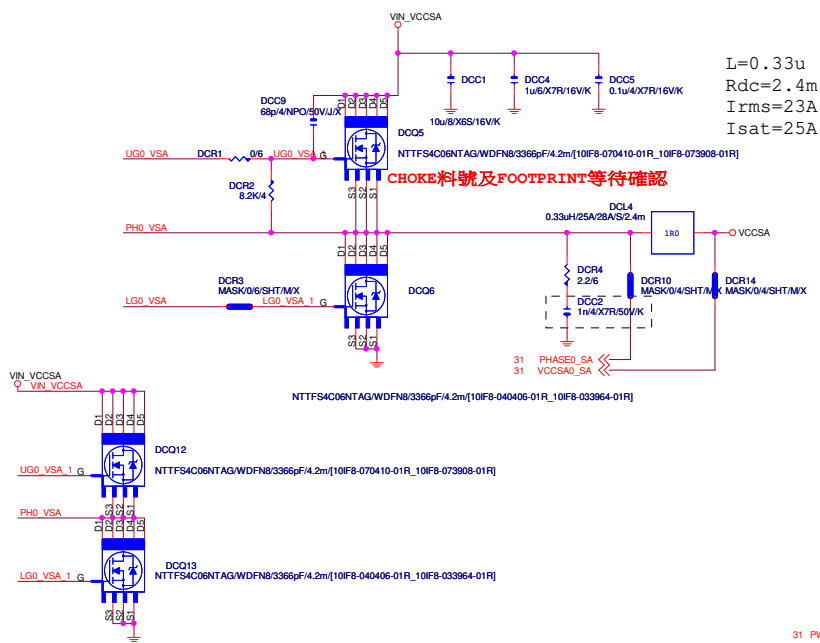






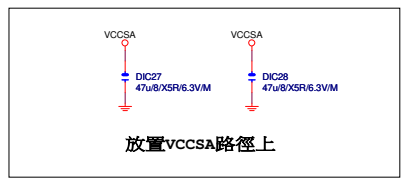
**DRMOS使用NCP302155時PIN2 and PIN3要上件(Ex:DA\_DR13.DA\_DR8.DA\_DC3)**





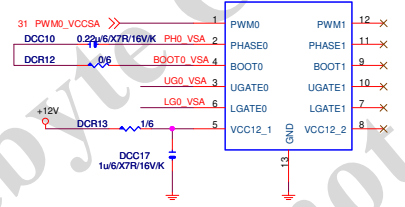
L=0.33u  
Rdc=2.4m  
Irms=23A  
Isat=25A

### VCCSA CAP



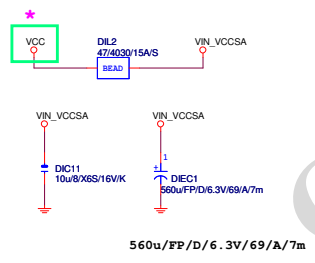
CPU SOCKET 已經有22u/6 \*4pcs

DCU2 RAA220002[10TA1-622002-01R]

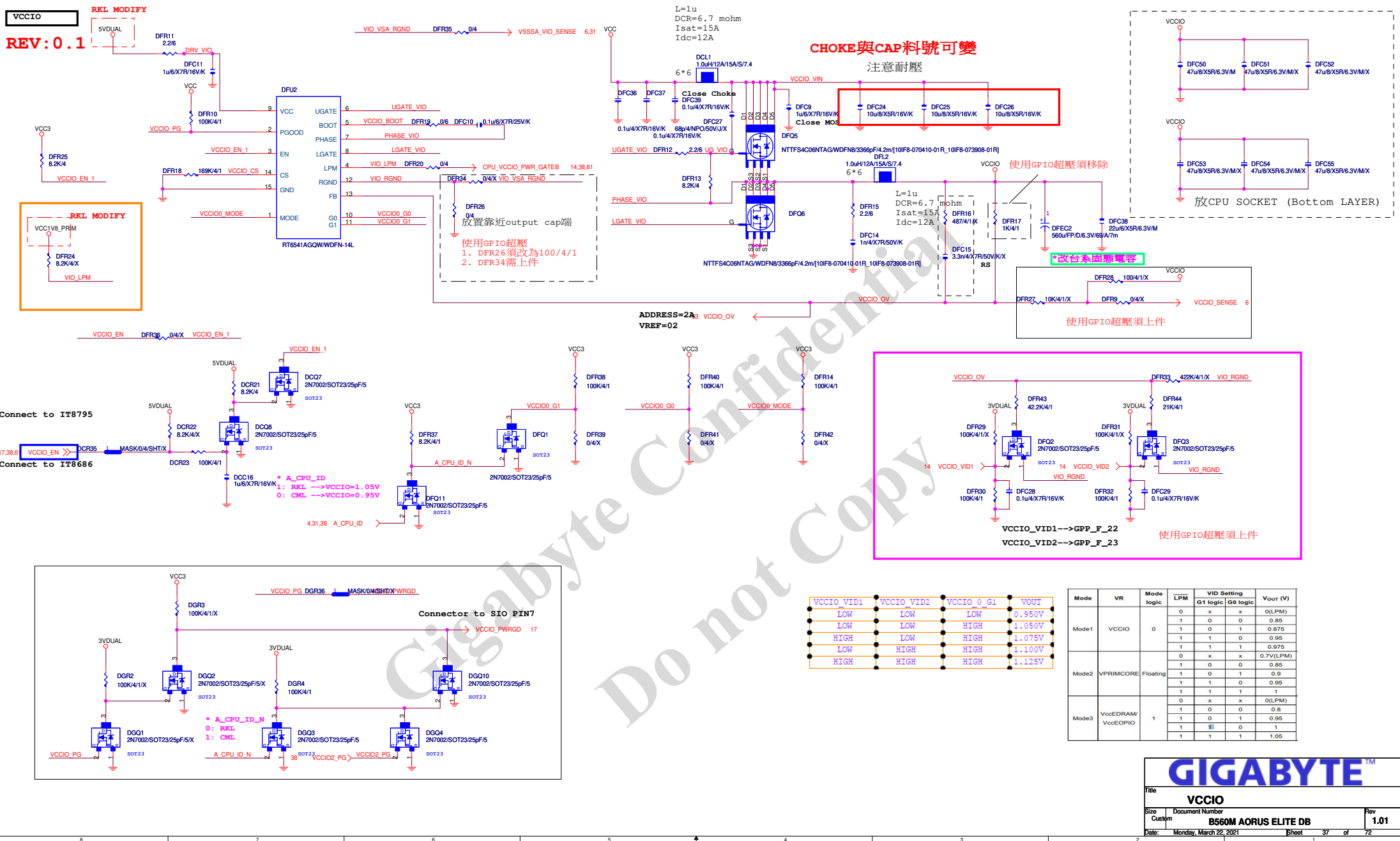


### VIN CAP

100u\*1PCS



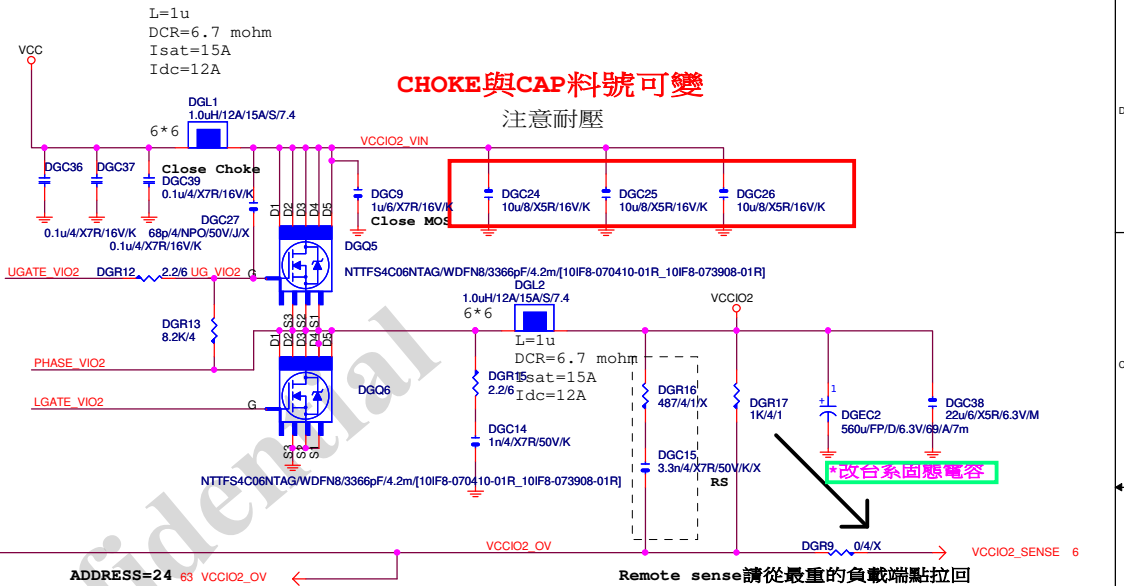
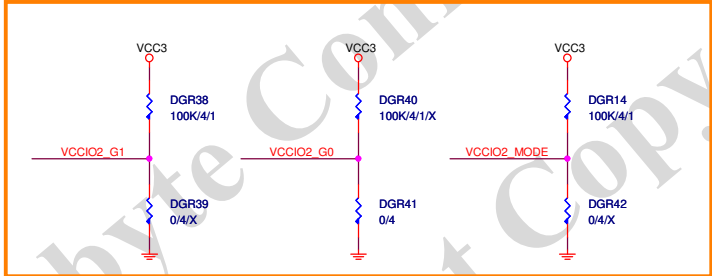
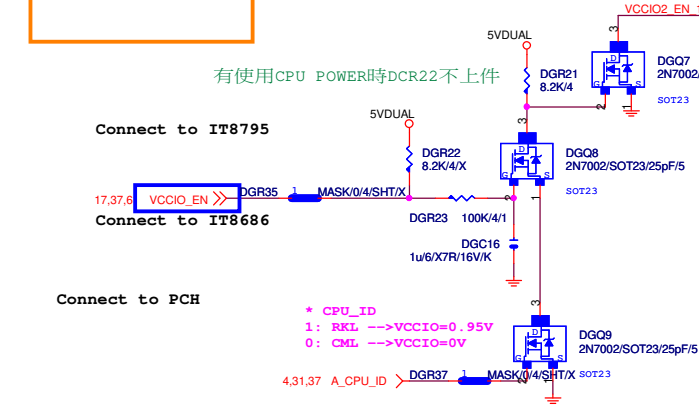
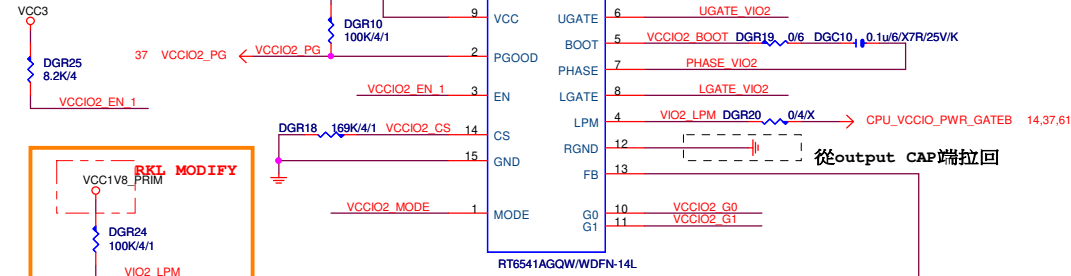
REV: 0.1

**GIGABYTE™**

VCCIO2

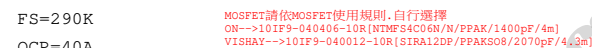
REV: 0.1

RKL MODIFY

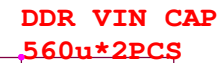
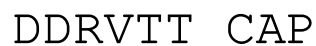


GIGABYTE™			
Title			
VCCIO2			
Size	Document Number	Rev	
Custom	B560M AORUS ELITE DB	1.01	
Date:	Monday, March 22, 2021	Sheet	38 of 72

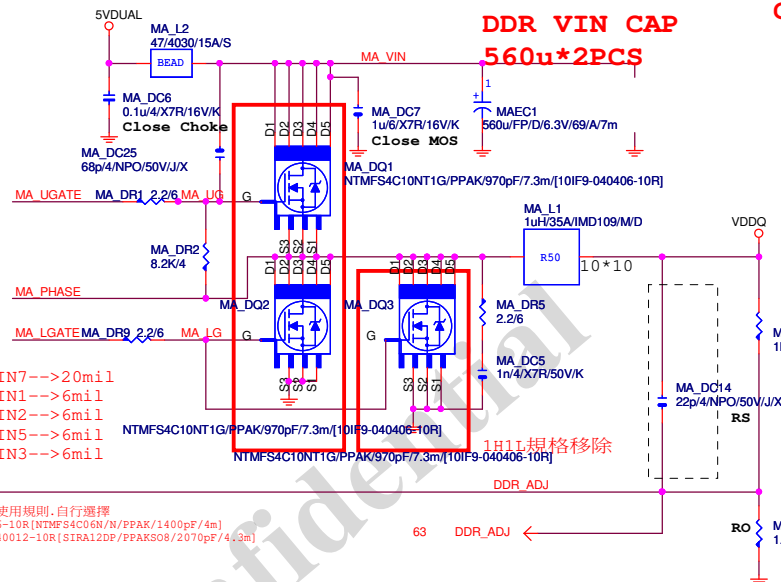




VPP\_25V使用8120時上件



**CHOKES與CAP料號可變**



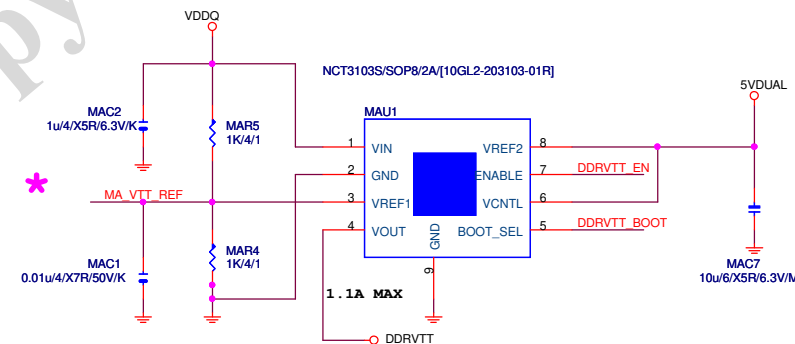
SUPPORT DDR4 1.2V

25A MAX

L=1u  
DCR=2.5 mohm  
Isat=35A  
Idc=28A

請放置CHOKE一出來位置.先預留.  
請自行確認ripple後再決定是否上件

Remote sense 請從最重的負載端點拉回



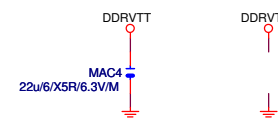
10 17 29 51 61 65 70 N-SLP\_S3


4 DDR\_VTT\_CTL

DDR VTT CTL MAR110 MASK0/4/SHT/M/10/X DDRVTT\_EN

N-SLP\_S3 MAR111 MASK0/4/SHT/M/10/X DDRVTT\_BOOT

☆ *Journal of Management Education* 32(10):1109-1121



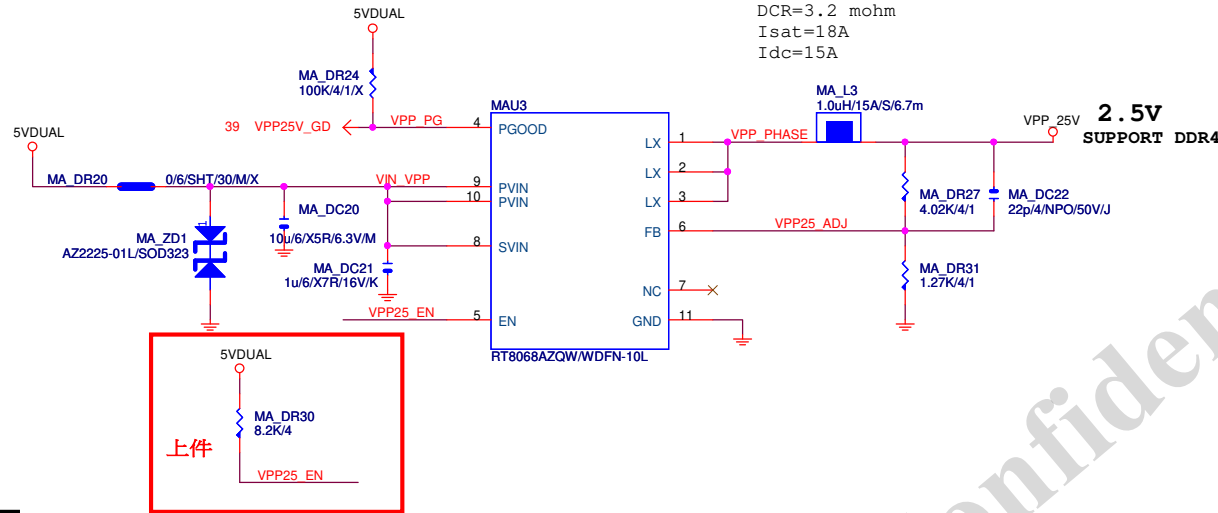
			
Title			
RT8237_DDR4 POWER			
Size	Document Number		Rev
Custom	B560M AORUS ELITE DB		1.01
Date:	Monday, March 22, 2021	Sheet	39 of 72

REV:0.11

VPP 25V

CHOKE與CAP料號可變

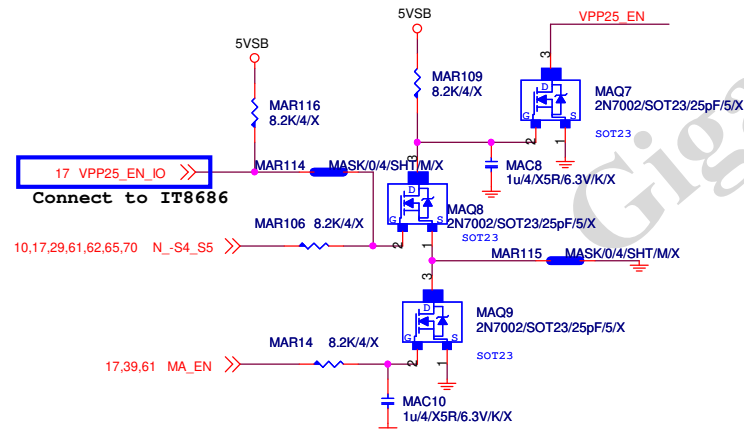
L=1u  
DCR=3.2 mohm  
Isat=18A  
Idc=15A



PWR SEQ

PWR SEQ

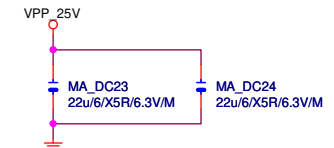
\* 刪 MA\_DR32



17 VPP25\_EN\_IO >> MASK/0/4/SHT/M/X  
VPP25\_EN

VPP CAP 22u\*2PCS

\* 大電容 x0

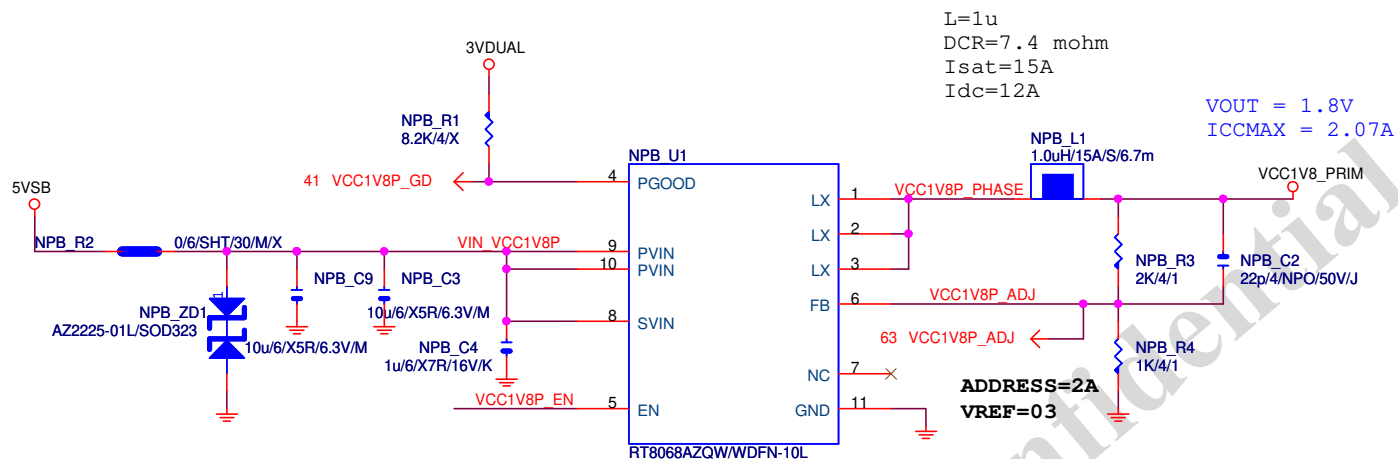


<b>GIGABYTE™</b>			
<b>RT8068A_VPP25 POWER</b>			
Document Number	B560M AORUS ELITE DB		Rev 1.01
Date: Monday, March 22, 2021	Sheet 40	of 72	

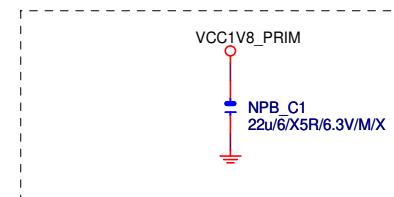


REV: 0.1

# VCC1V8 PRIM

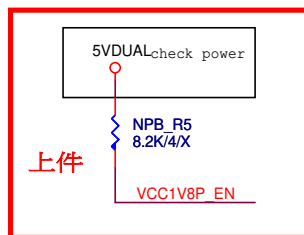


CHOKE與CAP料號可變



請放置CHOKE一出來位置. 先預留.  
請自行確認ripple後再決定是否上件

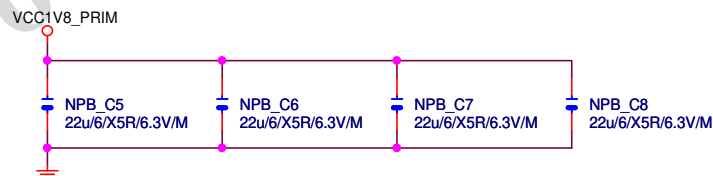
## PWR SEQ



connect to PCH pin AD46

11,41 SLP\_SUS\_N >> NPB\_R6 0/4 VCC1V8P\_EN

## VCC1V8\_PRIM CAP 22u\*4PCS



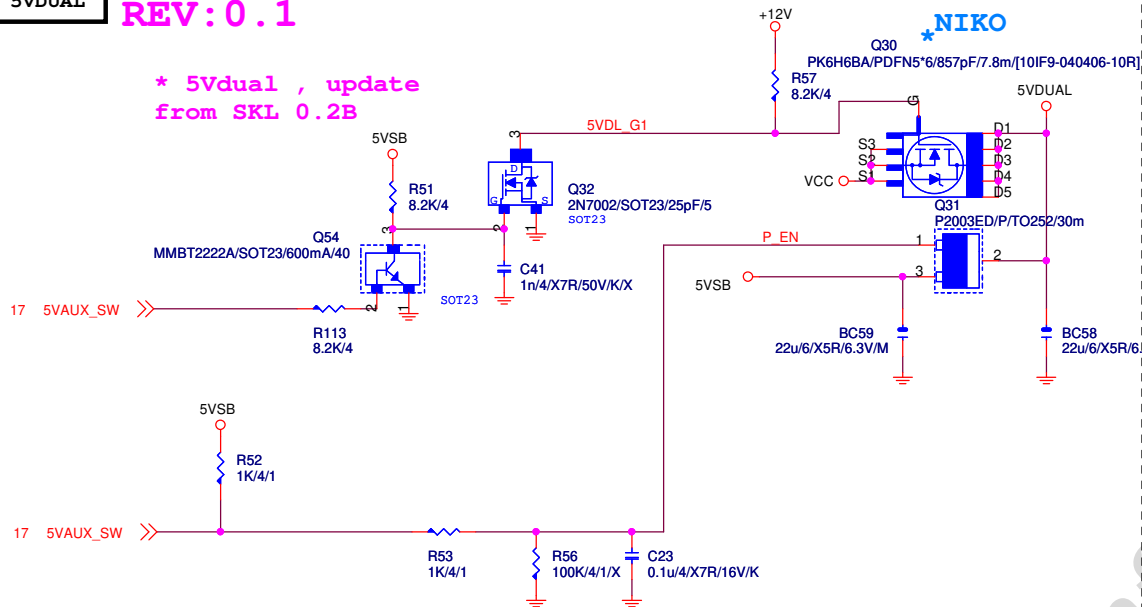
**GIGABYTE™**

Title <b>RT8068_VCC1V8_PRIM</b>		
Size Custom	Document Number <b>B560M AORUS ELITE DB</b>	Rev <b>1.01</b>
Date: Monday, March 22, 2021	Sheet 42	of 72

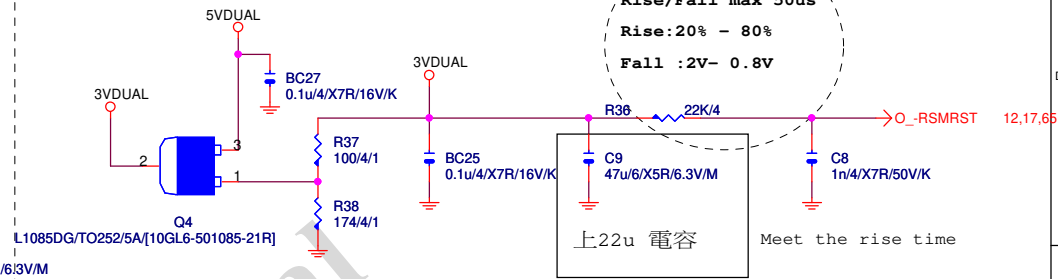
# 5VDUAL

REV:0.1

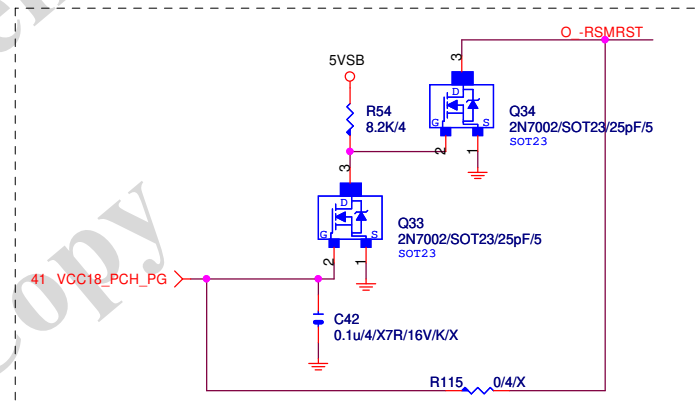
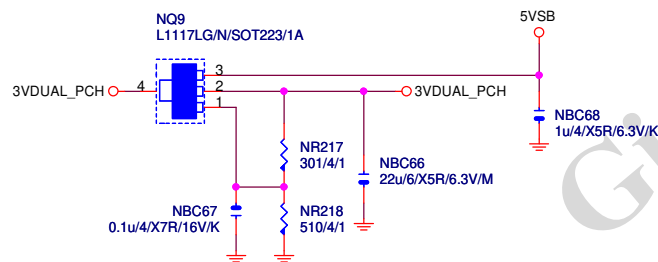
\* 5Vdual , update  
from SKL 0.2B



# 3VDUAL



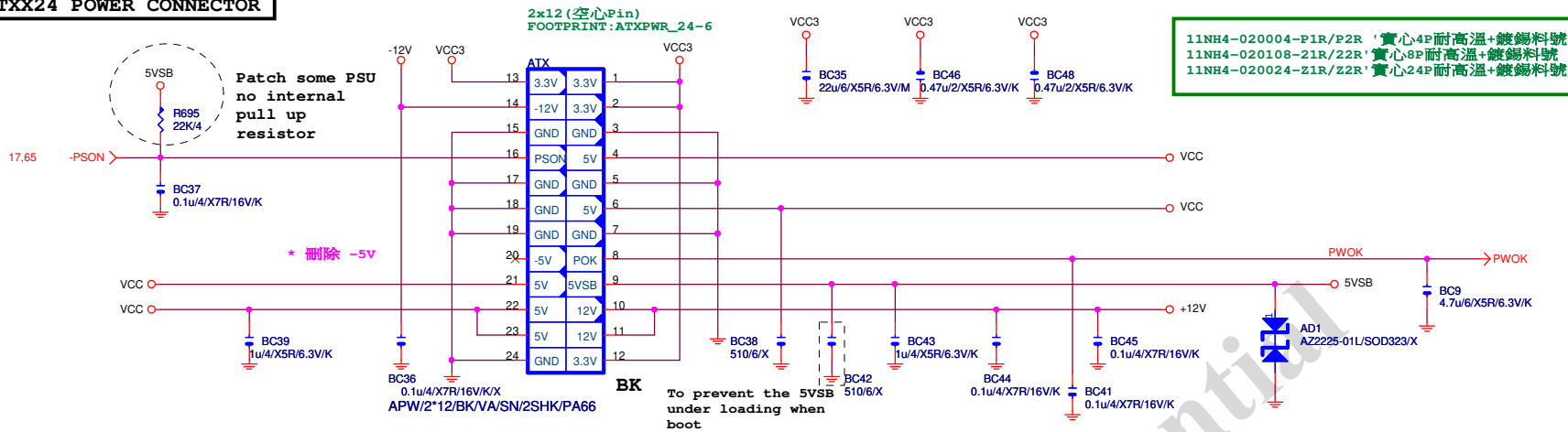
# 3VDUAL\_PCH



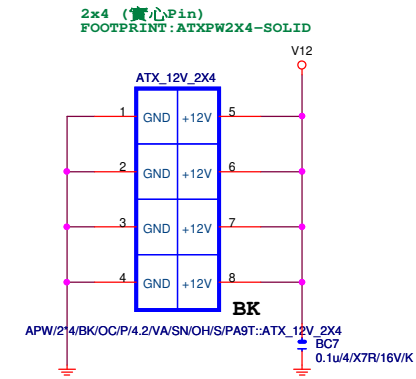
Gigabyte Technology

Title			
DISCRETE POWER			
Size	Document Number	Rev	
Custom		B560M AORUS ELITE DB1.01	
Date:	Monday, March 22, 2021	Sheet	43 of 72

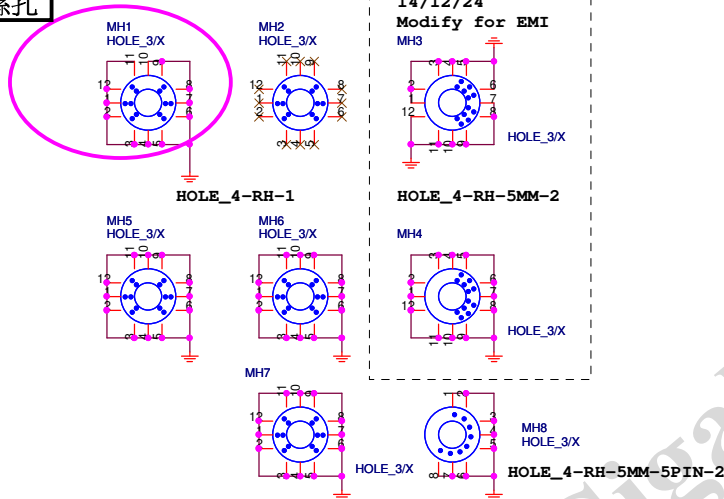
## ATXX24 POWER CONNECTOR



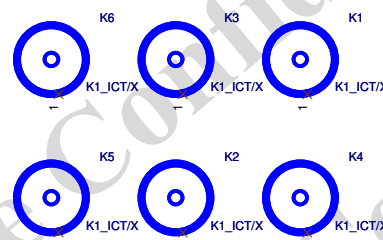
## ATXX4 POWER CONNECTOR



## 螺絲孔

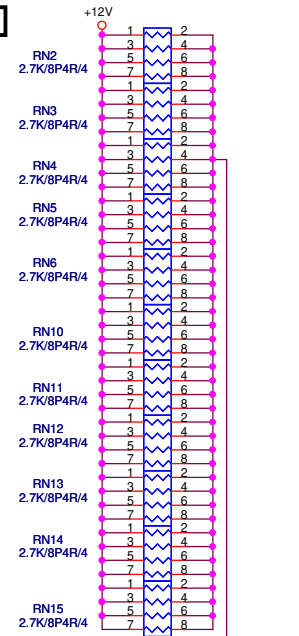


## 固定孔/光學點

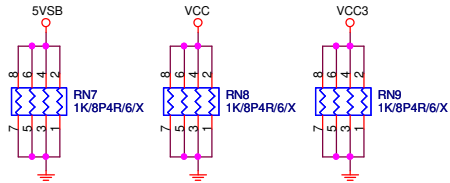


## +12V DUMMY LOAD

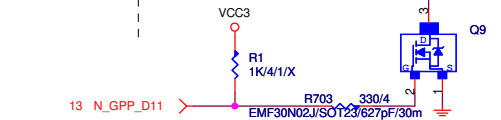
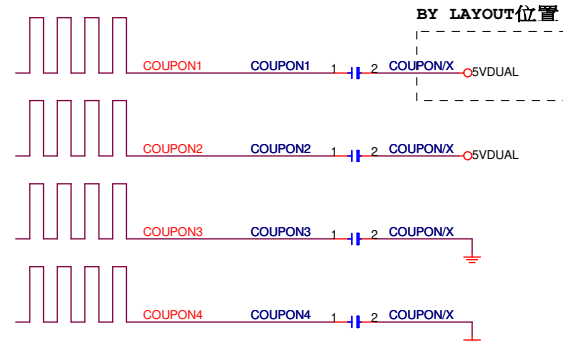
To fix 12V light load  
abnormal issue



## DUMMY LOAD



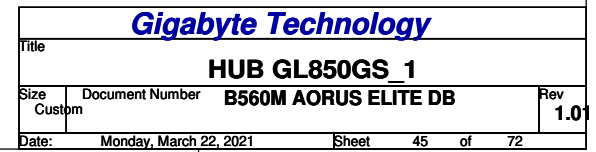
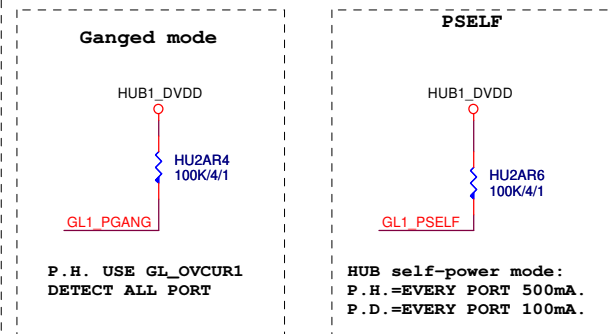
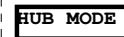
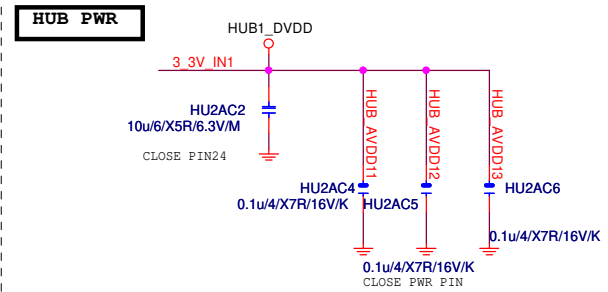
## COUPON



Gigabyte Technology

Title			ATX POWER CONNECTOR		
Size	Custom	Document Number	B560M AORUS ELITE DB		
Date:	Monday, March 22, 2021	Sheet	44	of	72
			1	Rev	



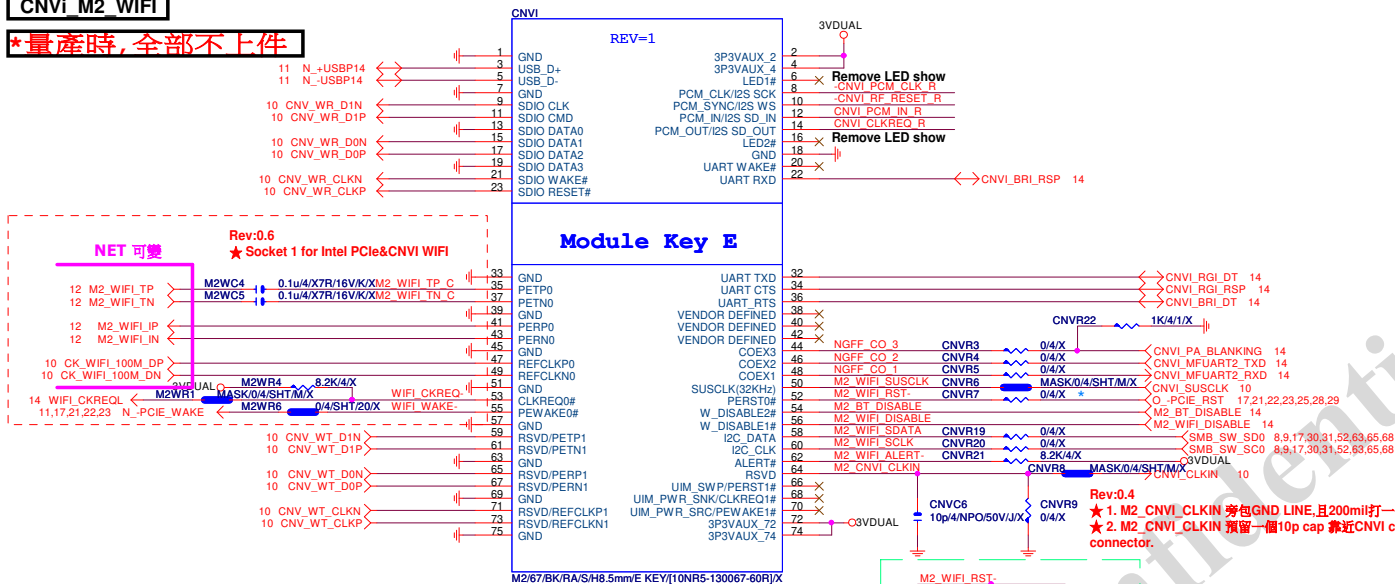


Rev: 0.94

CNVI M2 WIFI

\*量產時,全部不上件

支援PCIE介面WIFI及USB介面BT



直立  
Footprint Notice.

★Update 2015-07-22

★Footprint for 直立式 SMD:  
WIFI-EKEY

★SMD P/N: 直立式  
10NH5-130067-11R.

高的CNVI

橫躺

Footprint Notice.

★Update 2015-07-22

★Footprint for 橫躺式高:  
NGFF-E-75P-3

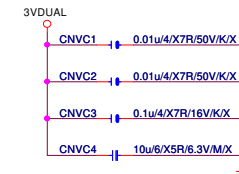
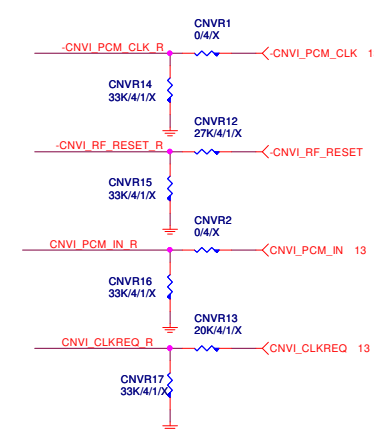
★Footprint for 橫躺式矮:  
CNVI

★橫躺式高SMD  
P/N:10NR5-130067-61R

★橫躺式矮SMD  
P/N:10NR5-130067-22R

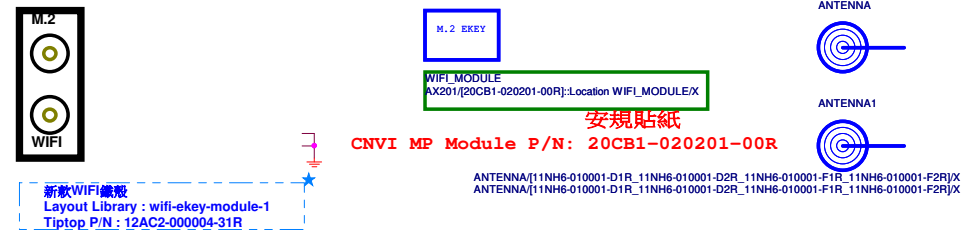
Rev:0.6

★1. 將PCH吐出的3.3V 經分壓為1.8V 才連到CNVI



量產不附

一套WIFI MODULE包含外框+WIFI CARD+天線



Footprint WIFI-EKEY+ WIFI-EKEY-MODULE should be a package.



CR[12KS2-110202-11R]X



CR[10KS2-S40126-01R]X  
10KS2-S40126-01R



M2\_WIFI\_CAP[11KWP-000001-31R]X

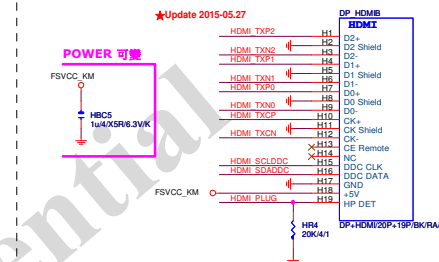
白色透明塑膠蓋

螺絲

GIGABYTE™

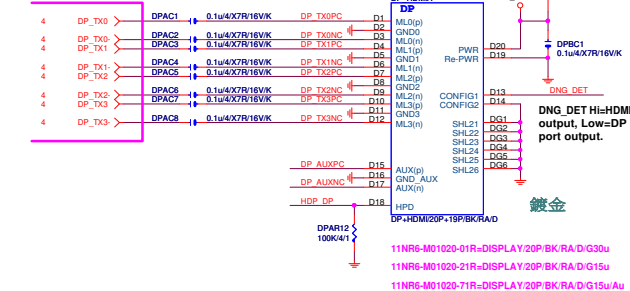
Title		CNVI M2_WIFI	
Size	Document Number	Rev	
Custom	B560M AORUS ELITE	09.01	
Date:	Monday, March 22, 2021	Sheet	46 of 72

## Rev: 0.8



ASM1442:紅色框要上,HR12:3.16K

原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram  
改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)



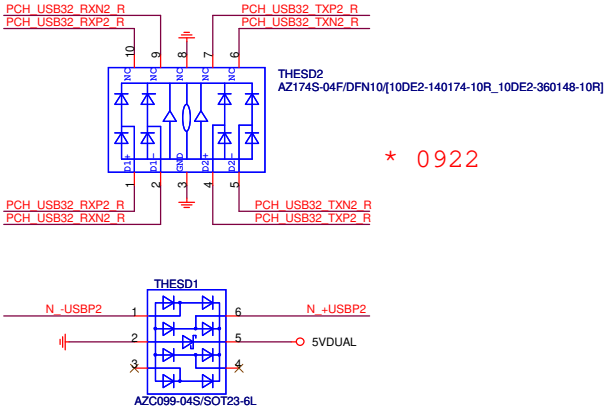
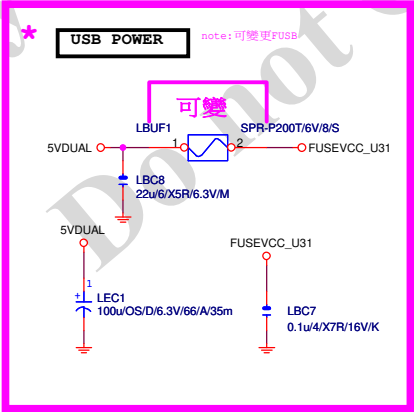
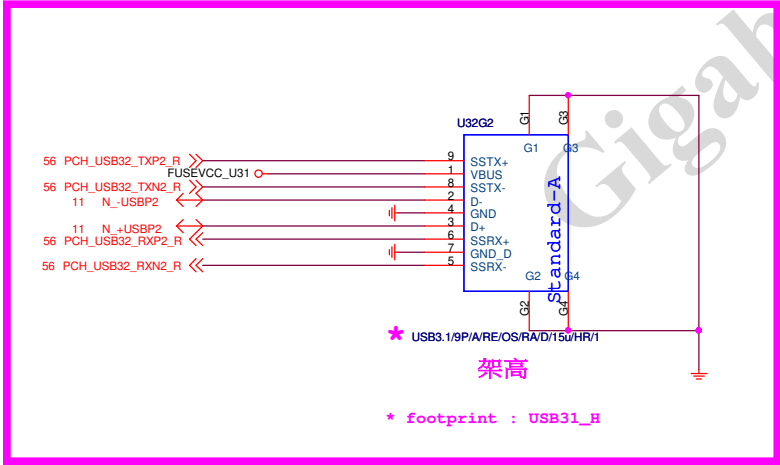
-M01020-01R=DISPLAY/20P/BK/RA/D/G30u  
-M01020-21R=DISPLAY/20P/BK/RA/D/G15u  
-M01020-71R=DISPLAY/20P/BK/RA/D/G15u/Au



## Gigabyte Technology

Title		<b>HDMI_NO LS/U32</b>		Rev	
Size	Document Number	<b>B560M AORUS ELITE D5</b>		Rev	
Custom					
Date:	Monday, March 22, 2021	Sheet	47	of	79

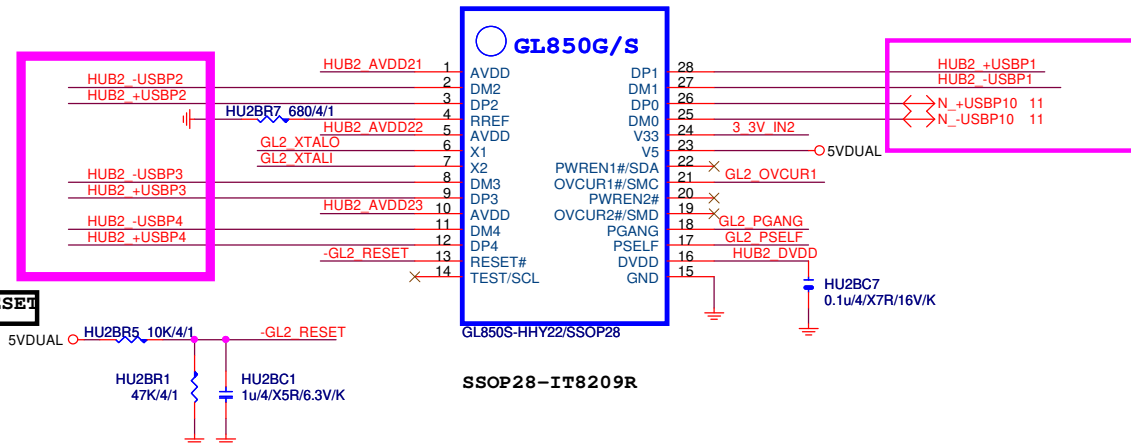
USB31 TYPE A Connector / power source / FUSE which choose for project demand



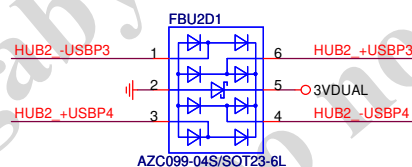
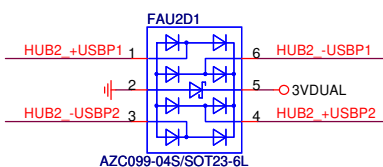
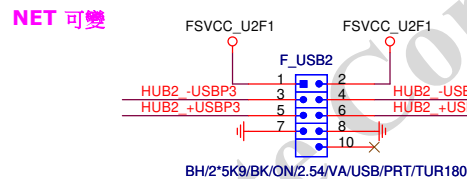
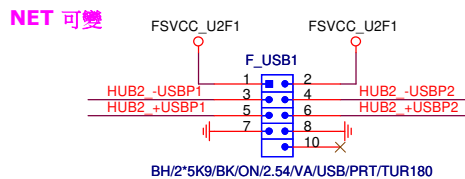
P/N SWAP

Gigabyte Confidential  
Do not Copy

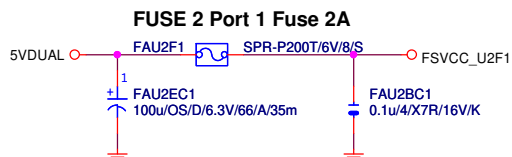
## HUB2



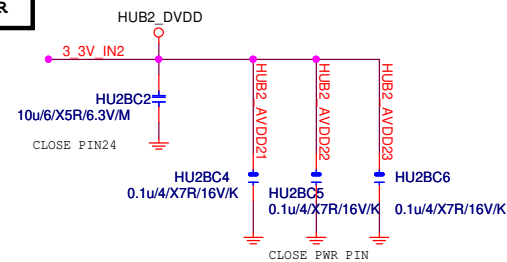
**FRONT USB2**



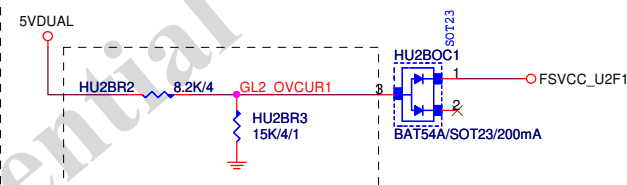
**Close to connector**  
**FUSE 2 Port 1 Fuse 2A**



## HUB PWR

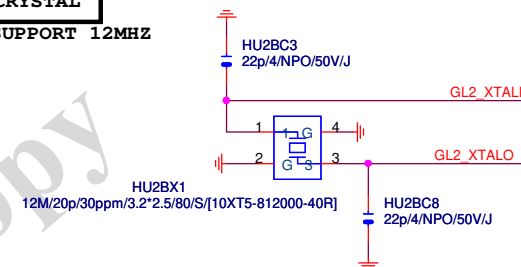


HUB OVER CURRENT SENSE
------------------------

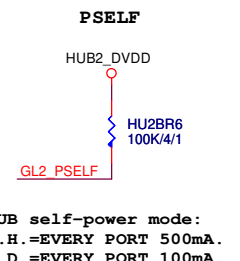
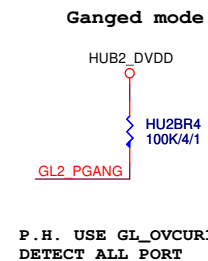


## HUB CRYSTAL

ONLY SUPPORT 12MHZ



**HUB MODE**





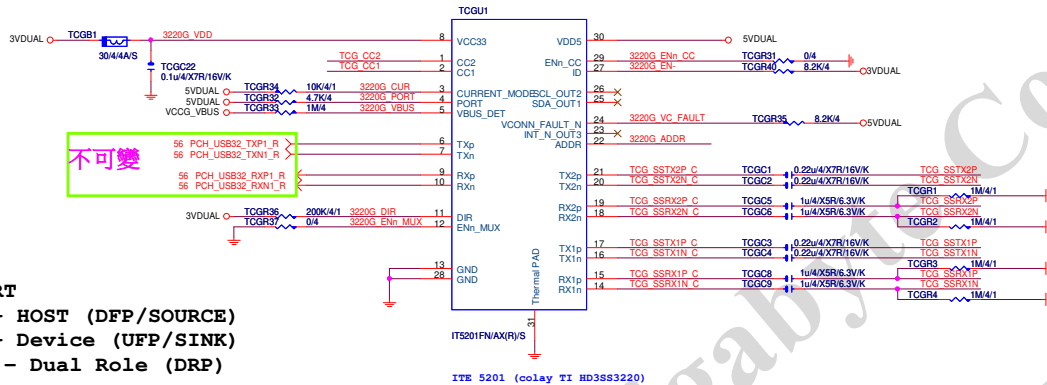
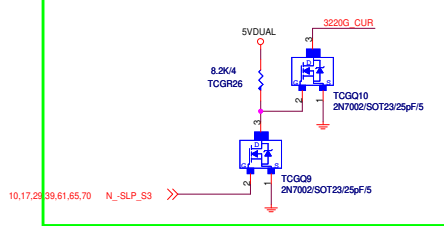
# USB3.2 GEN2 PI3EQX1004E Rev0.1

## IT5201 MUX Type-C

MUX 請擇一

GROUP G

For VBUS current limit at 900mA on S3

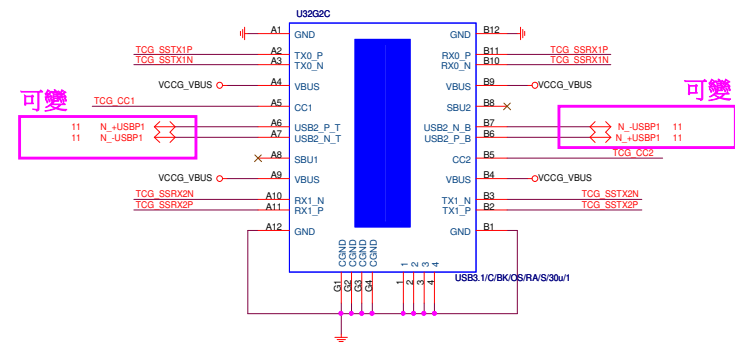


### PORT

- H - HOST (DFP/SOURCE)
- L - Device (UFP/SINK)
- NC - Dual Role (DRP)

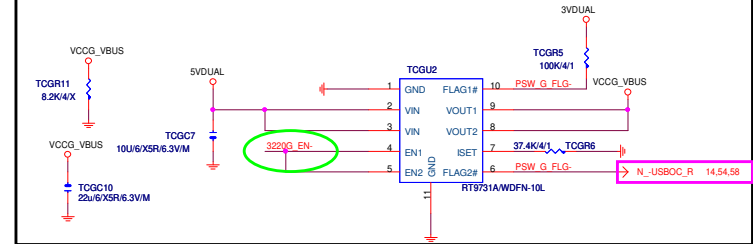
### CURRENT MODE

- L - Default (900mA) / Pull down to GND or NC
- M - Medium (1.5A) / Pull up to VDD 500K
- H - High (3.0A) / Pull up to VDD 10K

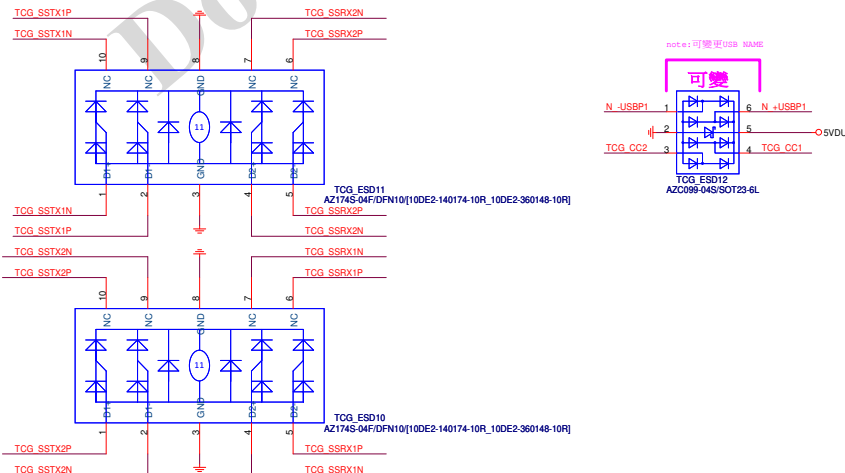


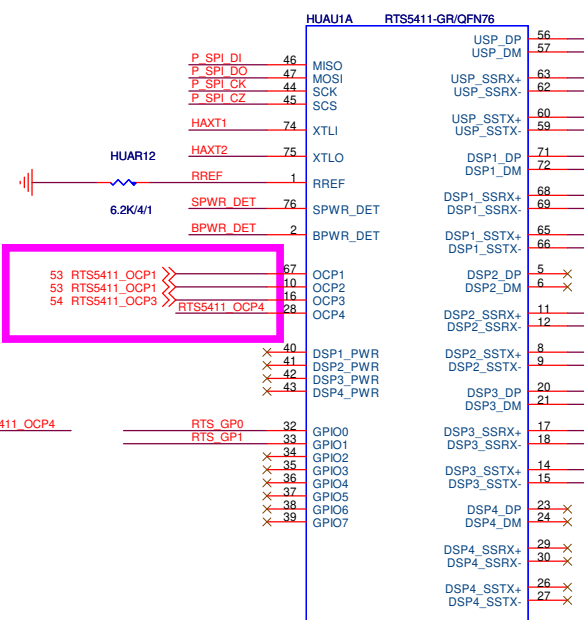
USB2.0 can be used the same source

## TypeC default 5V/3A

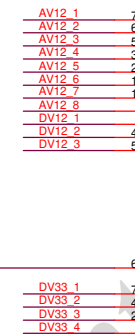
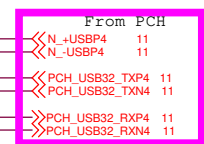


## Color markers can be changed by model



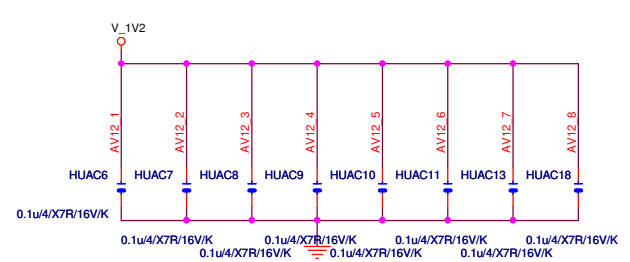
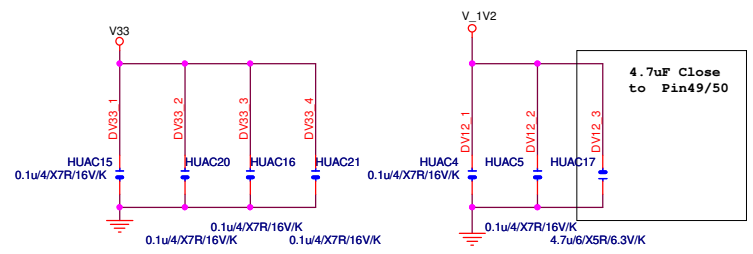
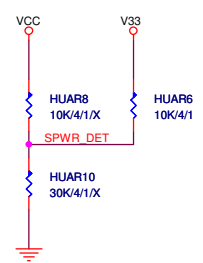
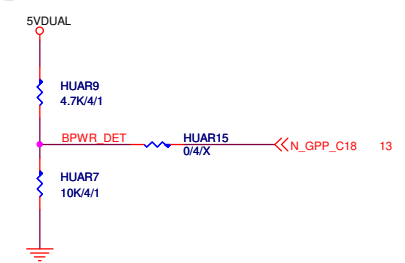
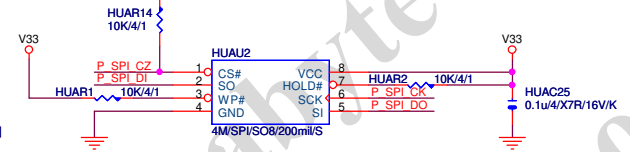
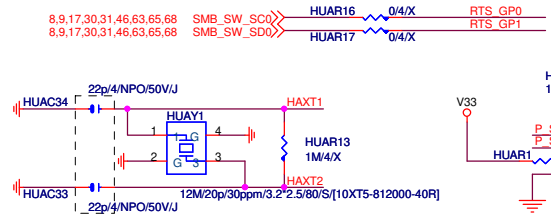
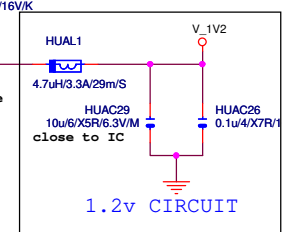
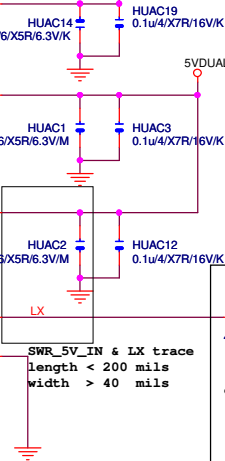
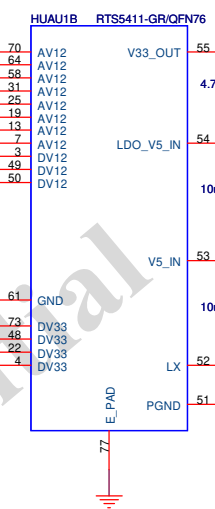


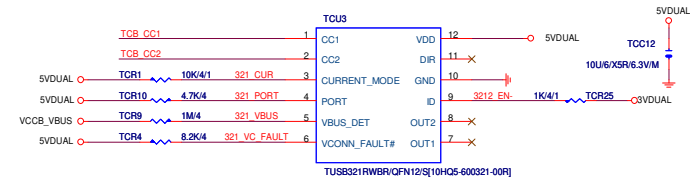
FW要修改



F\_U32C

F\_U32





CURRENT MODE

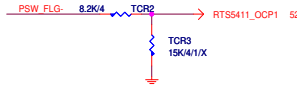
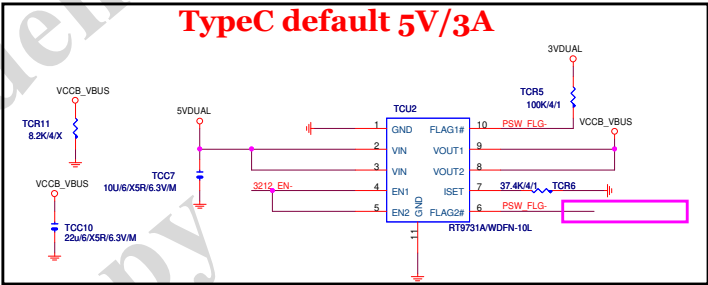
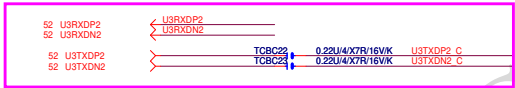
L - Default current / Pull down to GND or NC  
M - Medium (1.5A) current / Pull up to VDD 500K  
H - High (3.0A) current / Pull up to VDD 10K

PORT

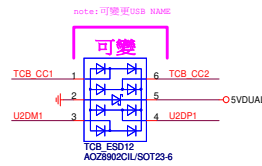
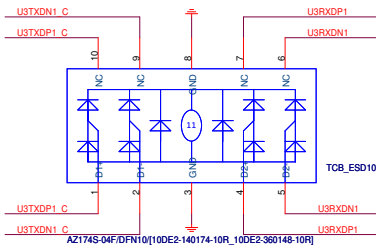
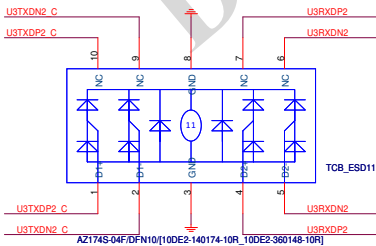
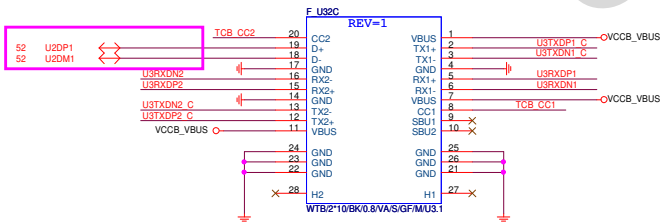
H - HOST  
L - Device  
NC - Dual Role

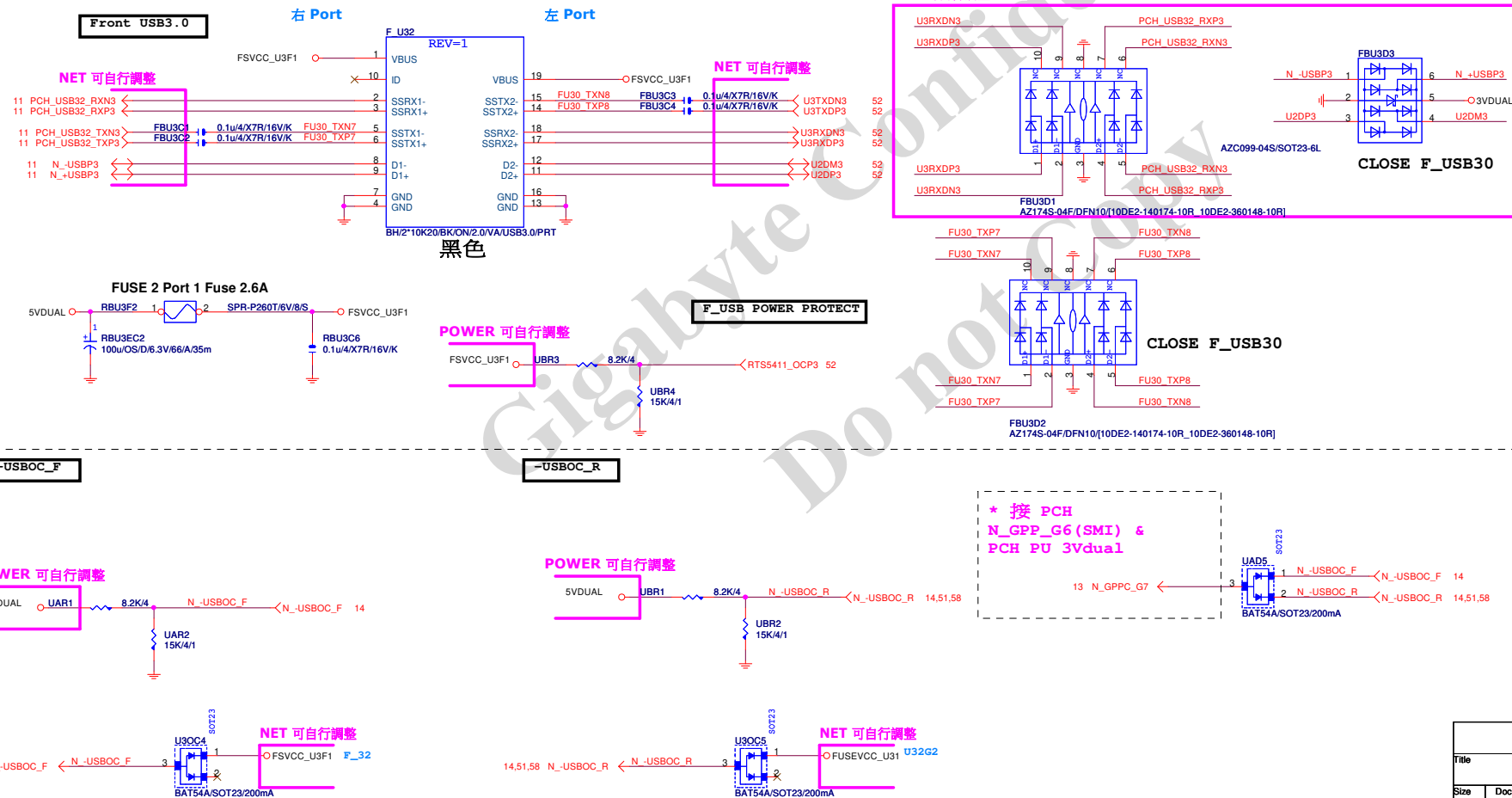
USB 3.x SuperSpeed

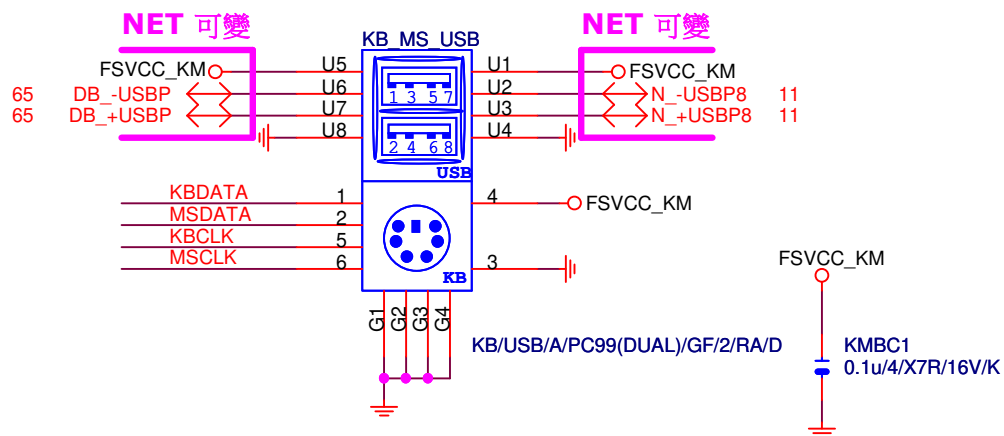
可變



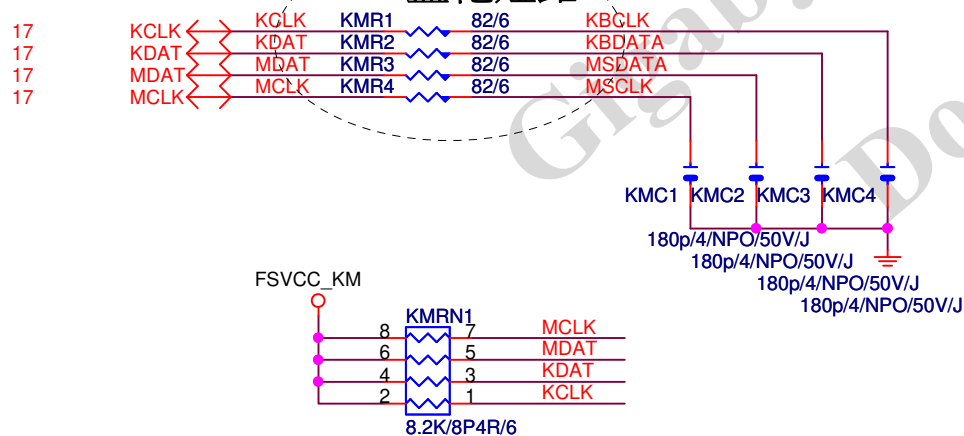
Color markers can be changed by model



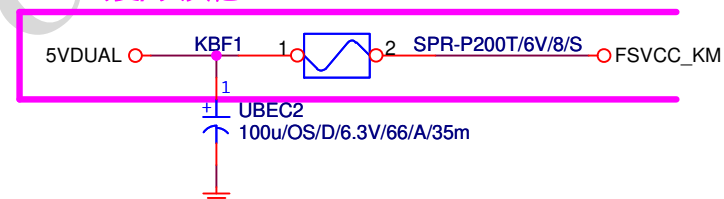


**Rev: 0.7**

## FOR鹽化短路



## NET 可變, 與其他USB SHARE



## Gigabyte Technology

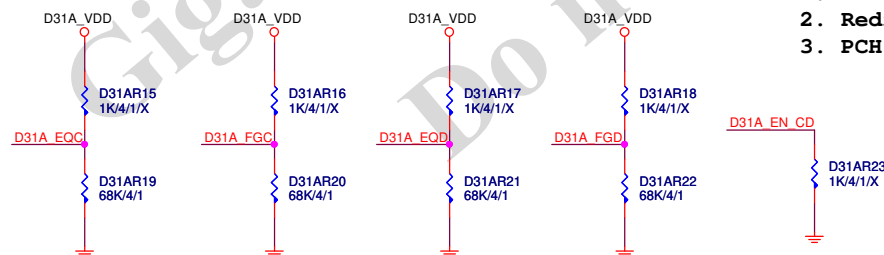
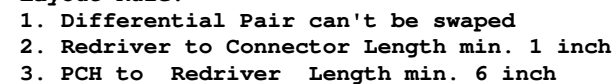
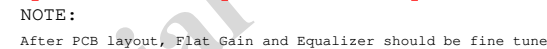
**KB MS USB30**

Rev

# B560M AORUS ELITE DB<sup>1.01</sup>

Sheet 55 of 72

**GROUP A**



**GIGABYTE™**

Title			
<b>Redriver_A_TYPE-A</b>			
Size B	Document Number		Rev
	<b>B560M AORUS ELITE DB</b>		<b>1.01</b>
Date:	Monday, March 22, 2021	Sheet	56 of 72

## R0.3

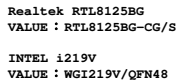
For RTL8125B  
\* Leave LKR1 to LKR2 NC

For INTEL i219V  
\* LKR1 NC  
\* LKR2 0ohm



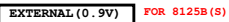
**25M SMD Type**

MDI ESD預留



VALUE : WGI219V/QFN48

## NC

LKL1  
4.7uH/0.8A

External SWR  
for RTL8125BS SWR disabled

## LAN POWER

(CLOSE LKU1 PIN: 29, 46)



(2.2u CLOSE LKU1 PIN:11,16,22)

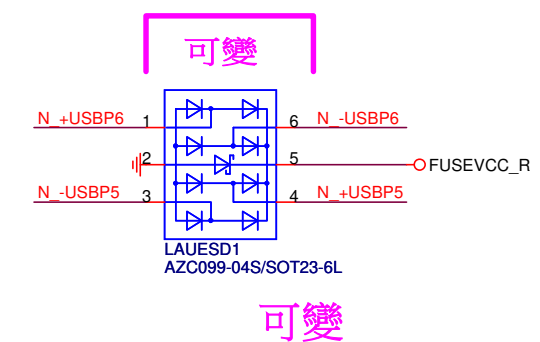
**Realtek RTL8125B(S)**

Size	Document Number	Rev
Custom		
<div style="text-align: center;"> <b>B560M AORUS ELITE DB</b> </div>		
Date:	Monday, March 22, 2021	Sheet 57 of 72



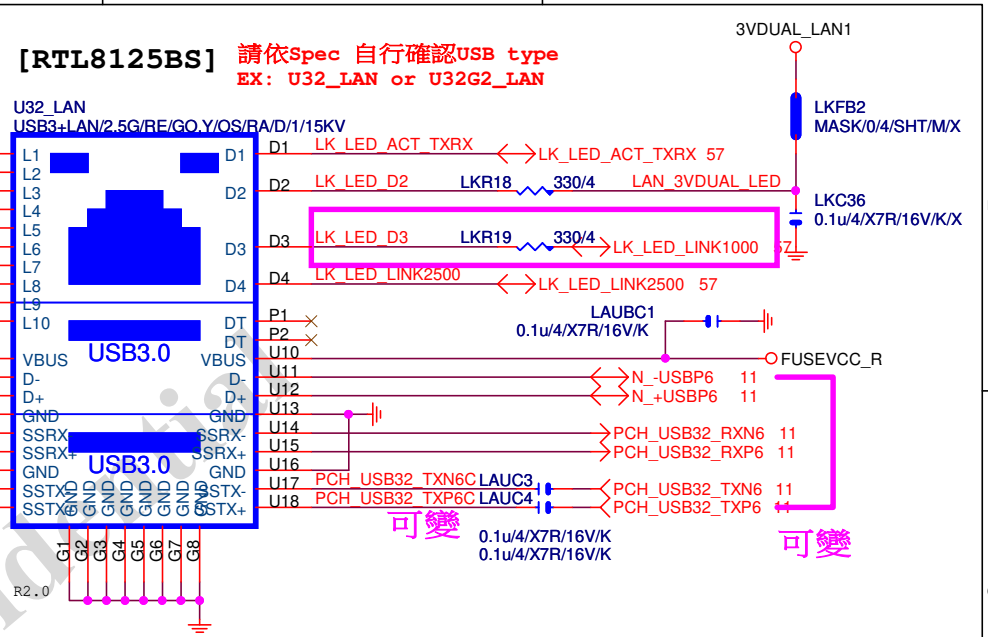
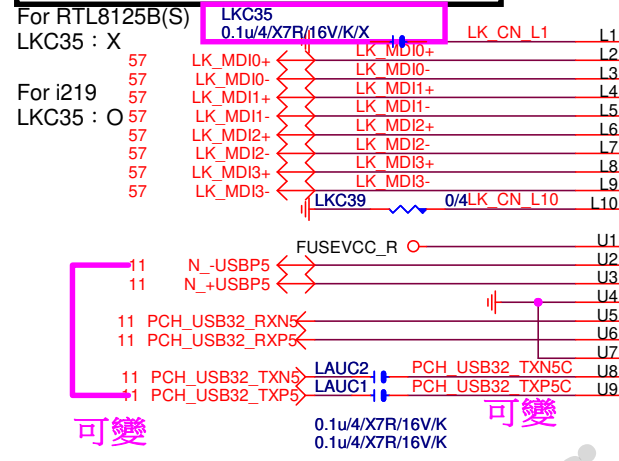
USB\_LAN CONNECTOR R0.3

RMA ESD PROTECT note:可變更USB NAME

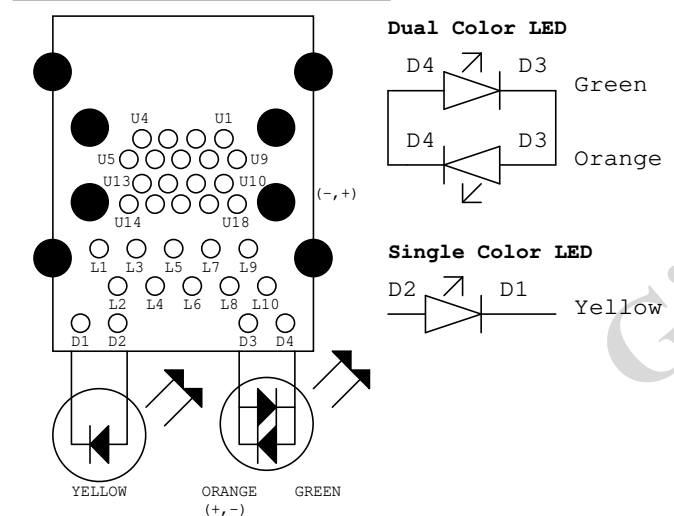


USB\_LAN CONNECTOR

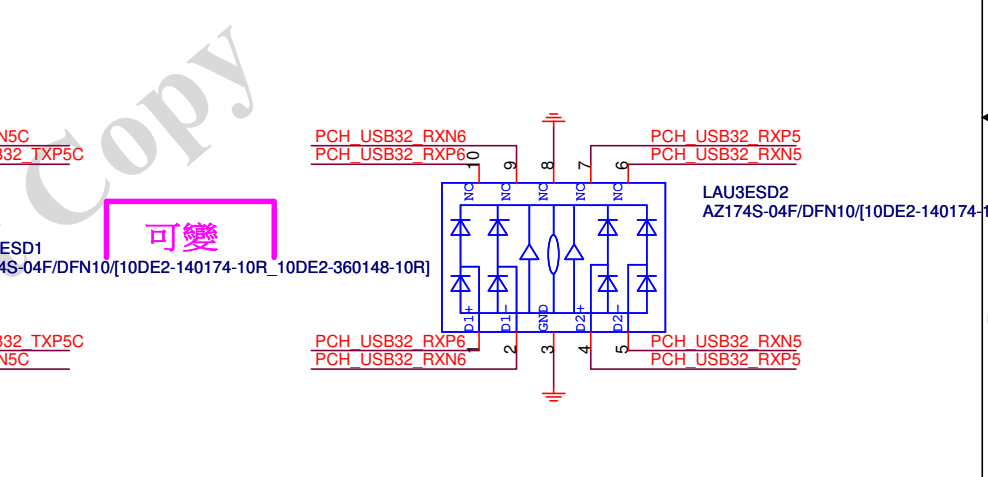
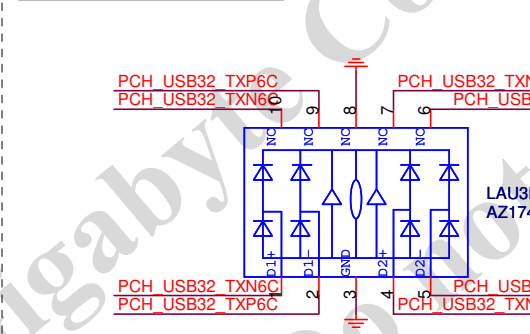
LA\_MDI-->100歐姆: [20/4/8/4/20]



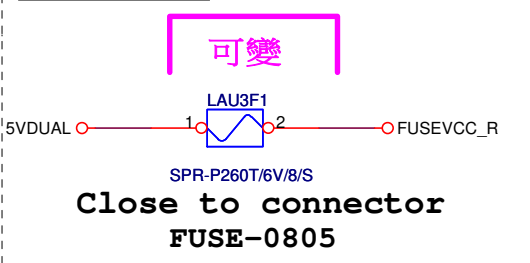
USB30\_LAN LAYOUT示意圖



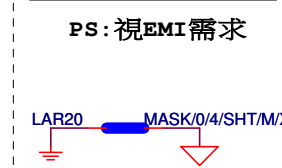
RMA ESD PROTECT note:可變更USB NAME



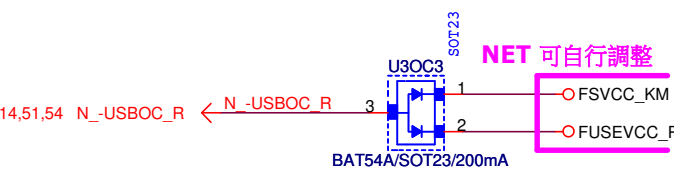
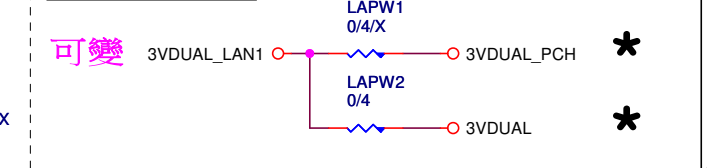
USB POWER note:可變更FUSE



EMI SHORT PAD PS:視EMI需求



LAN POWER note:lan power連接及電流



Close to connector FUSE-0805

CR88 100K 1%897 : X@887/892/662  
CR93 100K 1%897 : X@887/892/662  
CR89 200K 1%897 : X@887/892/662  
CR34 X@897 : 20K 1%887/892/662

CR84 100K 1%897 : X@887/892/662  
CR94 X@897 : 5.1K 1%887/892/662  
CR95 100K 1%897 : 10K 1%887/892/662  
CR85 200K 1%897 : X@887/892/662  
CR44 X@897 : 47 1%887/892/662

CR86 100K 1%897 : X@887/892/662  
CR87 ohm@897 : X@887/892/662

CR90 0ohm@897 : X@887/892/662  
CR2 0/4/X  
CR63 0/4/SHT/10/M/X  
CR91 ALC897-VA1 only  
14 C\_ACZ\_SDOOUT  
14 C\_ACZ\_BITCLK  
14 C\_ACZ\_SDOIN0  
14 C\_ACZ\_SYNC  
14 C\_ACZ\_RST

CR84 100K/4/1  
CR94 5.1K/4/1/X  
CR95 100K/4/1  
CR85 200K/4/1  
CR44 47/4/1/X  
CR86 100K/4/1  
CR87 0/4

CR16 8.2K/4  
CR19 8.2K/4  
CR18 200K/4/1  
CR92 100K/4/1

CR80 100K/4/1  
CR20 200K 1%897 : 5.1K 1%887/892/662  
CR23 X@897 : 10K 1%887/892/662  
CR18 X@897 : 20K 1%887/892/662  
CR92 100K 1%897 : 39.2K 1%887/892/662

CR81 0/4  
CR82 0/4  
CR83 0.1u4/X7R/16V/K  
CR84 100K/4/1  
CR85 200K/4/1  
CR86 100K/4/1  
CR87 0/4  
CR88 100K/4/1  
CR89 200K/4/1  
CR90 0/4  
CR91 ALC897-VA1 only  
14 C\_ACZ\_SDOOUT  
14 C\_ACZ\_BITCLK  
14 C\_ACZ\_SDOIN0  
14 C\_ACZ\_SYNC  
14 C\_ACZ\_RST

CR80 100K/4/1  
CR20 200K 1%897 : 5.1K 1%887/892/662  
CR23 X@897 : 10K 1%887/892/662  
CR18 X@897 : 20K 1%887/892/662  
CR92 100K 1%897 : 39.2K 1%887/892/662

CR81 0/4  
CR82 0/4  
CR83 0.1u4/X7R/16V/K  
CR84 100K/4/1  
CR85 200K/4/1  
CR86 100K/4/1  
CR87 0/4  
CR88 100K/4/1  
CR89 200K/4/1  
CR90 0/4  
CR91 ALC897-VA1 only  
14 C\_ACZ\_SDOOUT  
14 C\_ACZ\_BITCLK  
14 C\_ACZ\_SDOIN0  
14 C\_ACZ\_SYNC  
14 C\_ACZ\_RST

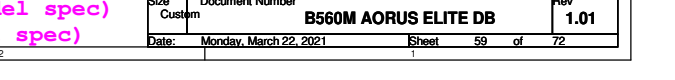
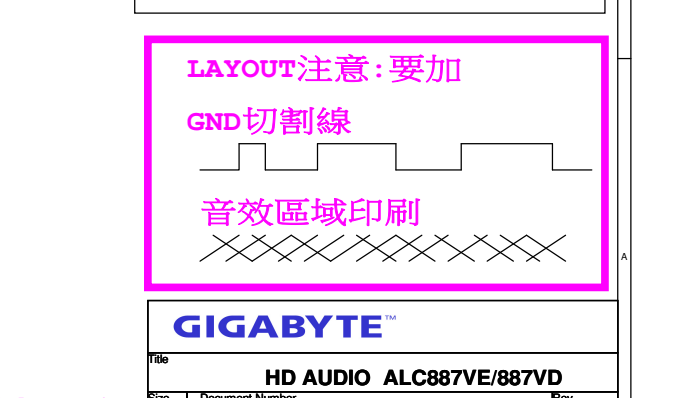
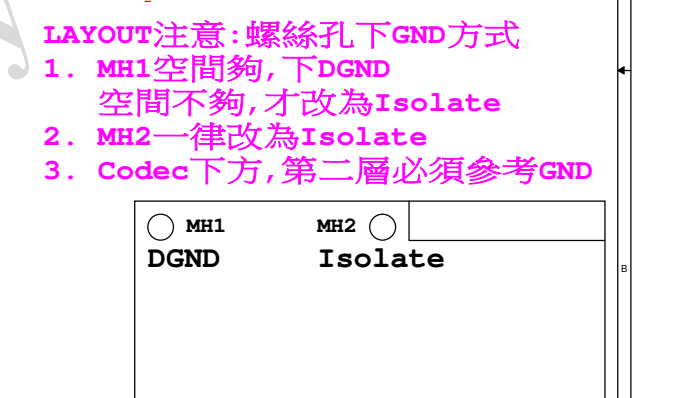
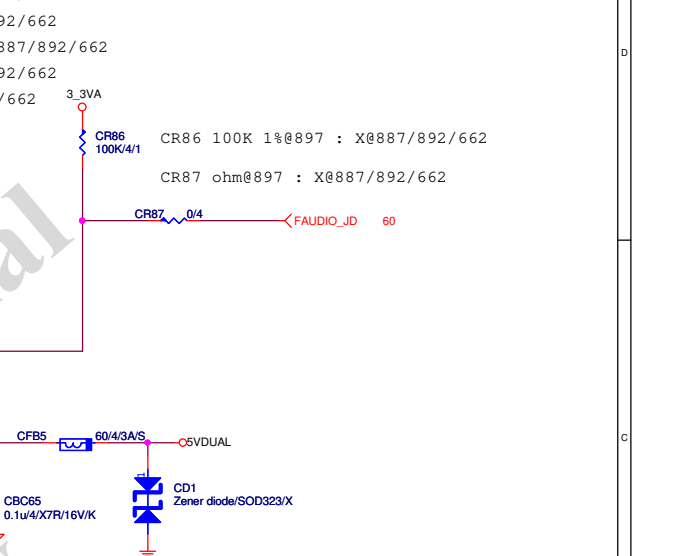
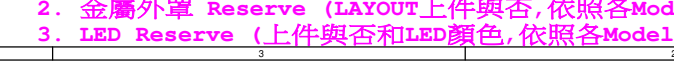
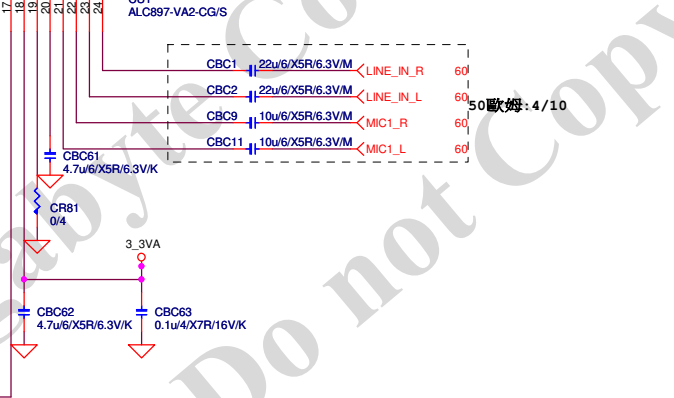
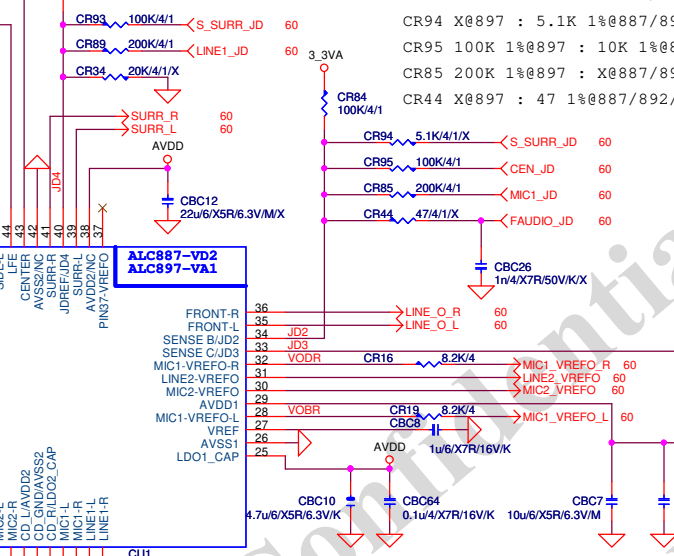
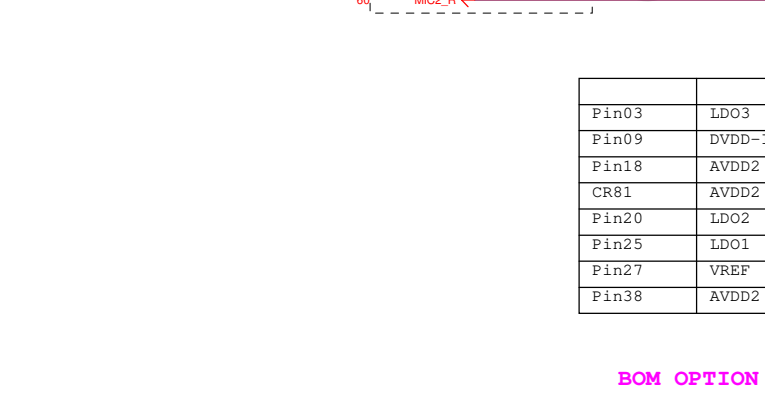
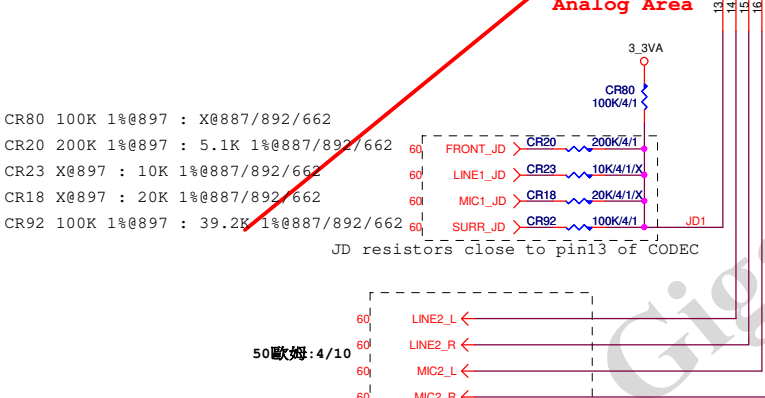
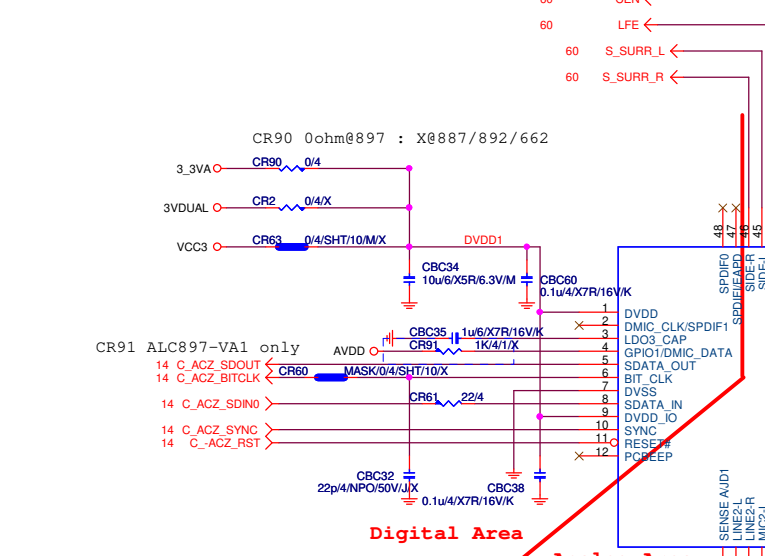
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CR20 200K 1%897 : 5.1K 1%887/892/662  
CR23 X@897 : 10K 1%887/892/662  
CR18 X@897 : 20K 1%887/892/662  
CR92 100K 1%897 : 39.2K 1%887/892/662

CR81 0/4  
CR82 0/4  
CR83 0.1u4/X7R/16V/K  
CR84 100K/4/1  
CR85 200K/4/1  
CR86 100K/4/1  
CR87 0/4  
CR88 100K/4/1  
CR89 200K/4/1  
CR90 0/4  
CR91 ALC897-VA1 only  
14 C\_ACZ\_SDOOUT  
14 C\_ACZ\_BITCLK  
14 C\_ACZ\_SDOIN0  
14 C\_ACZ\_SYNC  
14 C\_ACZ\_RST

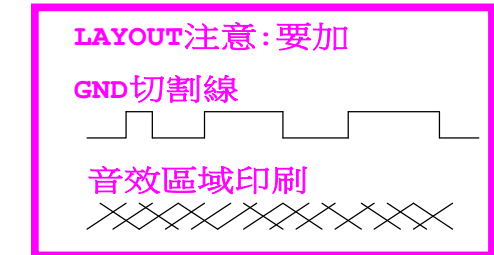
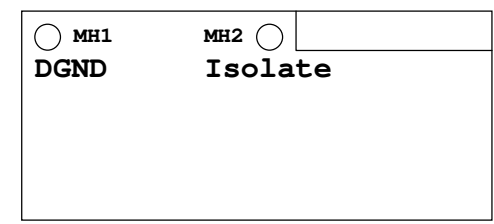
CR80 100K/4/1  
CR20 200K 1%897 : 5.1K 1%887/892/662  
CR23 X@897 : 10K 1%887/892/662  
CR18 X@897 : 20K 1%887/892/662  
CR92 100K 1%897 : 39.2K 1%887/892/662

CR81 0/4  
CR82 0/4  
CR83 0.1u4/X7R/16V/K  
CR84 100K/4/1  
CR85 200K/4/1  
CR86 100K/4/1  
CR87 0/4  
CR88 100K/4/1  
CR89 200K/4/1  
CR90 0/4  
CR91 ALC897-VA1 only  
14 C\_ACZ\_SDOOUT  
14 C\_ACZ\_BITCLK  
14 C\_ACZ\_SDOIN0  
14 C\_ACZ\_SYNC  
14 C\_ACZ\_RST

CR80 100K/4/1  
CR20 200K 1%897 : 5.1K 1%887/892/662  
CR23 X@897 : 10K 1%887/892/662  
CR18 X@897 : 20K 1%887/892/662  
CR92 100K 1%897 : 39.2K 1%887/892/662



LAYOUT注意:螺絲孔下GND方式  
1. MH1空間夠,下DGND  
空間不夠,才改為Isolate  
2. MH2一律改為Isolate  
3. Codec下方,第二層必須參考GND



		897	887/892/662
Pin03	LDO3	1uF	10uF
Pin09	DVDD-IO	0.1uF	1uF
Pin18	AVDD2	4.7uF/0.1uF	NC
CR81	AVDD2	0ohm	NC
Pin20	LDO2	4.7uF	NC
Pin25	LDO1	4.7uF	22uF
Pin27	VREF	1uF	10uF
Pin38	AVDD2	NC	22uF

BOM OPTION : 1. Chemicon音效電容  
2. 金屬外罩 Reserve (LAYOUT上件與否,依照各Model spec)  
3. LED Reserve (上件與否和LED顏色,依照各Model spec)

GIGABYTE™

File

HD AUDIO ALC887VE/887VD

Size

Document Number

B560M AORUS ELITE DB

Rev

1.01

Date

Monday, March 22, 2021

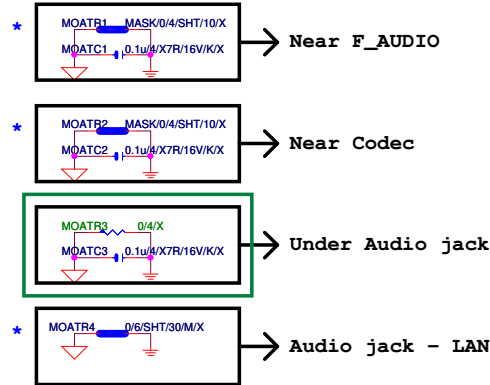
Sheet

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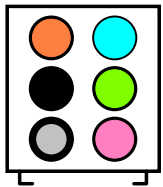
Rev 6.0



\*量産前,MOATR1/MOATR2/MOATR4 ....0ohm改short pad

For HDMI SPDIF (依SPEC保留或移除)

#### AZALIA JACK



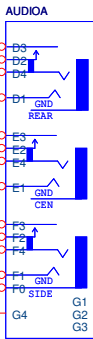
#### AZALIA JACK

BLUE  
LINE-IN

GREEN  
LINE-OUT

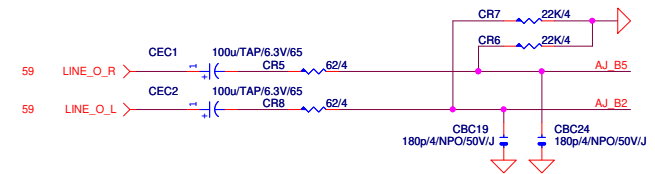
PINK  
MIC-IN

2X3RP/26P/OR,BK,GY,BU,GE,PK/RA

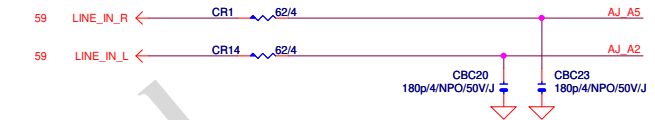


2X3RP/26P/OR,BK,GY,BU,GE,PK/RA

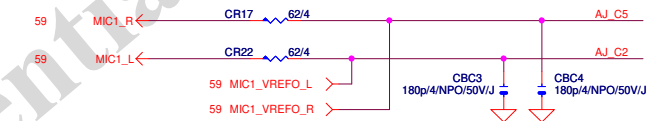
#### LINE-OUT



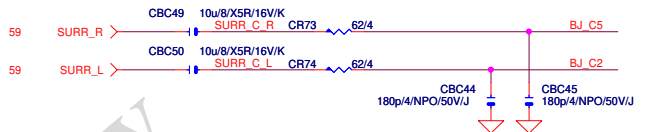
#### LINE-IN



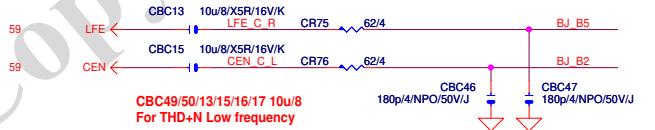
#### MIC-IN



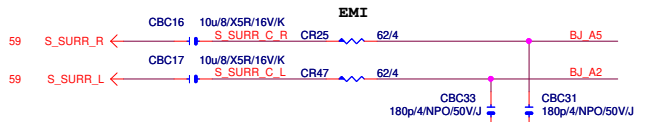
#### SURROUND



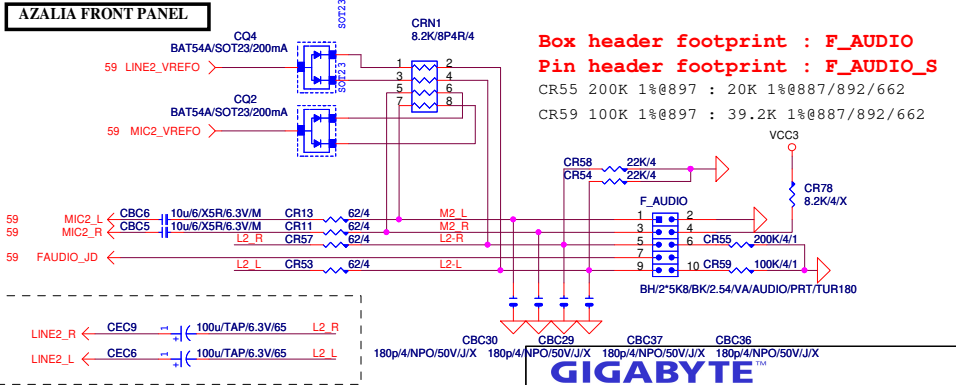
#### CEN/LFE



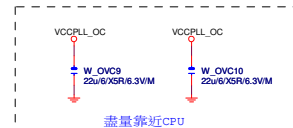
#### SURR BACK



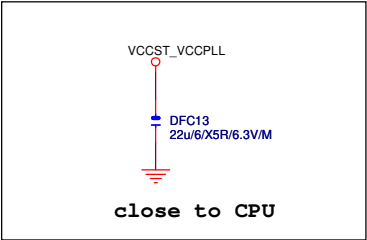
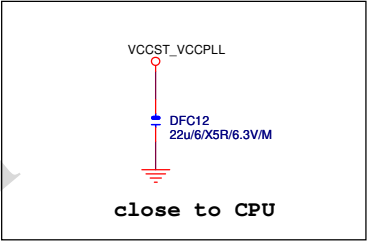
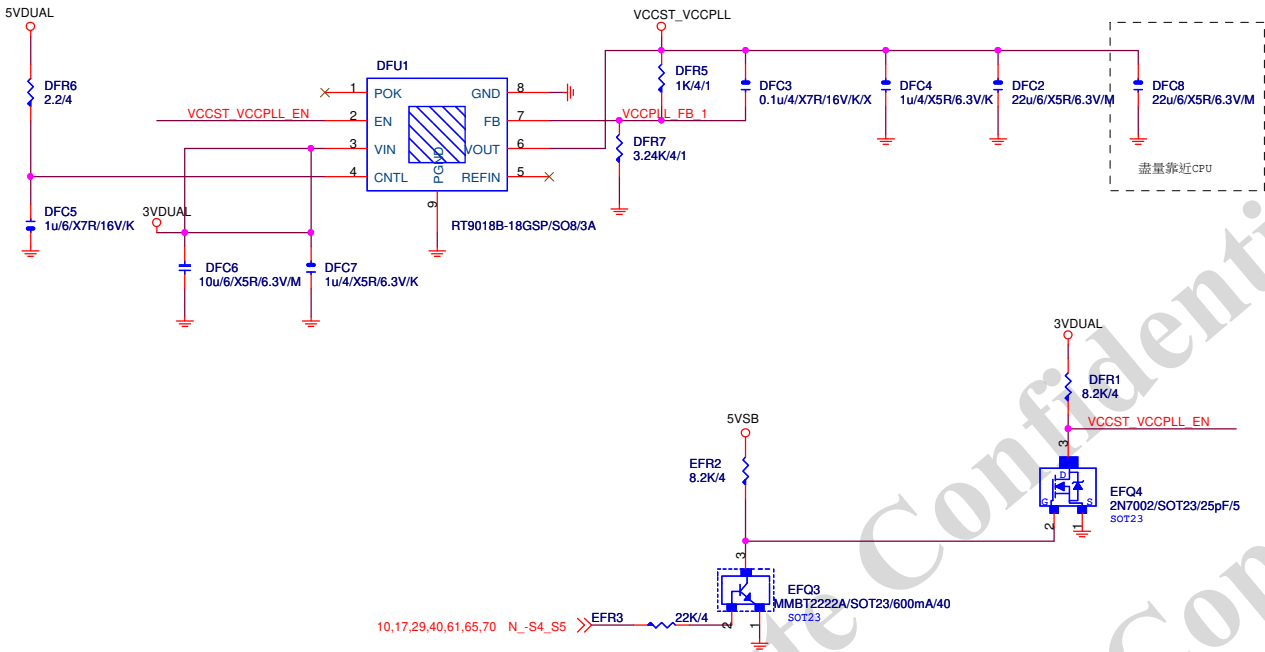
#### AZALIA FRONT PANEL

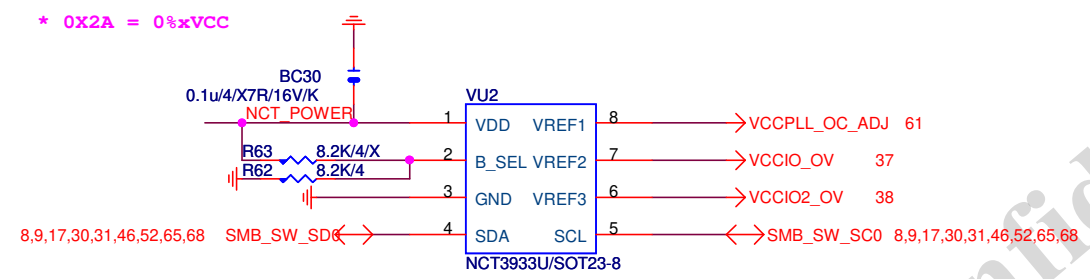
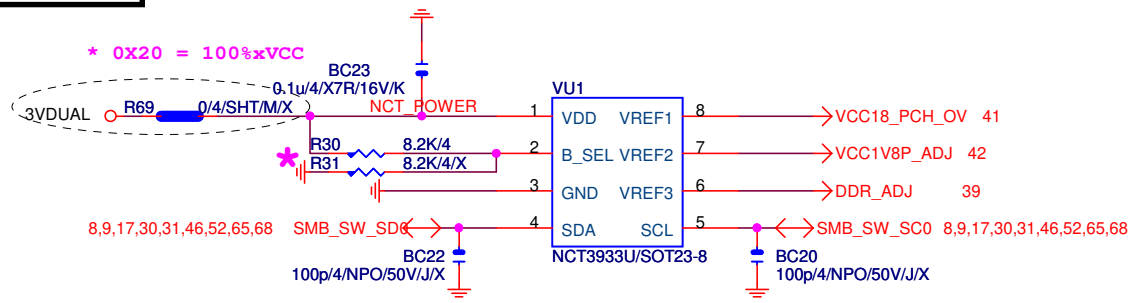


Title		
AUDIO JACK		
Size	Document Number	Rev
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VCCST\_VCCPLL 替換原先MOS開關線路





NCT3933	0X2A	0X20
VREF1	VCC18_PCH	VCCPLL_OC
VREF2	VCCIO	VCC1V8_PRIM
VREF3	VCCIO2	VDDQ

Gigabyte Technology

TitleCPU CORE VR-2

Size Custom

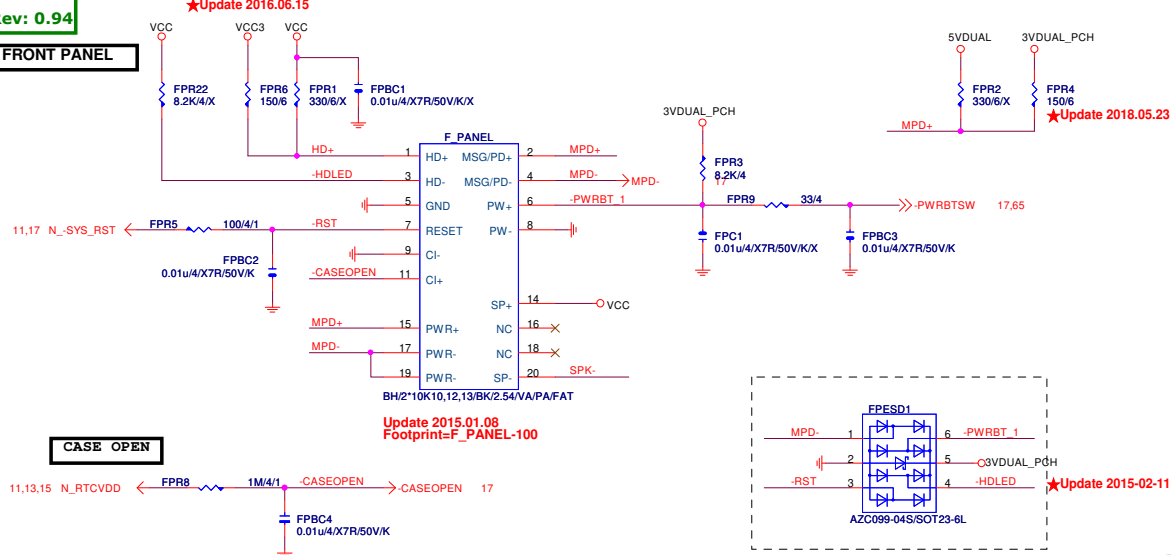
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Rev1.01

Date: Monday, March 22, 2021

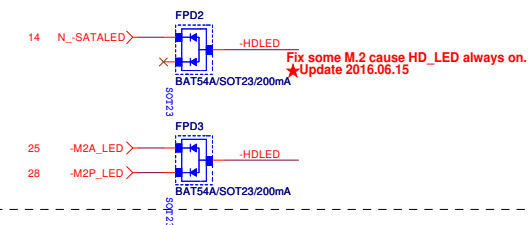
Sheet 63 of 72

## FRONT PANEL

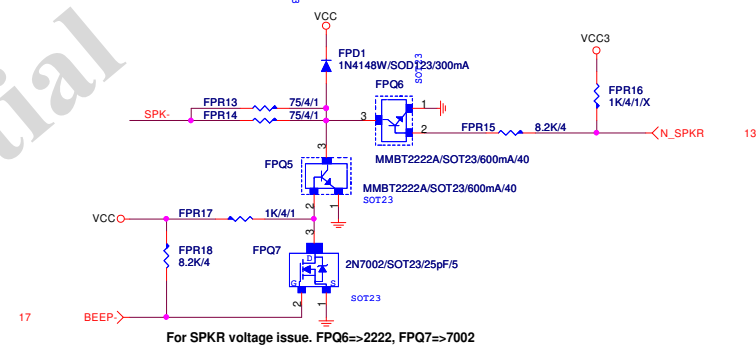


## FRONT PANEL SHORT

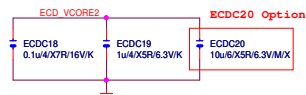
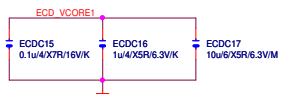
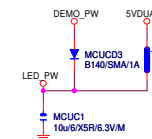
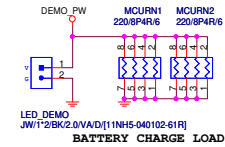
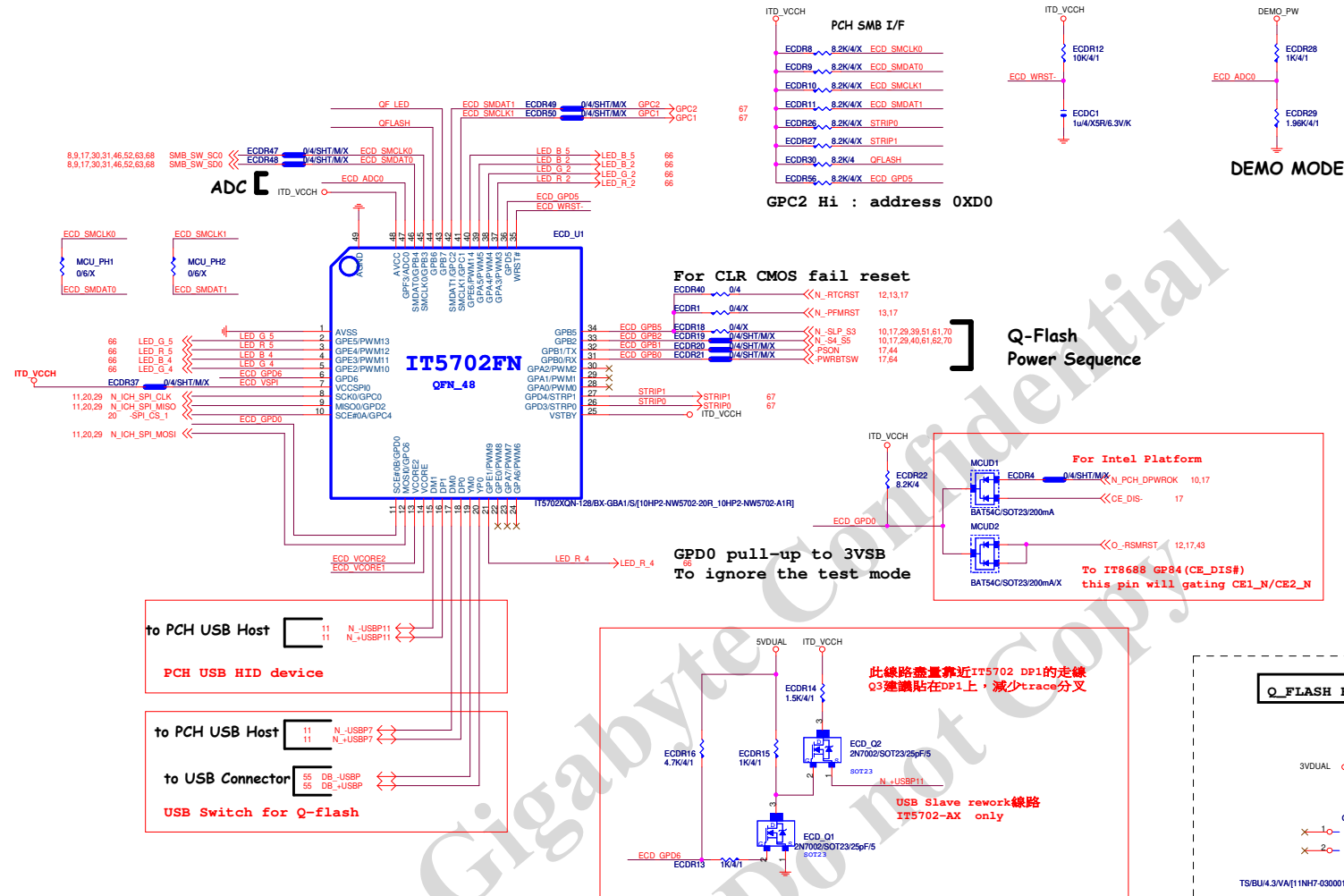
SATA/M.2 LED
--------------



## SPKR W/O EC



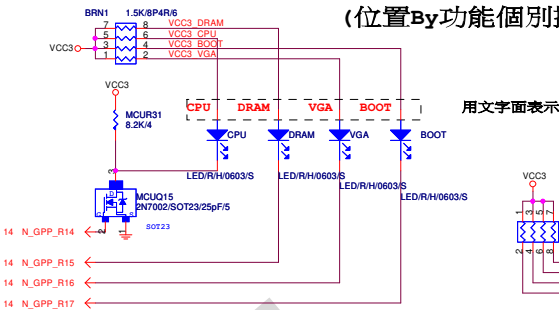
**ECD\_U1** 請放在PCH到BIOS路徑上.避免線過長





第一區 LED CONTROL

DEBUG PORT LED \*4  
(位置By功能個別擺放)



N_GPP_R14	CPU DEBUG
N_GPP_R15	DDR DEBUG
N_GPP_R16	VGA DEBUG
N_GPP_R17	BOOT DEVICE DEBUG

第三區 LED

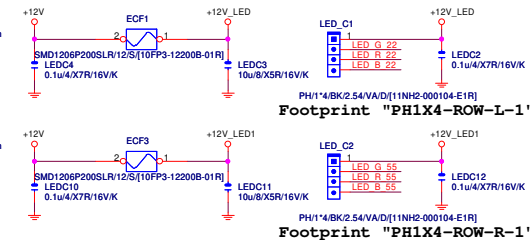
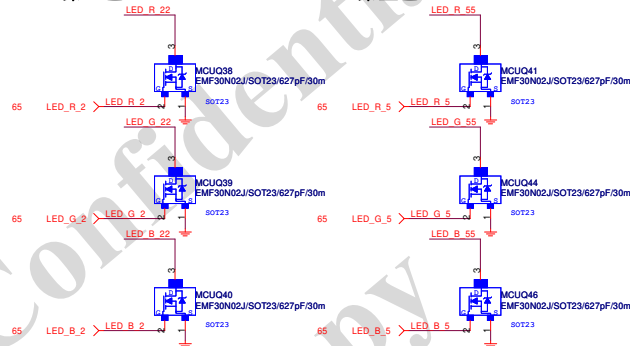
第三區 LED CONTROL

第五區 LED

第二區 LED CONTROL

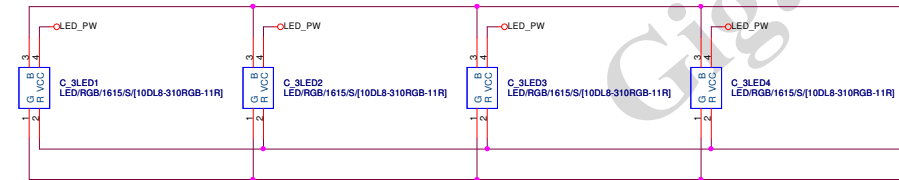
第五區 LED CONTROL

燈條 LED (LED\_C1放在PCB左邊板邊位置)  
燈條 LED (LED\_C2放在PCB右邊板邊位置)



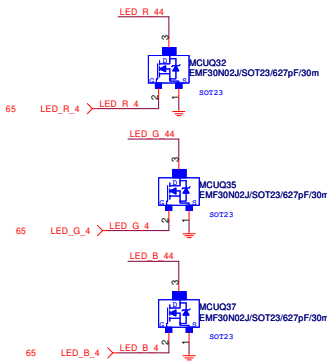
第四區 LED

FOR AUDIO 正發光 LED\*5 (位置在背板AUDIO切割線)



FOOTPRINT: LED-4P-RGB

第四區 LED CONTROL

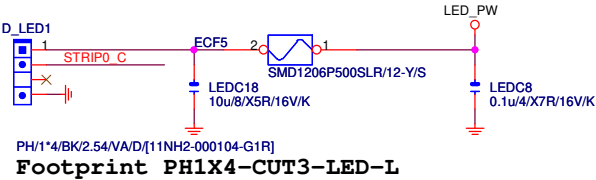


GIGABYTE™

File	PCH/AUDIO/DEBUG/C_LED1/2		
Size	Document Number	B560M AORUS ELITE DB	Rev 1.01
Custom			
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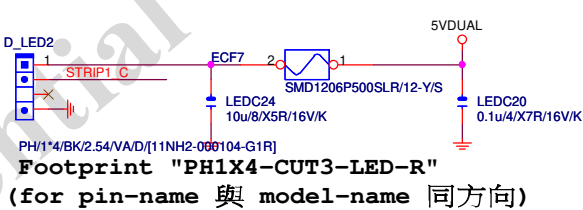
第六區 LED (靠近左上板邊位置)

Digital LED Strip1

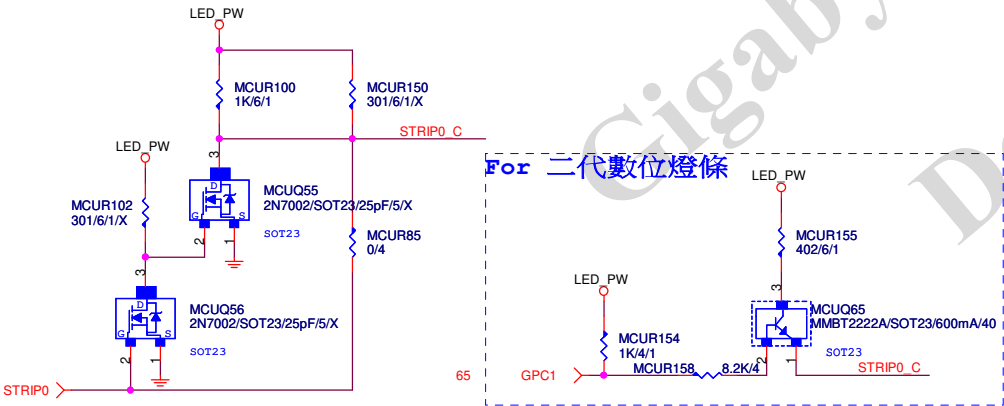


第七區 (靠近右下CPU板邊位置)

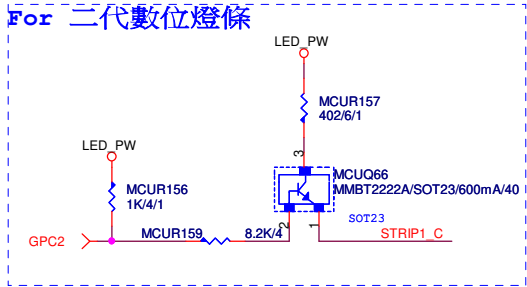
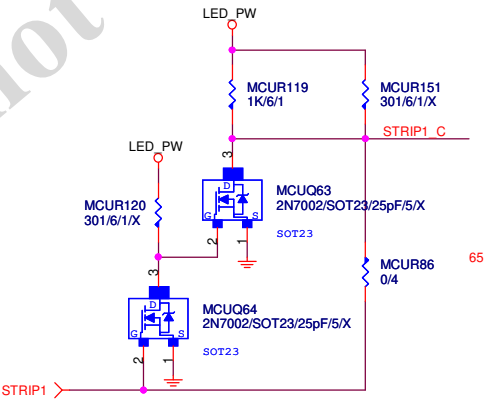
Digital LED Strip2



燈條 Level shift



燈條 Level shift



REV:0.1

SMB\_SW\_U2

5VDUAL

1 VIN

2 GND

3 EN

4 FB

5

R1

SMB\_SW\_R17

3K/4/1/X

SMB\_SW\_C1

22P/4/NPO/50V/J/X

SMB\_SW\_C2

10U/6/X5R/6.3V/M/X

SMB\_SW\_2V5

SMB\_SW\_C3

2.2u/4/X5R/6.3V/M/X

Vout = 0.8 \* (R1 + R2) / R2

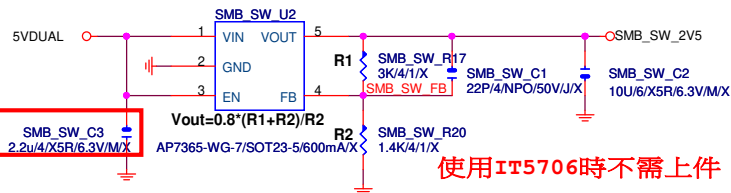
AP7365-WG-7/SOT23-5/600mA/X

R2

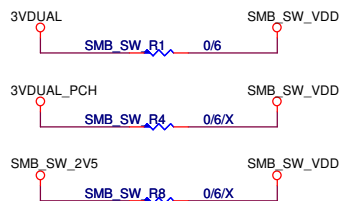
SMB\_SW\_R20

1.4K/4/1/X

使用IT5706時不需上件

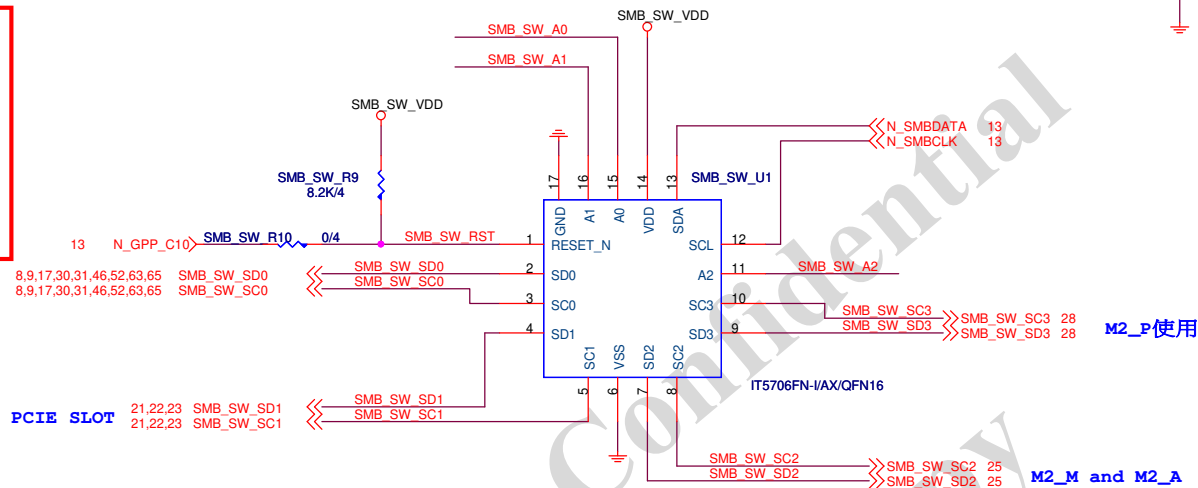


使用IT5706時不需上件



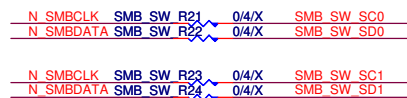
使用IT5706時改上SMB\_SW\_R1

other

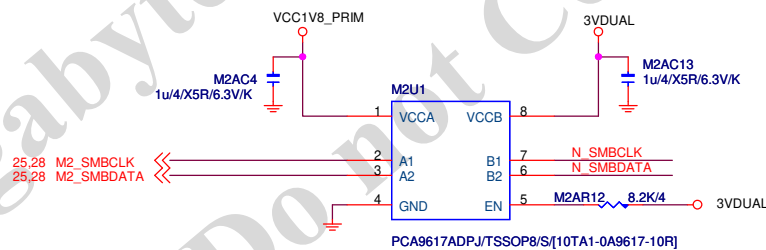


## M2\_P使用

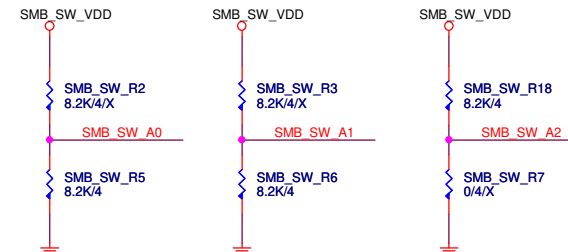
M2\_M and M2\_A



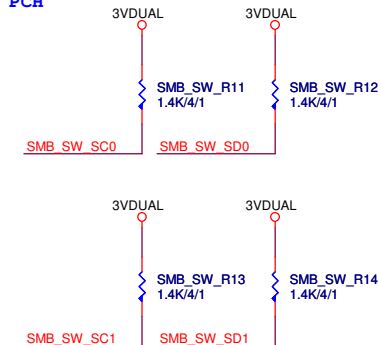
SMB\_SW\_U1 不上件時.電阻要上



**ADDRESS = 74**



PCH

**GIGABYTE™**

Title			
<b>SMBUS SWITCH</b>			
Size	Document Number		Rev
Custom	<b>B560M AORUS ELITE DB</b>		<b>1.01</b>
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## SMBUS SWITCH

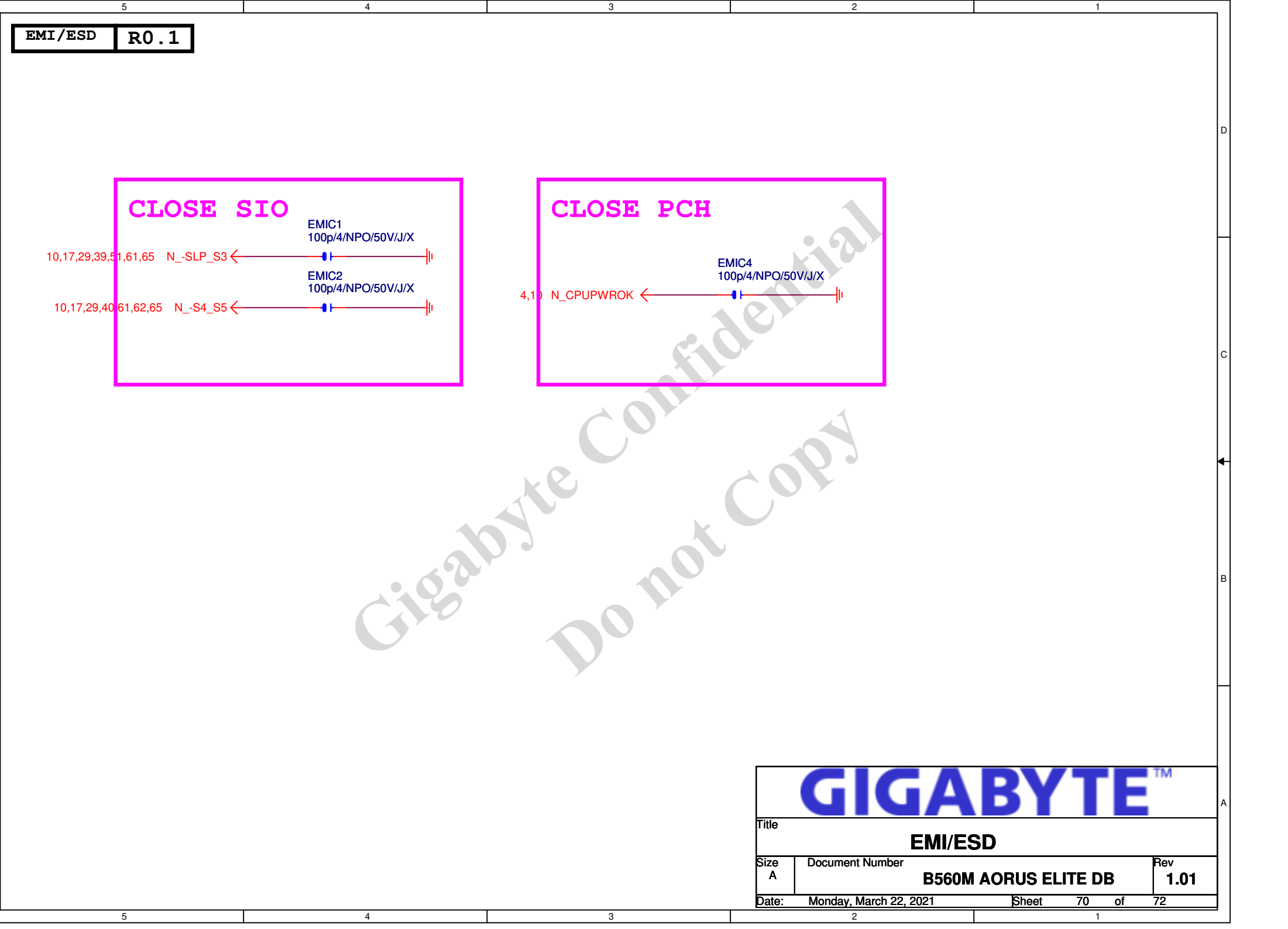
Size	Document Number	Rev
Custom	<b>B560M AORUS ELITE DB</b>	<b>1.01</b>

Date: Monday, March 22, 2021 Sheet 68 of 72

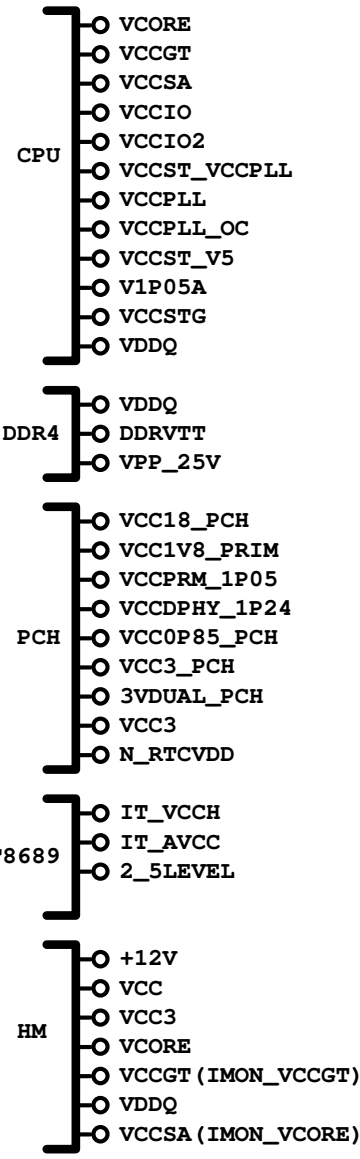
Sheet 68 of 72

Gigabyte Confidential  
Do not Copy

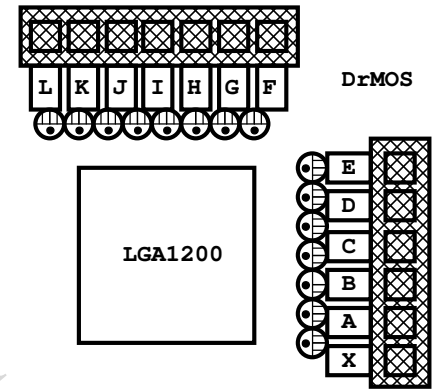
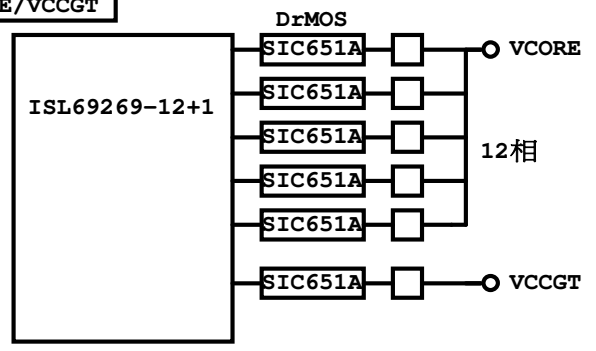
GIGABYTE™		
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AQC113C		
Size	Document Number	Rev
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Date:	Monday, March 22, 2021	Sheet 69 of 72



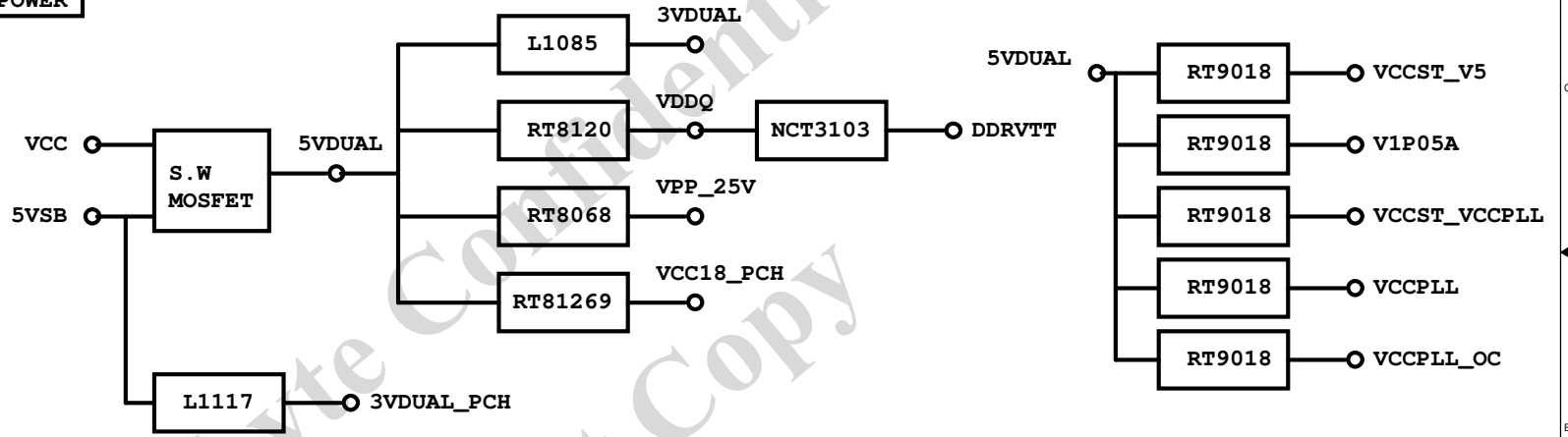
POWER BLOCK MAP



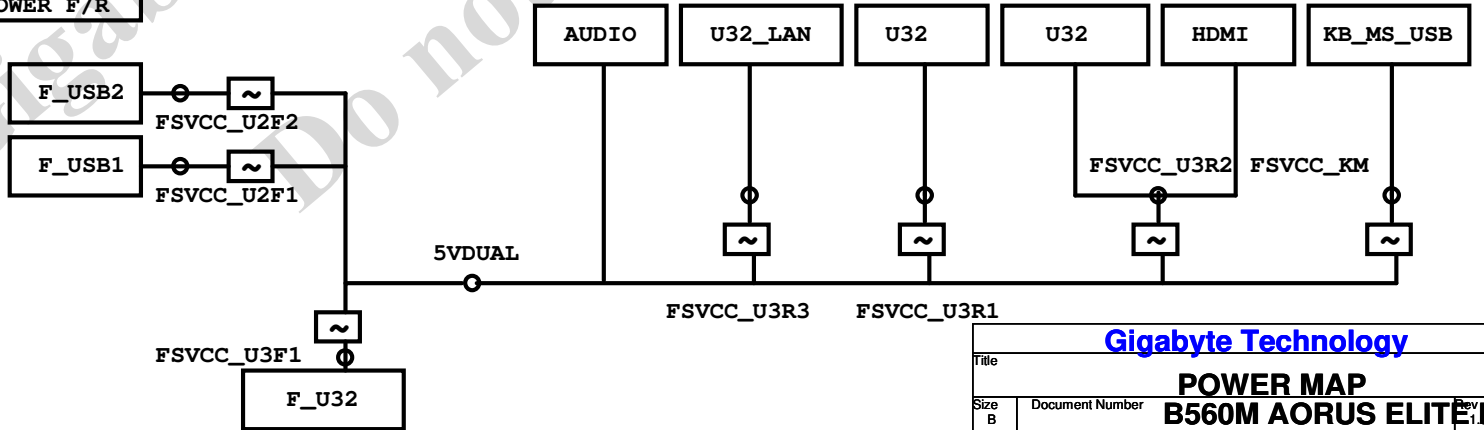
VCORE/VCCGT

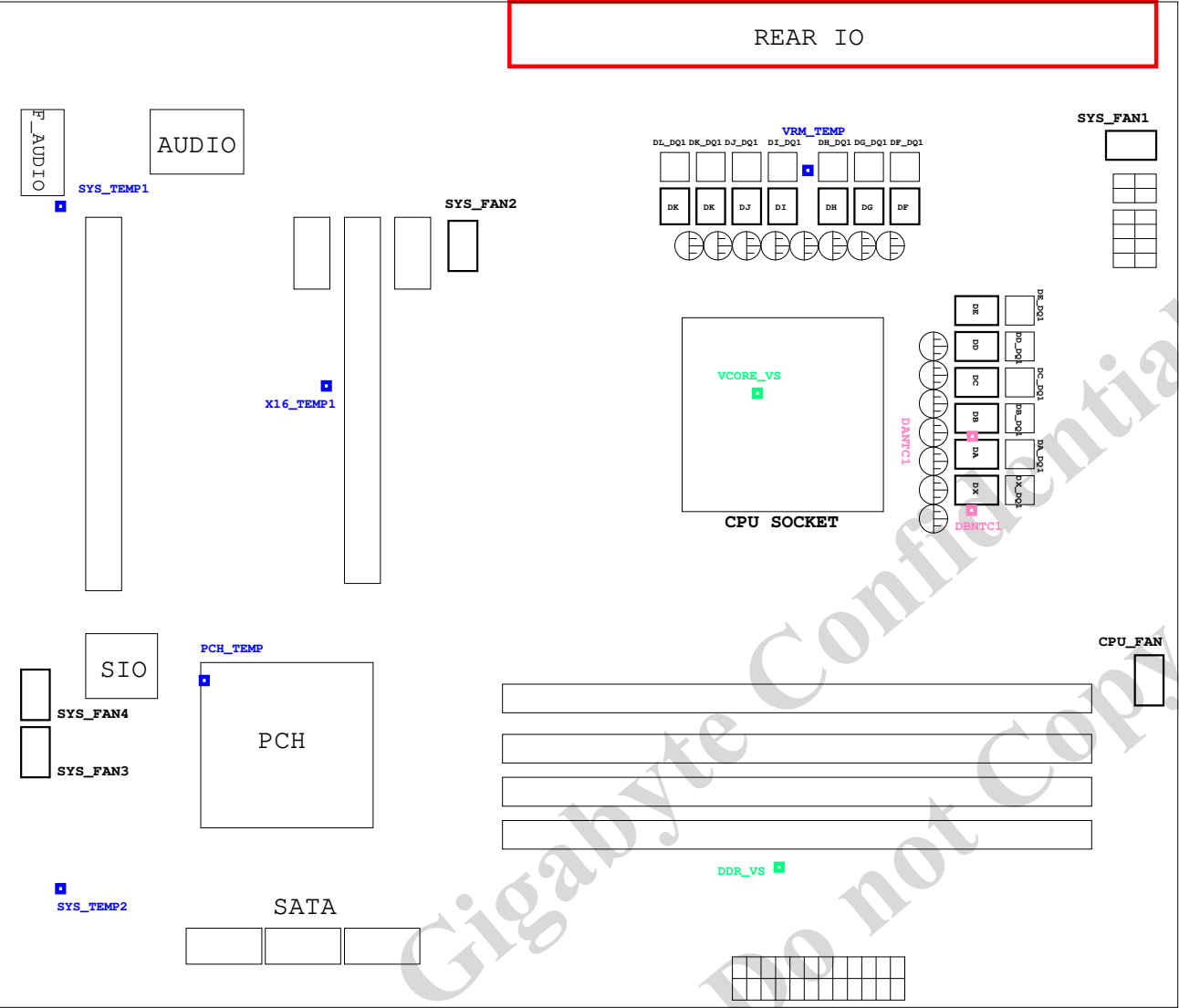


POWER



FUSE POWER F/R





熱敏電阻	擺放靠近位置	走線方式
DANTC1	DA_DL2	Differential
DANTC2	DA_DQ3	Differential
DANTC3	DM_DQ2	Differential
DANTC4	DM_DL1	Differential
VRM_TEMP	DC_DQ1	N/A
X16_TEMP1	PCIEX16	N/A
PCH_TEMP	PCH	N/A
SYS_TEMP1	F_AUDIO	N/A
SYS_TEMP2	F_PANEL	N/A

- SIO RS
- PWM RS
- SIO VIN



FAN